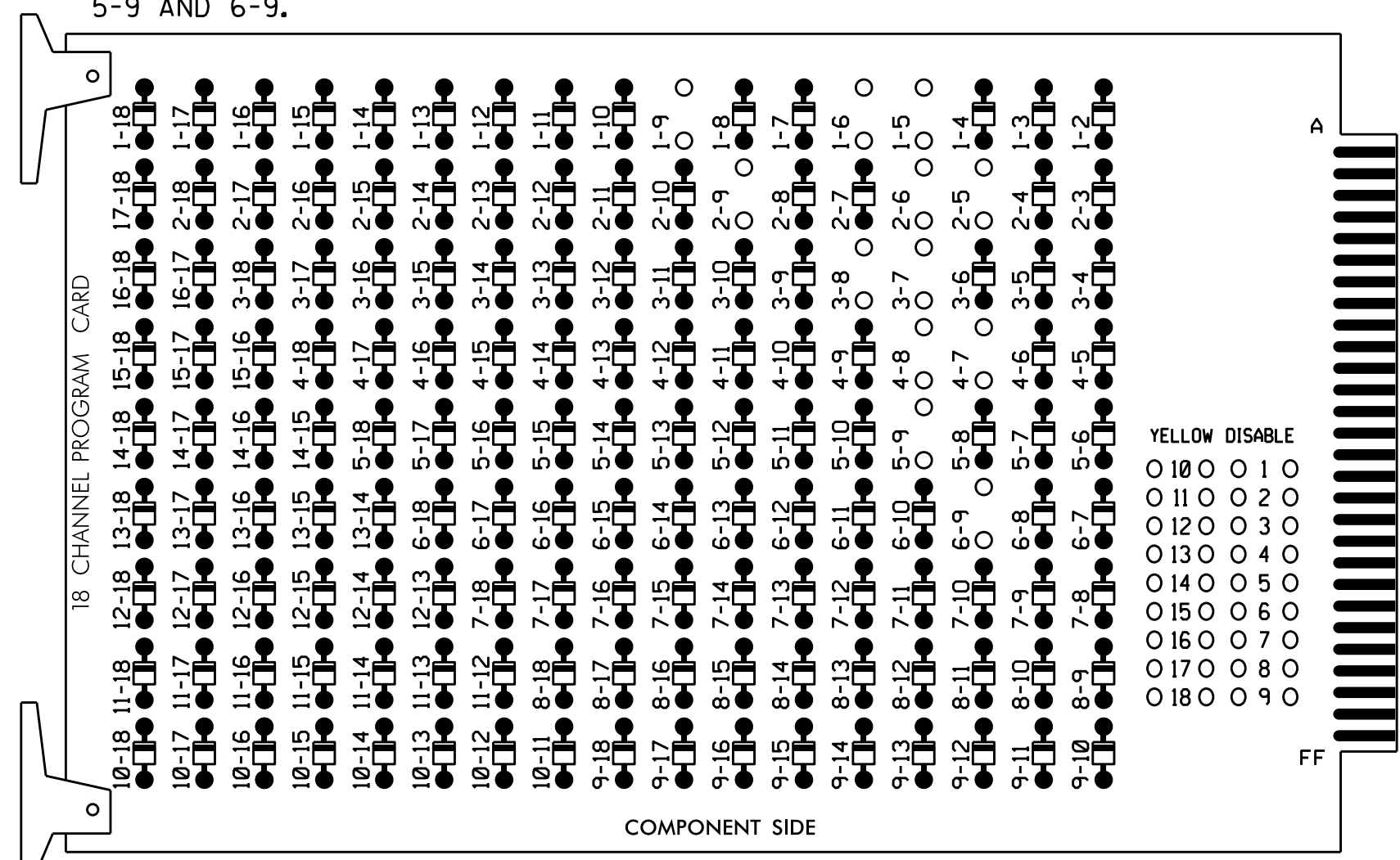


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

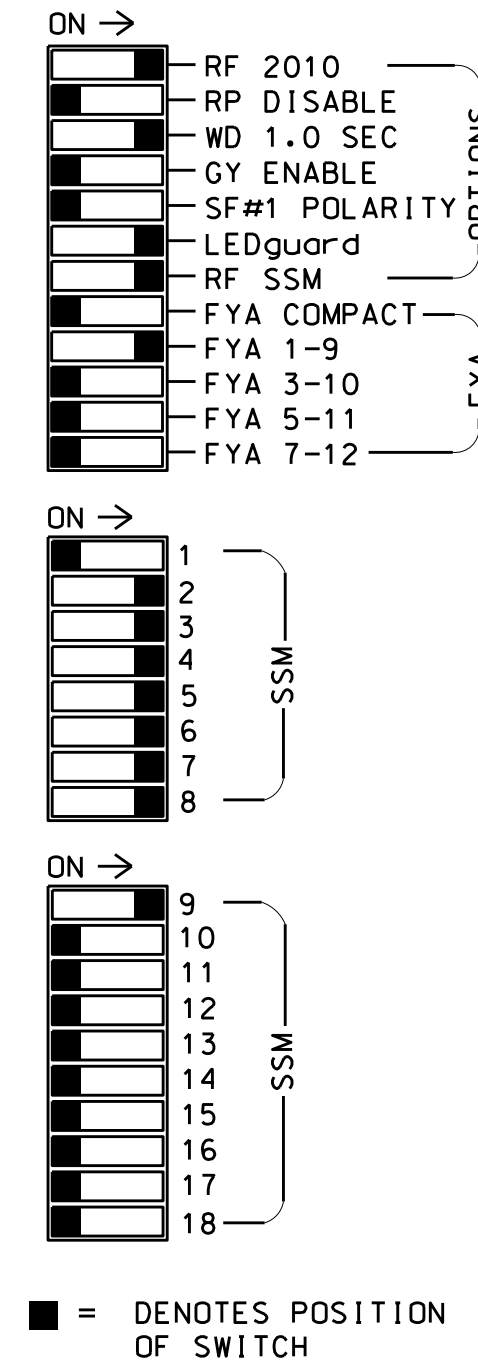
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 2-5, 2-6, 2-9, 3-7, 3-8, 4-7, 4-8, 5-9 AND 6-9.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all Phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Startup In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlap.
6. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
7. The cabinet and controller are part of the Salisbury Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,AUX S1
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	NU	22	31	41,42,43	NU	42	51	61,62	NU	62	71,72	81,82	NU	11	NU	NU	NU
RED		128			101			134			107								
YELLOW	*	129			102			135			108								
GREEN		130			103			136			109								
RED ARROW					116			131			122								A121
YELLOW ARROW					117	117		132	132		123	123							A122
FLASHING YELLOW ARROW																			A123
GREEN ARROW	127				118	118		133	133		124	124							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅2/SYS	-	-	∅ 3	-	-	-	-	-	-	-	-	FS
L	1A	2A/S16	-	-	3A	-	-	-	-	-	-	-	-	DC ISOLATOR
U	NOT USED	∅2/SYS	-	-	NOT USED	-	-	-	-	-	-	-	-	ST
L	2A/S16	2B/S17	-	-	-	-	-	-	-	-	-	-	-	DC ISOLATOR
U	∅ 5	-	-	-	-	NOT USED	-	-	-	-	-	-	-	* GPS EVVP
L	5A	-	-	-	-	∅ 8	-	-	-	-	-	-	-	8A
U	NOT USED	-	-	-	-	-	-	-	-	-	-	-	-	-
L	-	-	-	-	-	-	-	-	-	-	-	-	-	-

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 *See GPS Preemption Installation Note Below
 ⊗ Wired Input - Do not populate slot with detector card

GPS PREEMPTION INSTALLATION NOTE

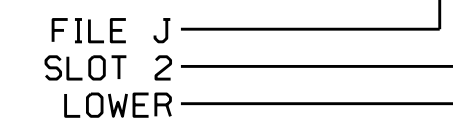
Install a GPS preemption system. Perform installation according to manufacturer's directions and NCDOT engineer approved mounting location to accomplish the preemption schemes shown on the Signal Design Plans.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
	-	I1U	56	18	51	1	Y	Y			3
2A/S16	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S17	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			3
8A	TB5-11,12	J6L	46	8	18	8	Y	Y			10

*Add jumper from I1-W to J4-W, on rear of input file.
 *See Input Page Assignment programming details on sheet 3.

INPUT FILE POSITION LEGEND: J2L

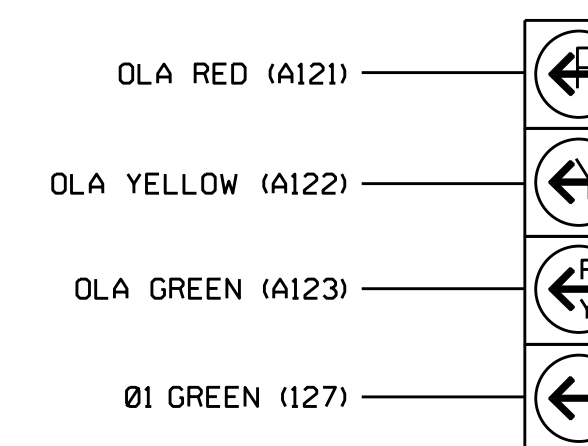


DETECTOR NOTES

- 1) Install a video detection system for detection zones 1A, 4A, 5B, 6A/S18, 6B/S19, 7A and 7B. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- 2) For detection area 1A detector card placement and slots reserved for wired inputs are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheet 3 of this electrical detail.

FYA SIGNAL WIRING DETAIL

(wire signal head as shown)



11

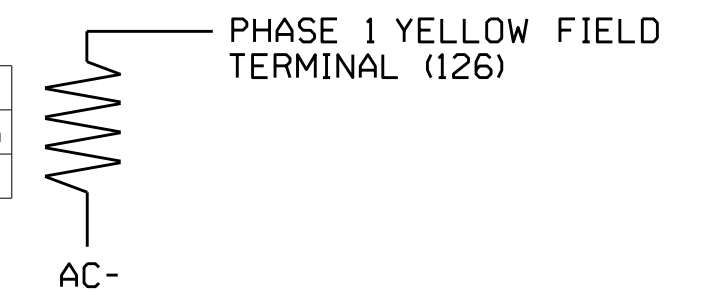
NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0640T1
 DESIGNED: January 2022
 SEALED: 1/27/2022
 REVISED:

Electrical Detail - Temp 1 - Sheet 1 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: US 601 (Jake Alexander Blvd S) at SR 2528 (Julian Rd) and Martin Luther King Jr Ave

Division 9 Rowan County Salisbury

PLAN DATE: January 2022 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SEAL 031001

ENGINEER T. JOYCE

Designed by: T. Joyce 01/28/2022

SIG. INVENTORY NO. 09-0640T1

2/1/2022 1:42 PM C:\Users\strickland\Documents\Projects\Signal Design\Temp 1 - Sheet 1 of 5.dgn