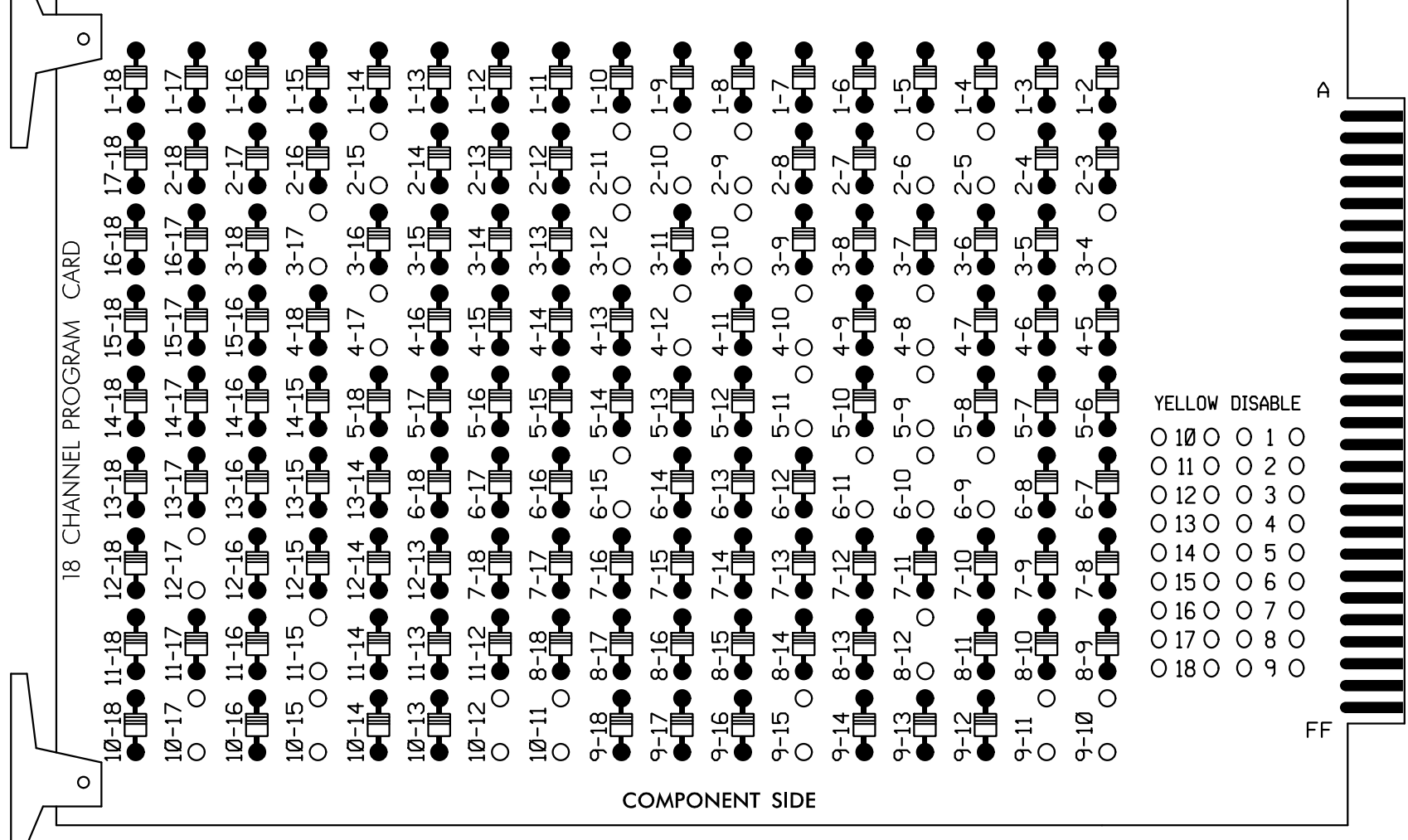


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

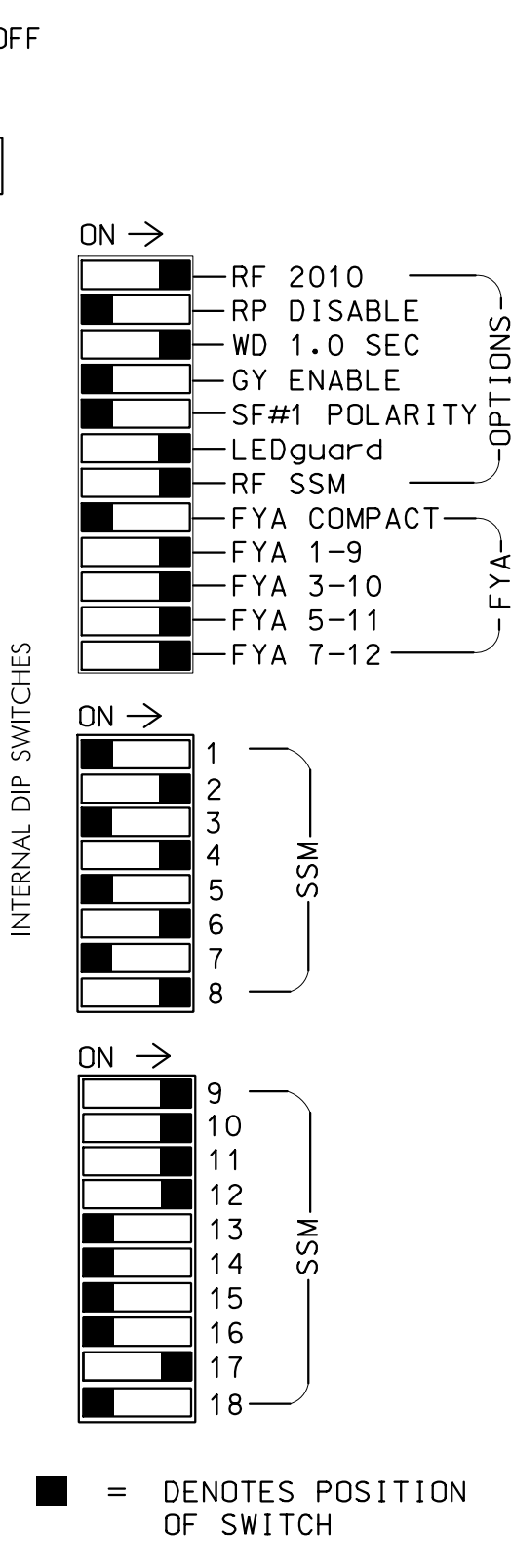
REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-10, 2-II, 2-15, 3-4, 3-10, 3-12, 3-17, 4-8, 4-10, 4-12 4-17, 5-9, 5-II, 6-9, 6-10, 6-II, 6-15, 8-12, 9-10, 9-II, 9-15, 10-II, 10-12, 10-15, 10-17, 11-15 AND 12-17.



REMOVE JUMPERS AS SHOWN

NOTES:

- 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Startup In Green and Yellow Flash.
6. Program phase 6 for Startup Ped Call.
7. Program overlaps 1, 2 and 4 as Wag Overlaps.
8. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
9. The cabinet and controller are part of the Salisbury Signal System.

EQUIPMENT INFORMATION

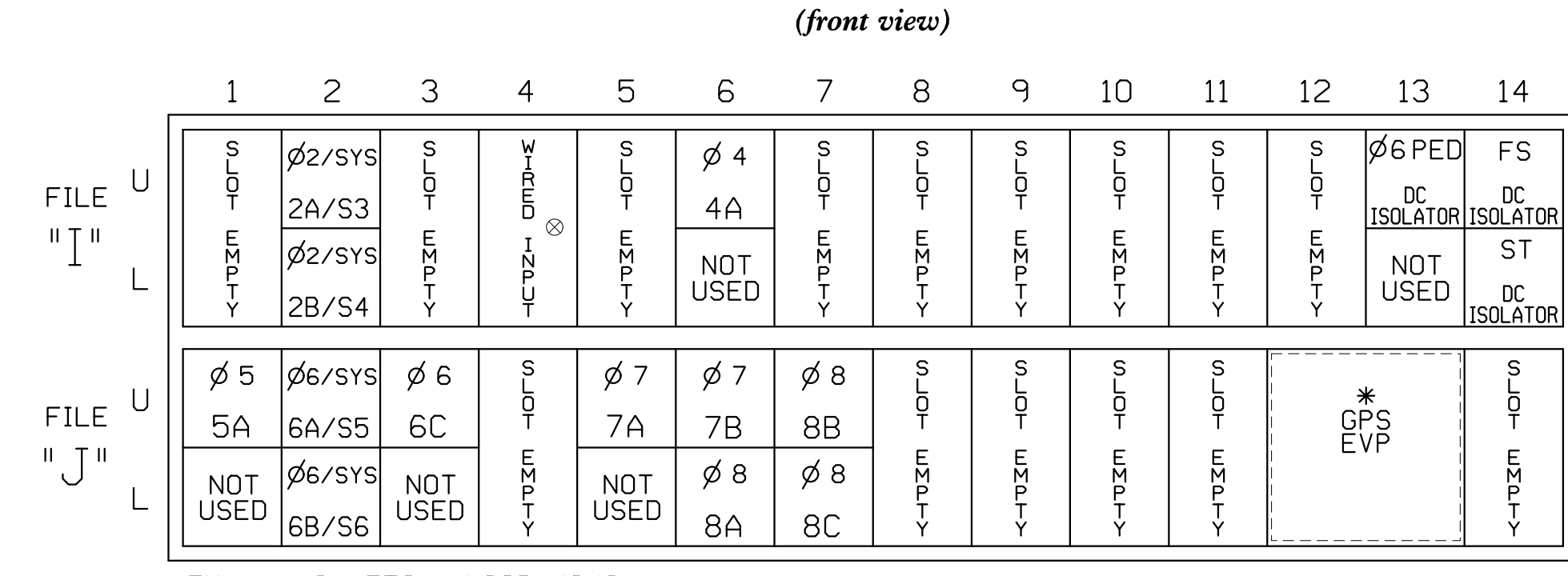
CONTROLLER.....2070
CABINET.....332 w/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S4,S5,S7,S8,S9,S11
AUX S1,AUX S2,AUX S3,AUX S4,AUX S5
PHASES USED.....2,4,5,6,6PED,7,8
OVERLAP "A".....2
OVERLAP "B".....6+7
OVERLAP "C".....5+6
OVERLAP "D".....4
OVERLAP "E".....7
OVERLAP "G".....7

SIGNAL HEAD HOOK-UP CHART

Table with columns for Load Switch No., S1-S12, AUX S1-S6, and Signal Head No. (RED, YELLOW, GREEN, RED ARROW, YELLOW ARROW, FLASHING YELLOW ARROW, GREEN ARROW).

NU = Not Used NC = Not Connected
\* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

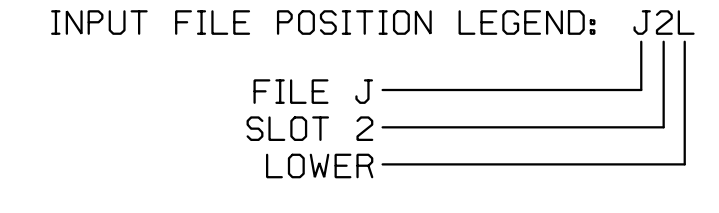


EX.: 1A, 2A, ETC. = LOOP NO.'S
See GPS Preemption Installation Note Below
\* Wired Input - Do not populate slot with detector card
FS = FLASH SENSE
ST = STOP TIME

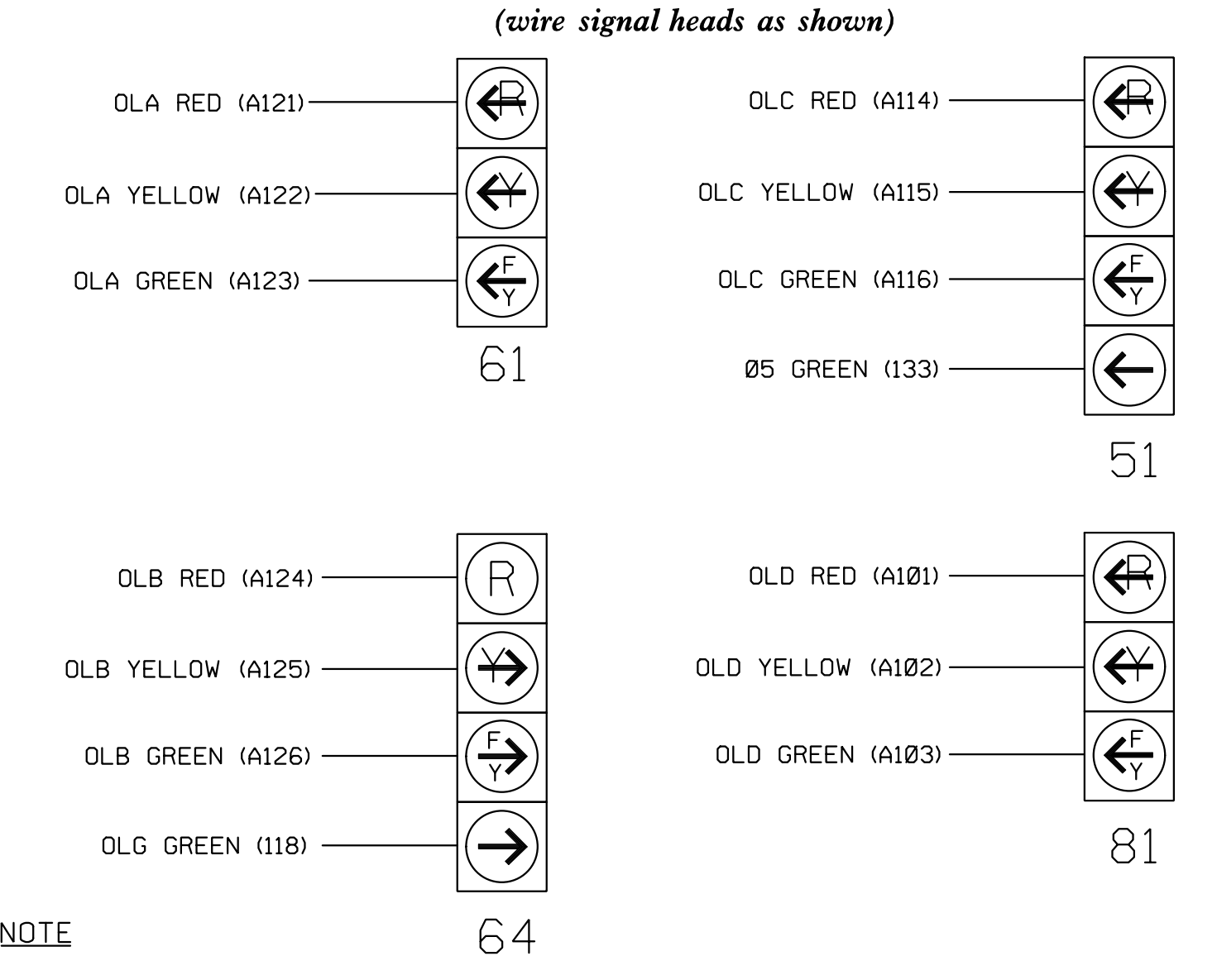
INPUT FILE CONNECTION & PROGRAMMING CHART

Table with columns: LOOP NO., LOOP TERMINAL, INPUT FILE POS., PIN NO., INPUT ASSIGNMENT NO., DETECTOR NO., NEMA PHASE, CALL, EXTEND, FULL TIME DELAY, STRETCH TIME, DELAY TIME.

1 Add jumper from J1-W to I4-W, on rear of input file.
★ See Input Page Assignment programming details on sheet 5.



FYA SIGNAL WIRING DETAIL

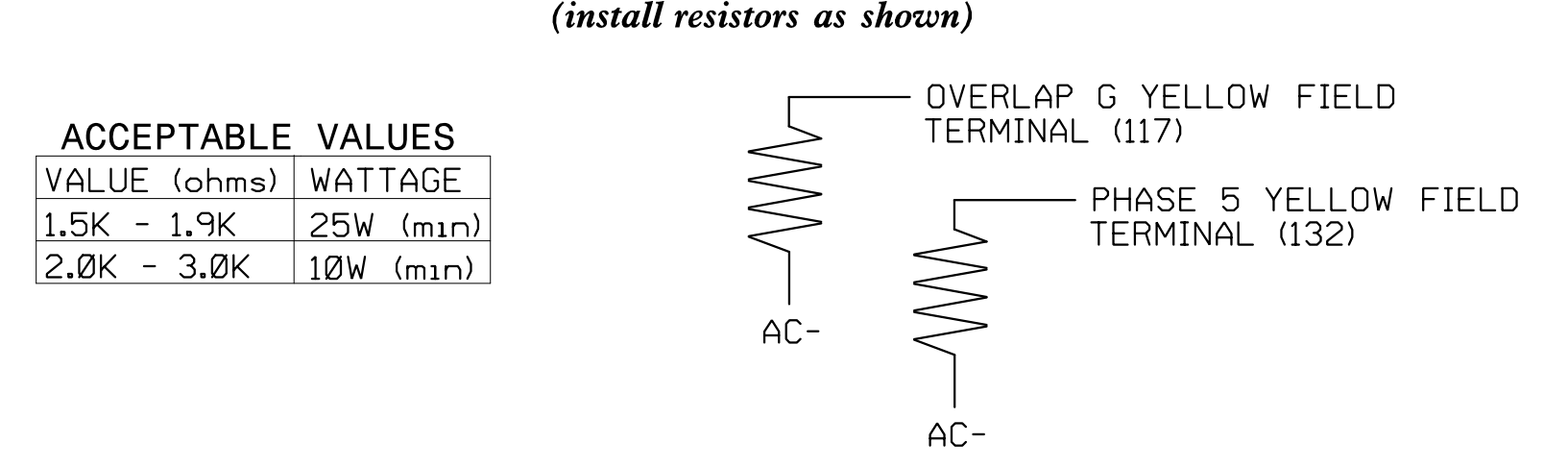


NOTE: The sequence display for signal heads 51 and 64 requires special logic programming. See sheet 4 for programming instructions.

SPECIAL DETECTOR NOTE

Install a GPS preemption system. Perform installation according to manufacturer's directions and NCDOT engineer approved mounting location to accomplish the preemption schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL



ACCEPTABLE VALUES table with columns VALUE (ohms) and WATTAGE. Values: 1.5K - 1.9K, 25W (min); 2.0K - 3.0K, 10W (min).

Final Design Electrical Detail - Sheet 1 of 6. Includes project info (SR 2528), design date (November 2021), and a professional engineer seal for Zhaolong Teng.

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