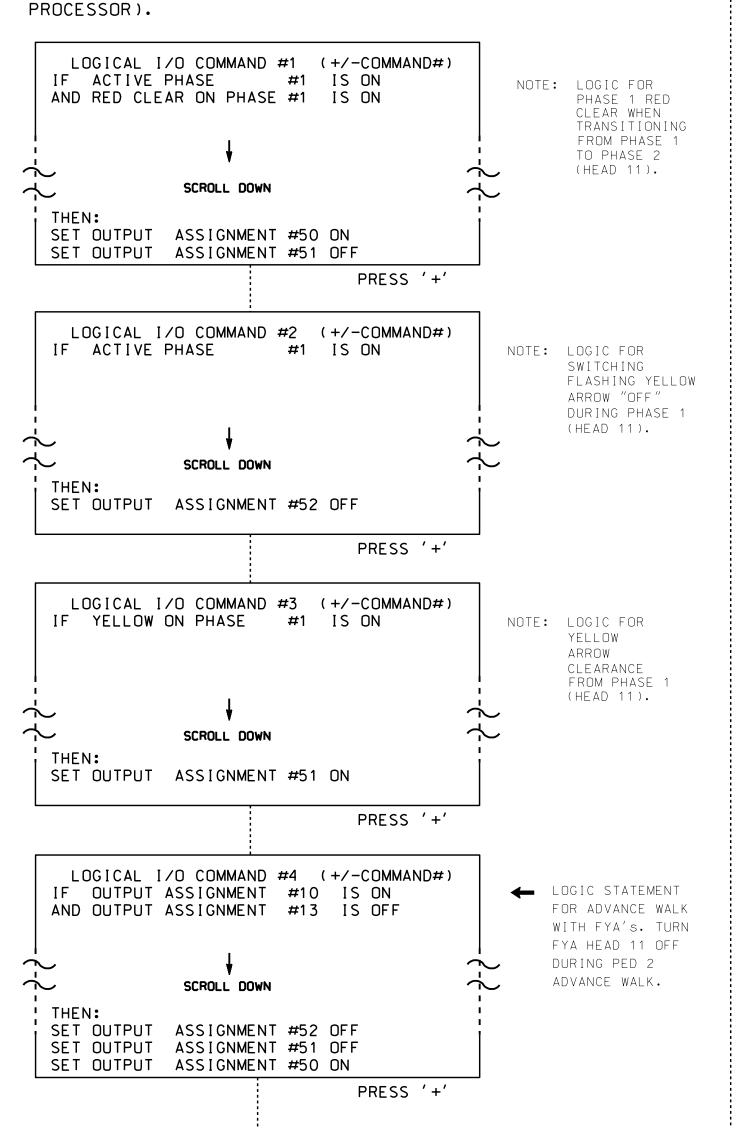
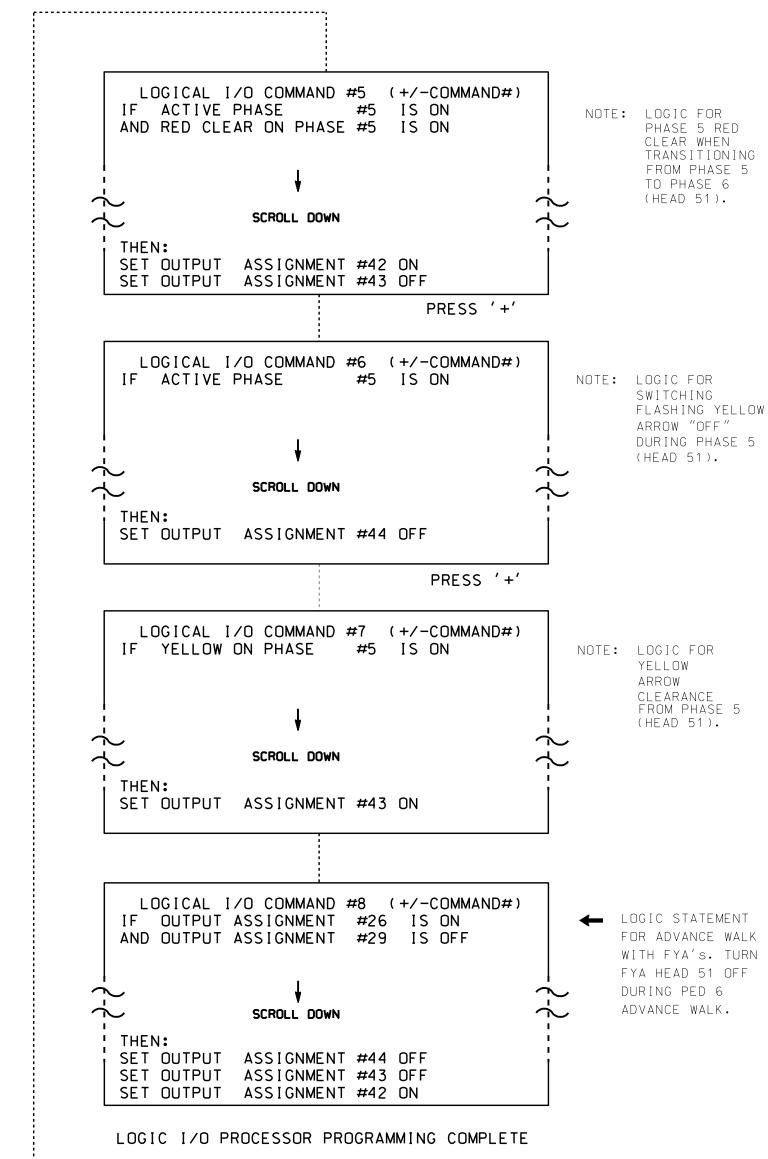
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, AND 8.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O





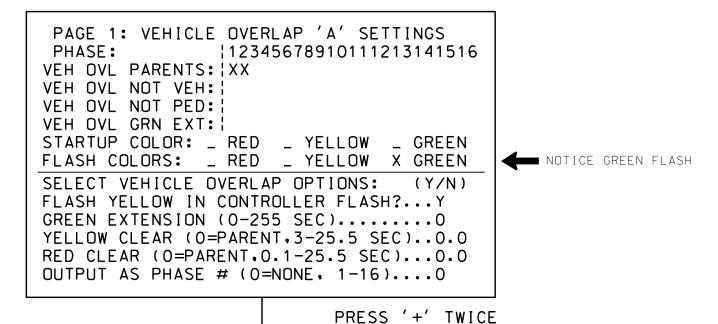
## **OUTPUT REFERENCE SCHEDULE**

OUTPUT 10 = 2 PED Walk OUTPUT 13 = Vehicle 2 Green OUTPUT 26 = 6 PED Walk OUTPUT 29 = Vehicle 6 Green OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



PAGE 1: VEHICLE OVERLAP 'C' SETTINGS ¦12345678910111213141516 PHASE: VEH OVL PARENTS: | XX VEH OVL NOT VEH: VEH OVL NOT PED: : VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN NOTICE GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

## OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

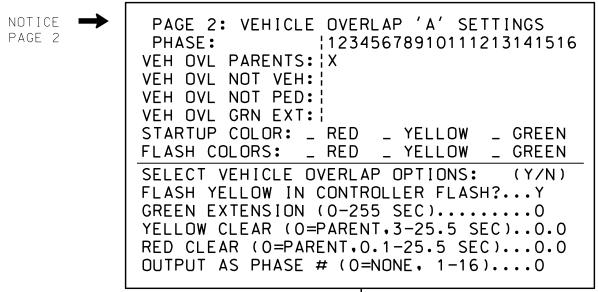
PROJECT REFERENCE NO.

R-5014

Sig. 3.2

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



PRESS '+' TWICE

NOTICE -PAGE 2: VEHICLE OVERLAP 'C' SETTINGS \\ 12345678910111213141516 PHASE: VEH OVL PARENTS: X VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW \_ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0

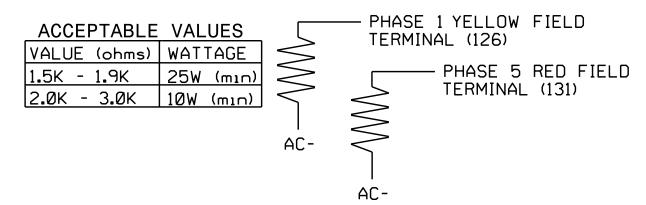
OVERLAP PROGRAMMING COMPLETE

OUTPUT AS PHASE # (O=NONE, 1-16)....0

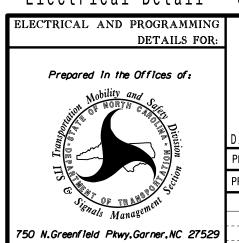
## LOAD RESISTOR INSTALLATION DETAIL

PAGE 2

(install resistors as shown below)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0030 DESIGNED: January 2020 SEALED: 2/12/2020 REVISED: N/A



Electrical Detail - Sheet 2 of 5 US 158 (Croatan Highway)

> SR 1217 (Collington Road/ Ocean Bay Boulevard)

Dare County Kill Devil Hills ivision 1 PLAN DATE: January 2020 REVIEWED BY: PREPARED BY: S. Armstrong | REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 01-0030

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

SIGNATURES COMPLETED

036833