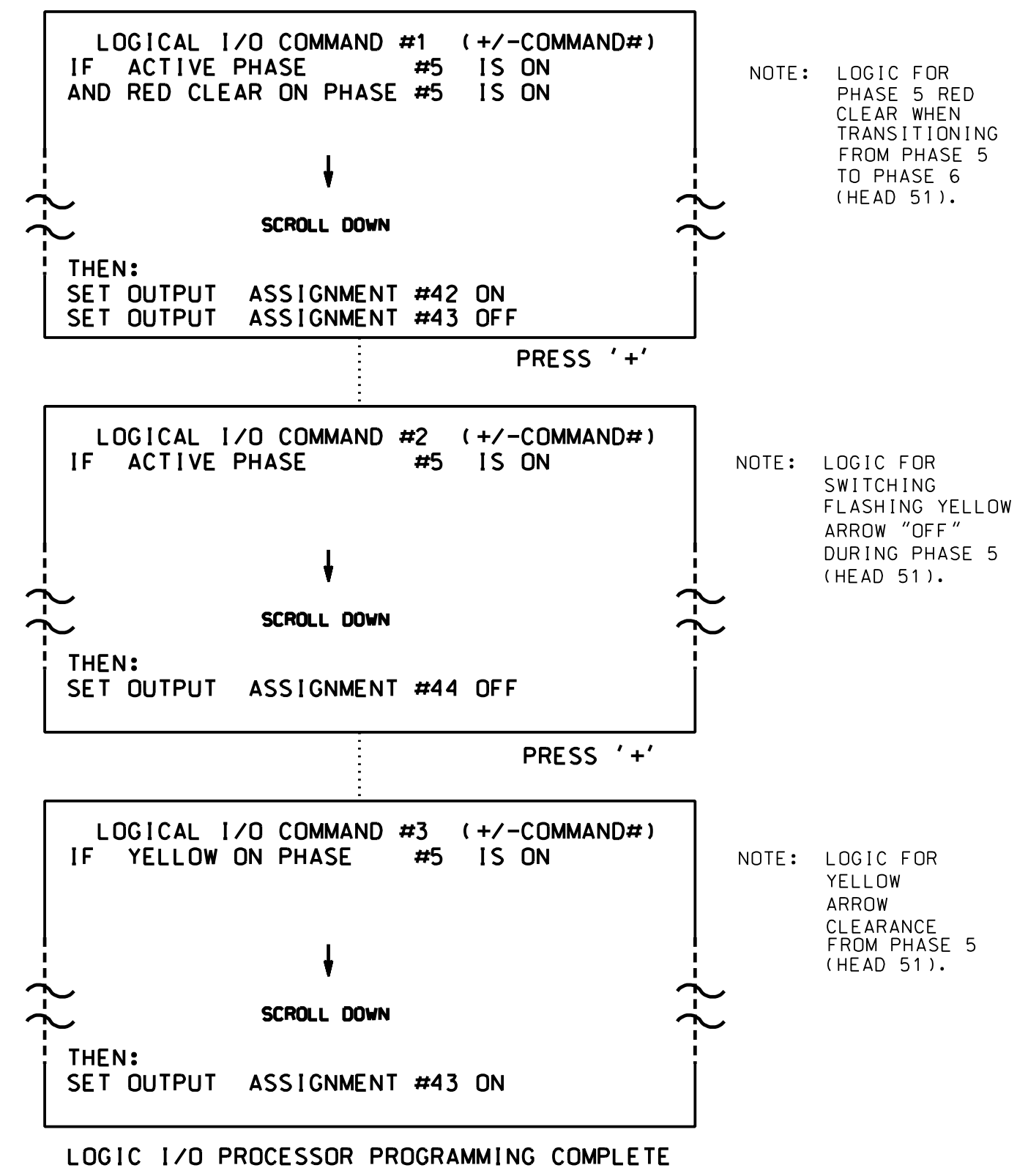


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

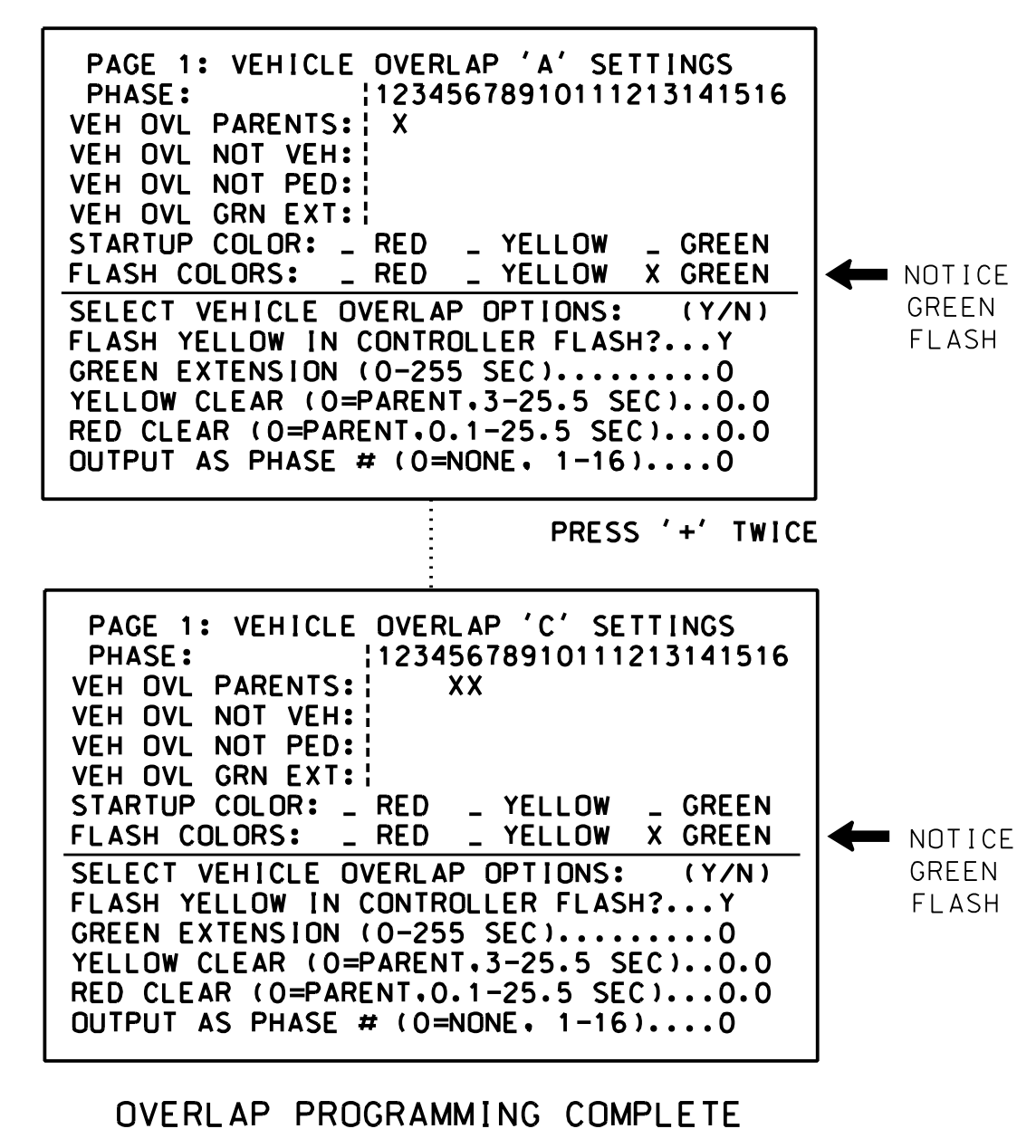


OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

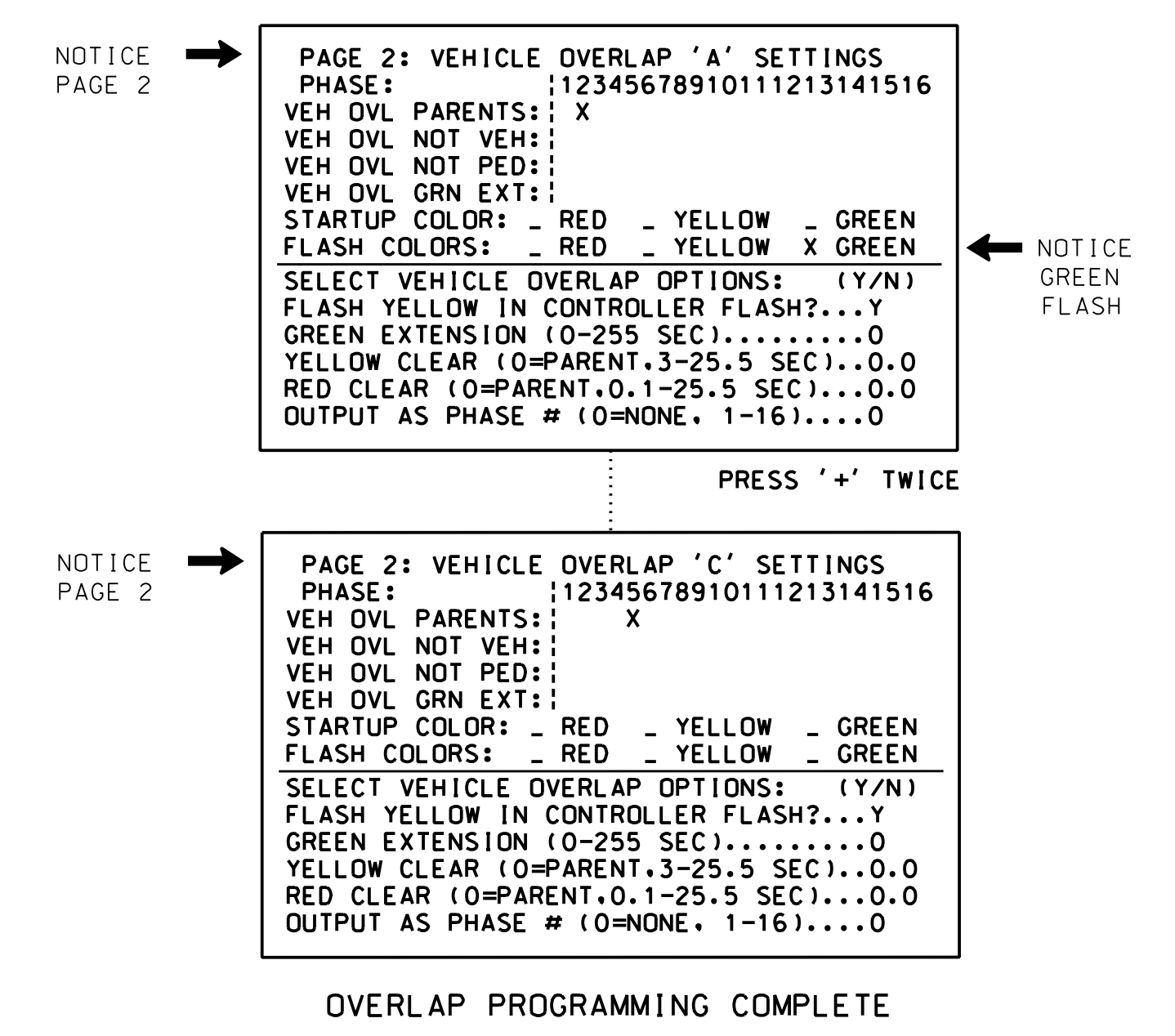
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

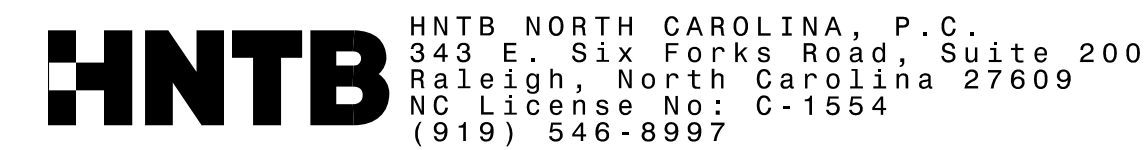
(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



Electrical Detail - Sheet 2 of 4
Signal Upgrade
Temporary Design 1

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**



	Prepared for: NC 211 (Southport-Supply Road) at J. Swain Boulevard Division 03 Brunswick Co. Southport	
	PLAN DATE: June 2017 REVIEWED BY: A.D. Klinksiek PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons	
REVISIONS: _____ INIT. DATE _____		Signature: <i>Natasha Simmons</i> 9/10/2021 DATE: _____ SIG. INVENTORY NO. 03-1043T1