

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT.....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
        
```

Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'OTHER/ECONOLITE'

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TMG VEH OVLP...[B] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . X . . . . .
PROTECT . . . . .
PED PRTC . . . . .
NOT OVLP . . . . .
FLSH GRN . . . 1 . . . . .
LAG X PH . . . . .
LAG 2 PH . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
        
```

Toggle Once

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

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TMG VEH OVLP...[C] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT.....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
        
```

Toggle Once

OVERLAP D

Select TMG VEH OVLP [D] and 'OTHER/ECONOLITE'

```

TMG VEH OVLP...[D] TYPE: OTHER/ECONOLITE
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . 8 . . . . .
PROTECT . . . . .
PED PRTC . . . . .
NOT OVLP . . . . .
FLSH GRN . . . . . 1 . . . . .
LAG X PH . . . . .
LAG 2 PH . . . . .

LAG GRN 0.0 YEL 0.0 RED 0.0 ADV GRN 0.0
        
```

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

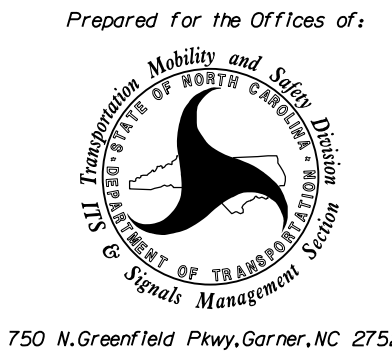


IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 06-1283
 DESIGNED: November 2019
 SEALED: 05/15/2020
 REVISED: N/A

Final Design
 Electrical Detail - Sheet 2 of 2

	<p>US 701 Bypass (N. J.K. Powell Blvd) at SR 1552 (Smyrna Road)- McDonald's D/W</p> <p>Division 06 Columbus County Whiteville</p> <p>PLAN DATE: November 2019 REVIEWED BY:</p> <p>PREPARED BY: M Copple REVIEWED BY: GG Murr Jr</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>	REVISIONS	INIT.	DATE				<p>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p> <div style="border: 1px solid black; padding: 5px; text-align: center;"> <p>SEAL</p>  <p>Matthew B. Copple</p> </div>
REVISIONS	INIT.	DATE						
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