

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR LEADING PED INTERVAL (DELAYED GREEN) & PREEMPT ONLY PHASE OMIT

(program controller as shown)

The following logic processor configuration holds the FYA's on signal heads 31 and 71 red for the duration of the delayed green time (leading ped interval) when serving a ped call on the opposing through phase.

1. From Main Menu select 1. CONFIGURATION
2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	1	COPY FROM:	1	ACTIVE:	M	(T/F)
IF	PED ON PH WALK		4	IS	ON	
AND	VEH GREEN ON PH		4	IS	OFF	
THEN	SIG SET OLP RED		2		ON	
	SIG SET OLP YELLOW		2		OFF	
	SIG SET OVLP GREEN		2		OFF	
ELSE						

HOLD SIGNAL HEAD 31 FYA RED DURING THE PHASE 4 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	2	COPY FROM:	2	ACTIVE:	M	(T/F)
IF	PED ON PH WALK		8	IS	ON	
AND	VEH GREEN ON PH		8	IS	OFF	
THEN	SIG SET OLP RED		4		ON	
	SIG SET OLP YELLOW		4		OFF	
	SIG SET OVLP GREEN		4		OFF	
ELSE						

HOLD SIGNAL HEAD 71 FYA RED DURING THE PHASE 8 DELAYED GREEN TIME (LEADING PED INTERVAL)

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	3	COPY FROM:	3	ACTIVE:	M	(T/F)
IF	PMT PREEMPT ACTIVE		5	IS	OFF	
THEN	CTR OMIT PHASE		3		ON	
ELSE						

LOGIC FOR OMITTING PHASE 3 AT STARTUP AND/OR WHEN NOT IN PREEMPT

4. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-6 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM .

LOGIC STATEMENT CONTROL												
	1	2	3	4	5	6	7	8	9	0	1	2
LP 1-15	E	E	E
LP 16-30
LP 31-45
LP 46-60
LP 61-75
LP 76-90

END PROGRAMMING

ECONOLITE ASC/3-2070 PREEMPT FILTERING PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 4. PREEMPTOR/TSP
2. From PREEMPT/TSP/SCP Submenu select 2. ENABLE PREEMPT FILTERING & TSP/SCP

ENABLE PREEMPT FILTERING & TSP/SCP		
FILTERED	SOLID	PULSING
INPUT	1 ...BYPASSED..	...BYPASSED..
	2 ...BYPASSED..	...BYPASSED..
	3 ..PREEMPT	3. ...BYPASSED..
	4 ..PREEMPT	4. ...BYPASSED..
	5 ..PREEMPT	5. ...BYPASSED..
	6 ..PREEMPT	6. ...BYPASSED..
	7 ...BYPASSED..	...BYPASSED..
	8 ...BYPASSED..	...BYPASSED..
	9 ...BYPASSED..	...BYPASSED..
	10 ...BYPASSED..	...BYPASSED..

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0004
DESIGNED: MARCH 2018
SEALED: 09/20/2018
REVISED: N/A

Electrical Detail - Sheet 3 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

<p style="font-size: x-small;">ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="font-size: x-small;">Prepared for the Offices of:</p> <p style="font-size: x-small;">DRMP, Inc. 8000 Regency Parkway, Suite 175 Cary, NC 27519 NC License No. C-2215 (019) 650-1038</p>	<p style="font-size: large; font-weight: bold;">US 158 (Elizabeth Street) at US 17 Bus. (N. Road Street)</p> <p style="font-size: small;">Divison 1 Pasquotank County Elizabeth City</p> <p style="font-size: x-small;">PLAN DATE: March 2018 REVIEWED BY: AJ Davis</p> <p style="font-size: x-small;">PREPARED BY: DJ White REVIEWED BY: LM Moon</p> <table border="1" style="width: 100%; border-collapse: collapse; font-size: x-small;"> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </table>	REVISIONS	INIT.	DATE				<p style="font-size: x-small;">SEAL</p> <p style="font-size: x-small;">DocuSigned by: <i>Lisa M. Moon</i> 9/20/2018 SIG. INVENTORY NO. 01-0004</p>
REVISIONS	INIT.	DATE						