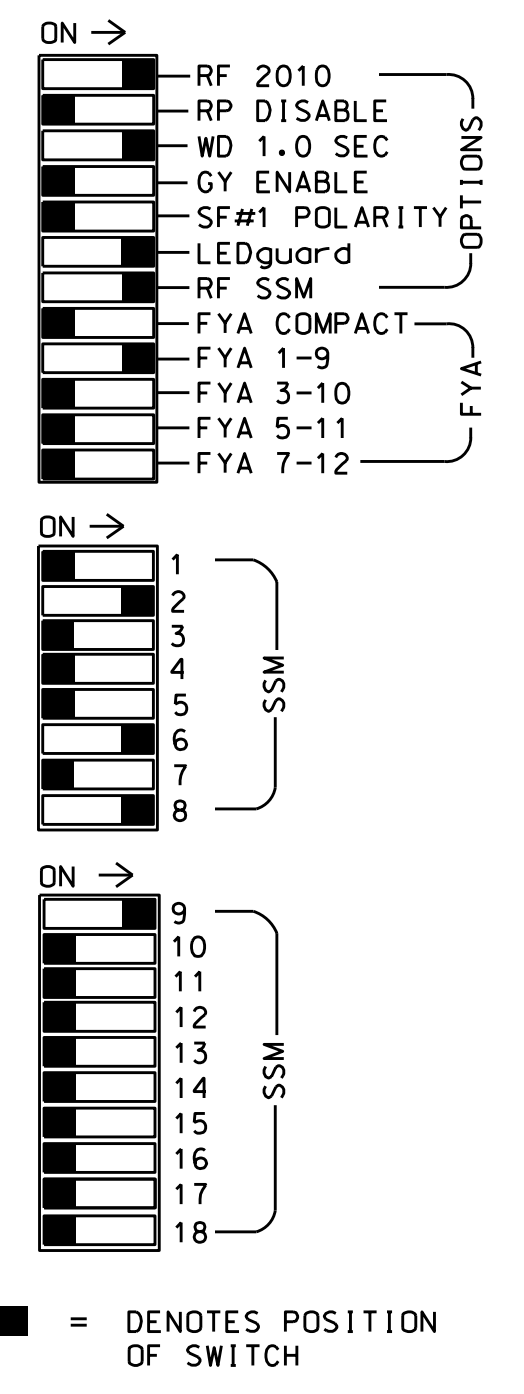
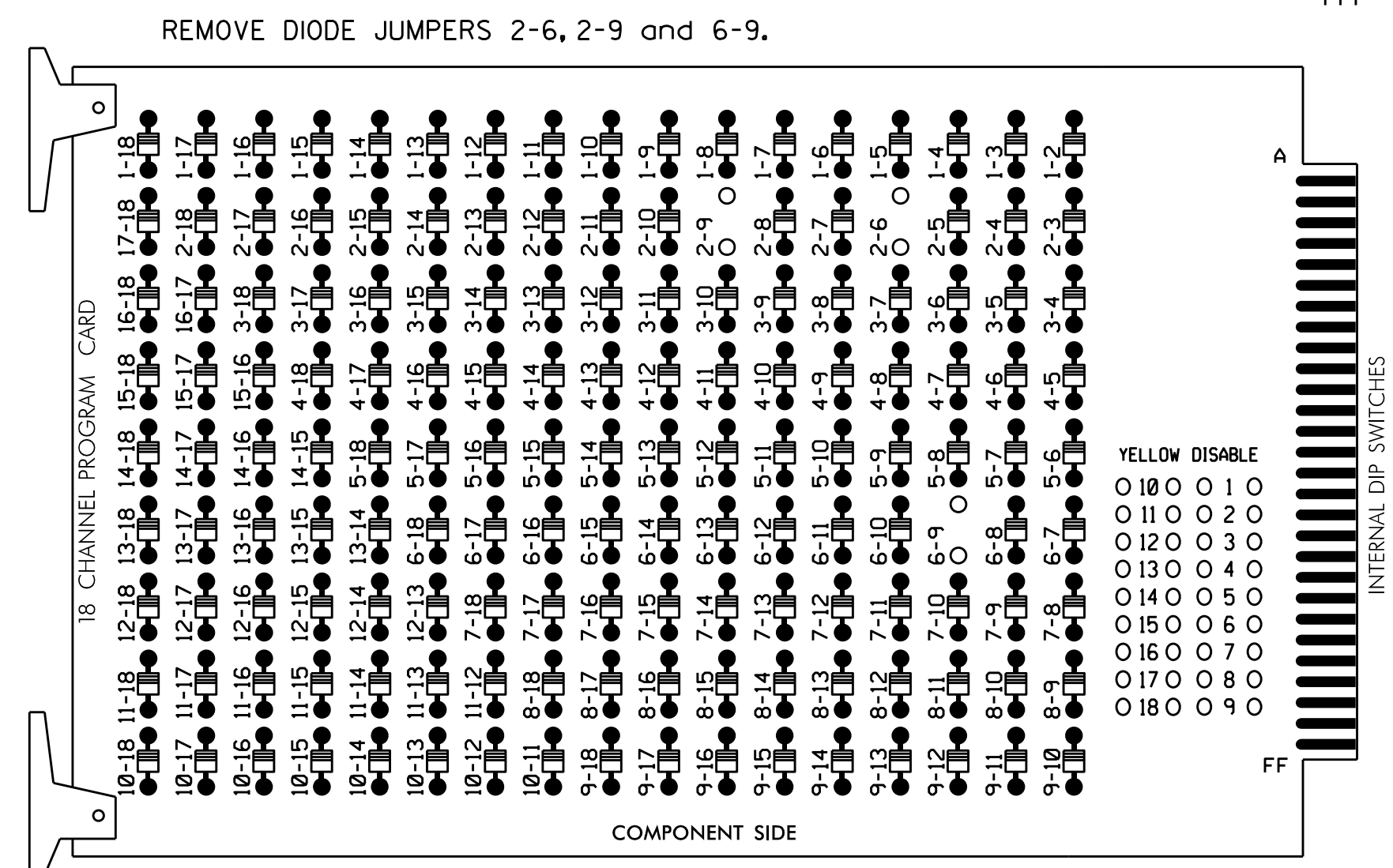


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the SR 2526 (Summit Avenue) CLS.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S8,S11,AUX S1
 PHASES USED.....2,6,8
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

PROJECT REFERENCE NO.	SHEET NO.
R-4707	Sig.3.1

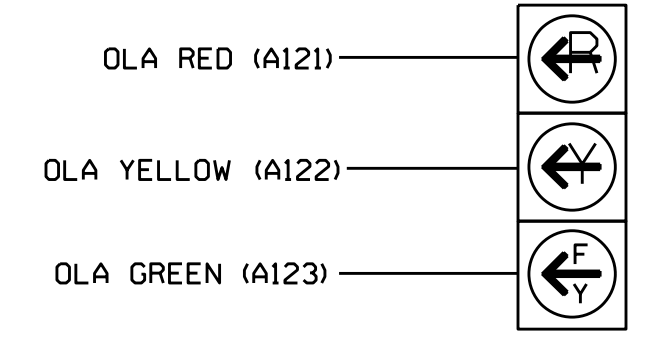
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	NU	NU	NU	62,63	NU	NU	81,82	NU	61	NU	NU	NU	NU	NU
RED		128							134									
YELLOW		129							135									
GREEN		130							136									
RED ARROW												107	A121					
YELLOW ARROW												108	A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW												109						

NU = Not Used
 * See pictorial of head wiring in detail below.

3 SECTION FYA PPLT SIGNAL WIRING DETAIL

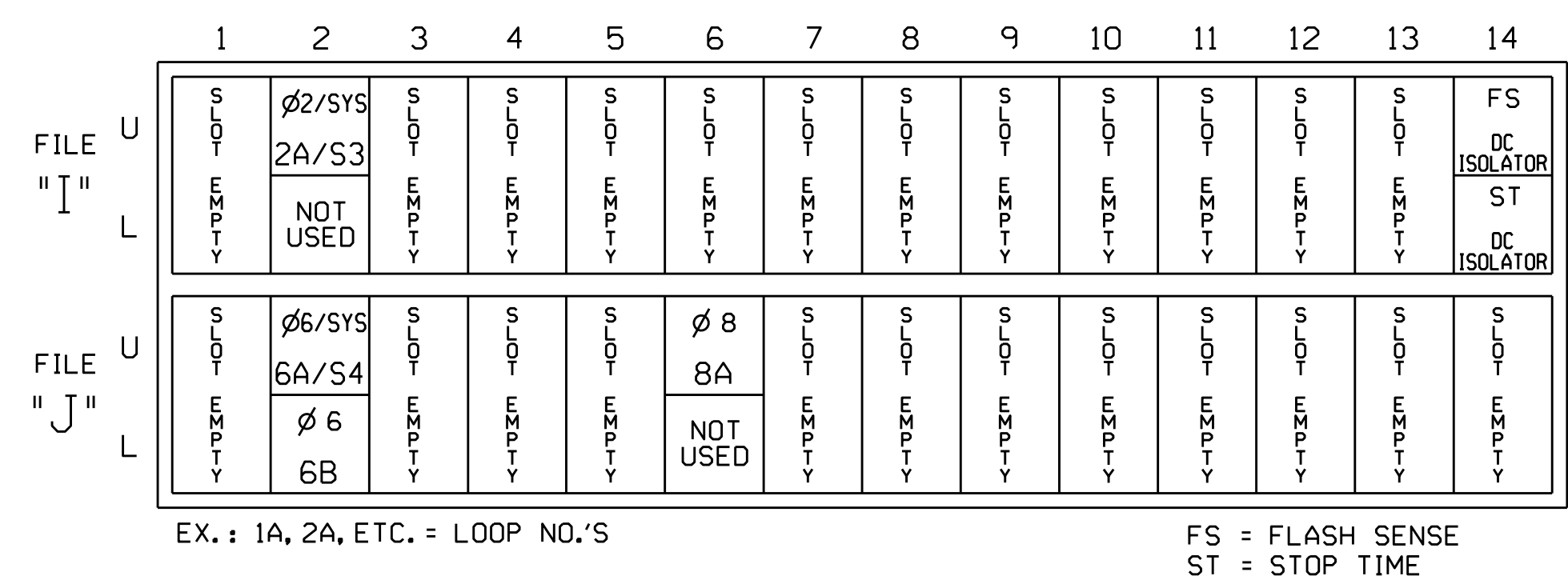
(wire signal heads as shown)



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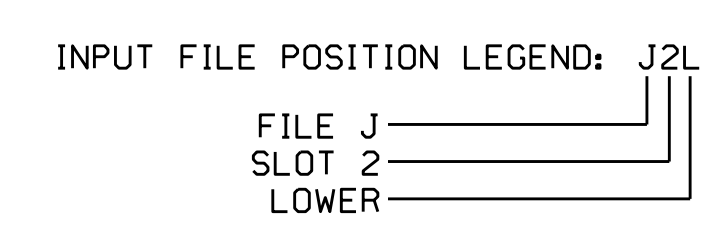
INPUT FILE POSITION LAYOUT

(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S3	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
6A/S4	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y	Y		3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			



OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:          12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  - RED - YELLOW - GREEN
FLASH COLORS:   - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1533T2
 DESIGNED: April 2020
 SEALED: April 20, 2020
 REVISED: N/A

Project #: 180914

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 NCBELS FIRM LICENSE NO. C-2522

Electrical Details - Temporary Design 2 (TMP Phase II - Step 8)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SR 2526 (Summit Avenue)
 at
 SR 4771 (Reedy Fork Parkway)

Division 7 Guilford County Greensboro

PLAN DATE: April 2020 REVIEWED BY: R. Hinshaw

PREPARED BY: A. Ayuninjam REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: 4/20/2020
 Signature: R. Hinshaw DATE: 4/20/2020
 SIG. INVENTORY NO. 07-1533T2