PROJECT REFERENCE NO.

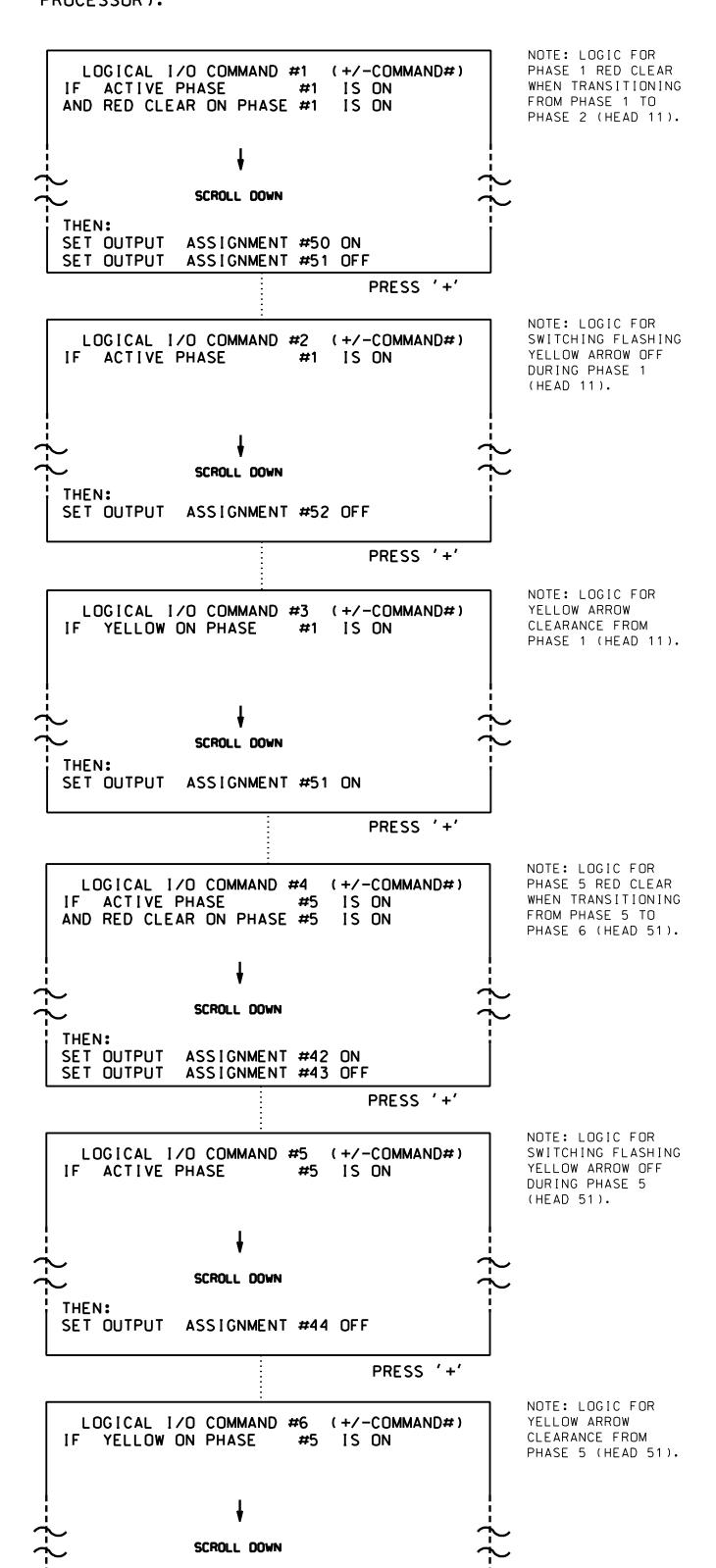
R-1015 Sig. 21

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, and 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



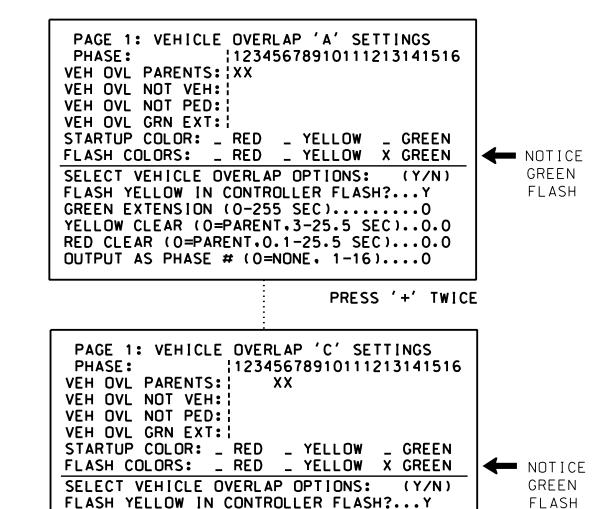
SET OUTPUT ASSIGNMENT #43 ON

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

GREEN EXTENSION (0-255 SEC).....

YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0

RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE. 1-16)....0

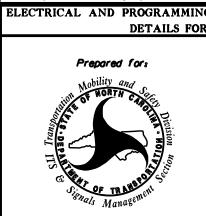
OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 02-0720 DESIGNED: March 2018 SEALED: 12-7-18 REVISED: N/A

Electrical Detail - Sheet 2 of 2 |Signal Upgrade

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



NC 101 (Fontana Boulevard) at SR 1824 (McCotter Boulevard)/ Fike Street

Division 02 Craven Co. Havelock March 2018 REVIEWED BY: A.D. Klinksiek PLAN DATE: PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons INIT. DATE

TH CAROL OR WESSION 031464 Natasha R. Simmons 12/7/2018

SIG. INVENTORY NO. 02-0720

ERDAR SHENATURE

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