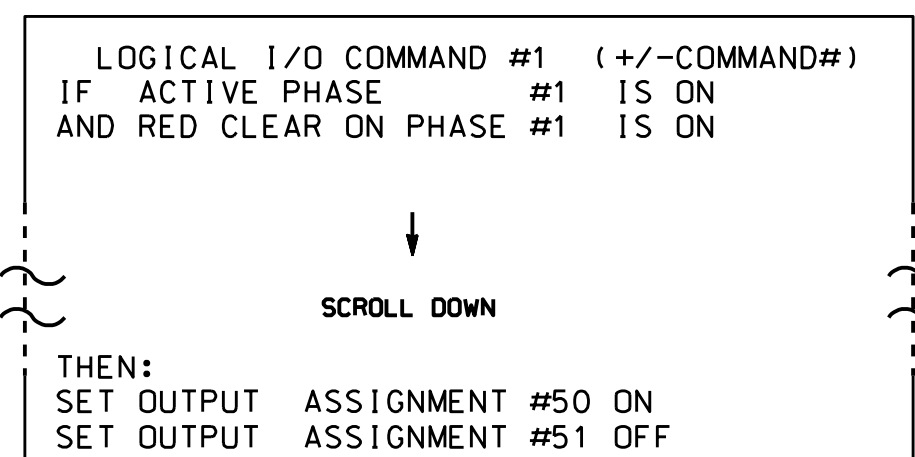


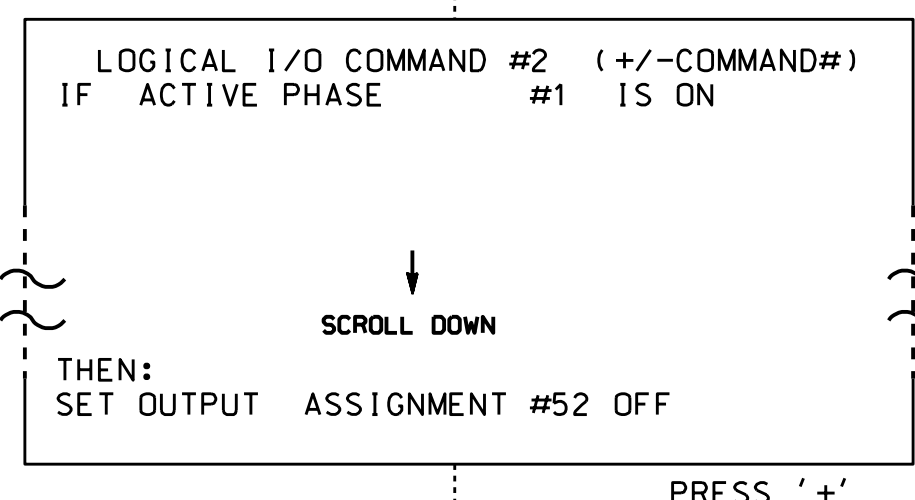
**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL**  
**TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

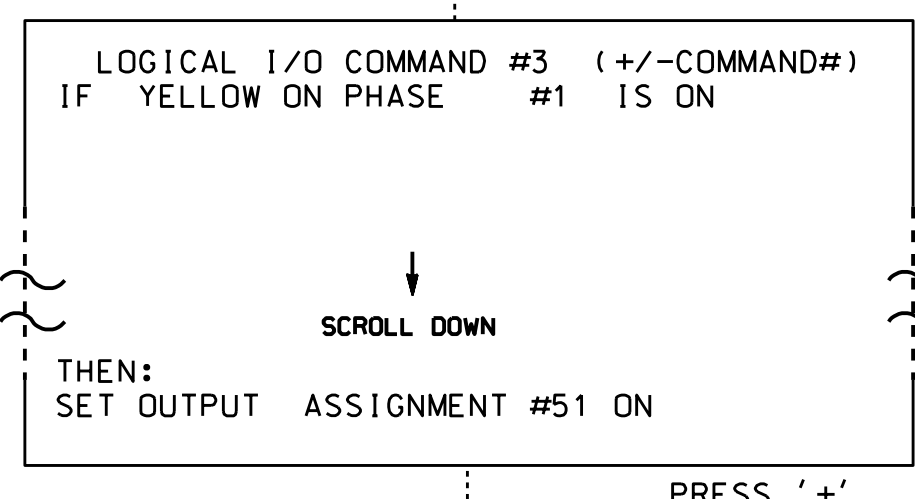
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, and 6.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



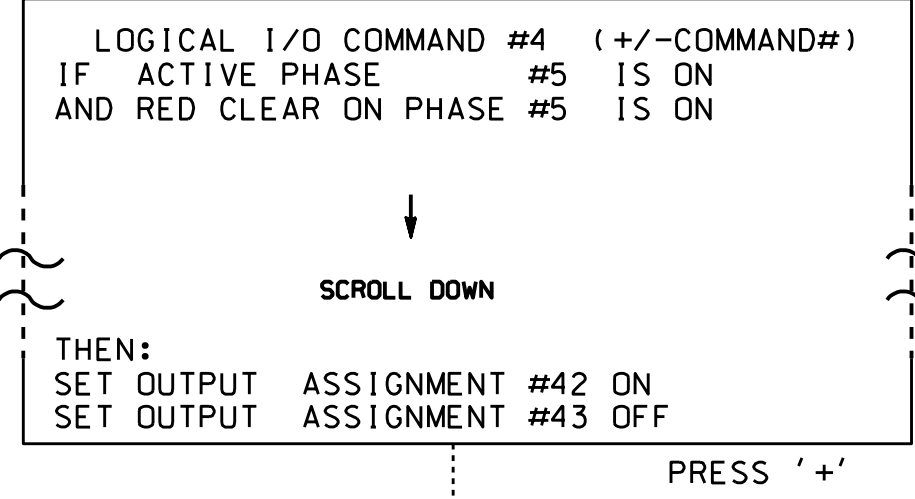
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



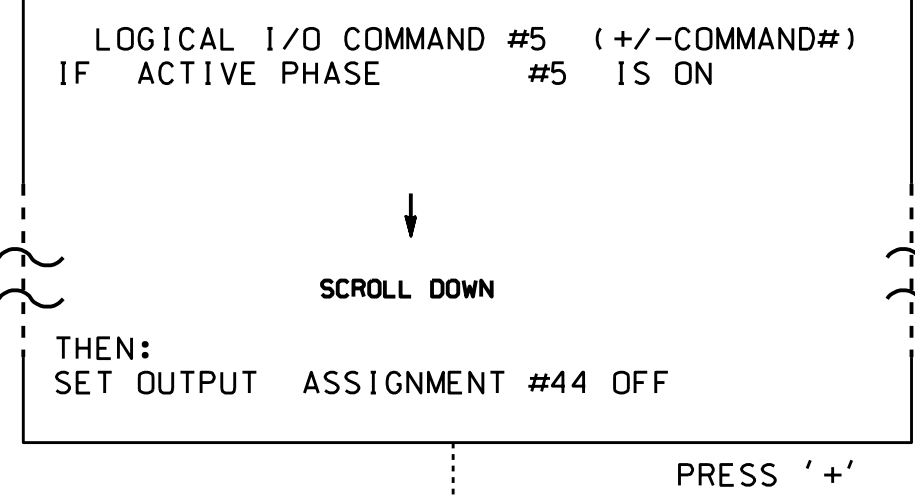
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEAD 11).



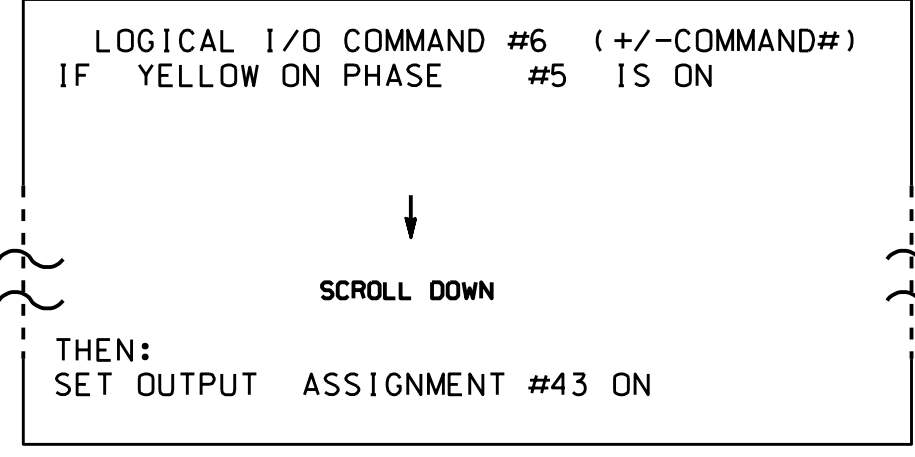
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

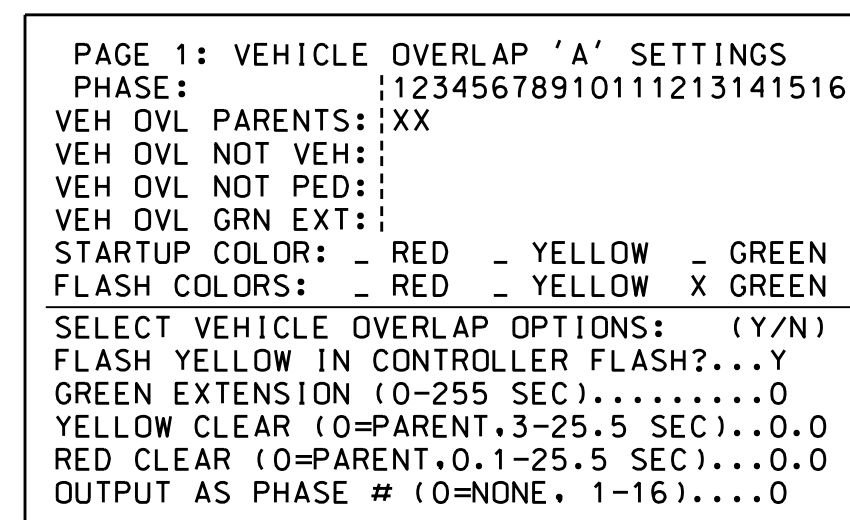
LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

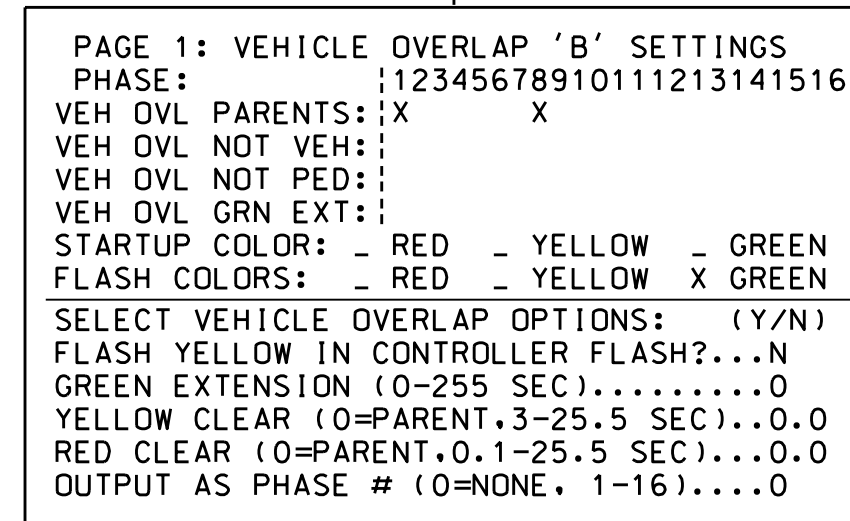
**OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING**

(program controller as shown below)

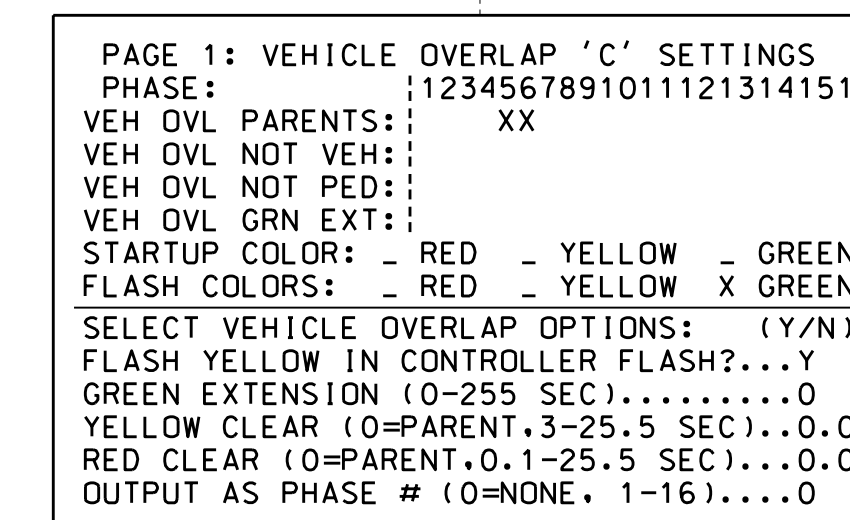
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



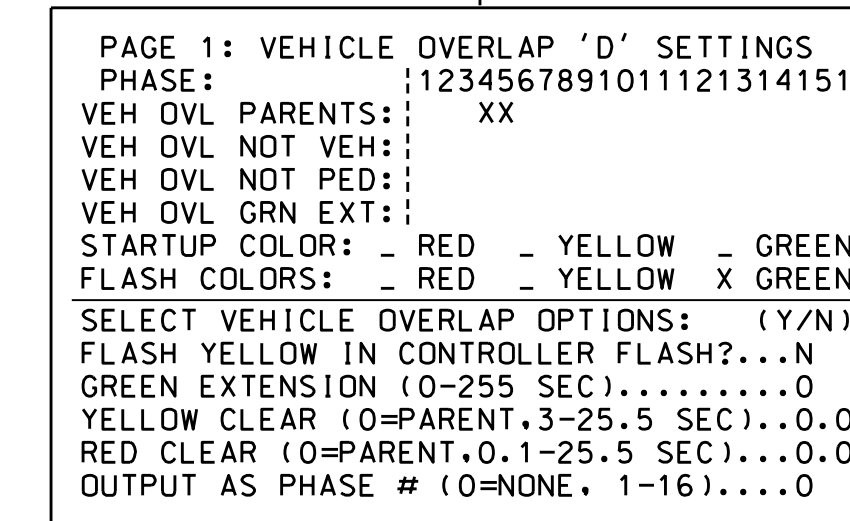
← NOTICE GREEN FLASH



← NOTICE GREEN FLASH



← NOTICE GREEN FLASH



← NOTICE GREEN FLASH

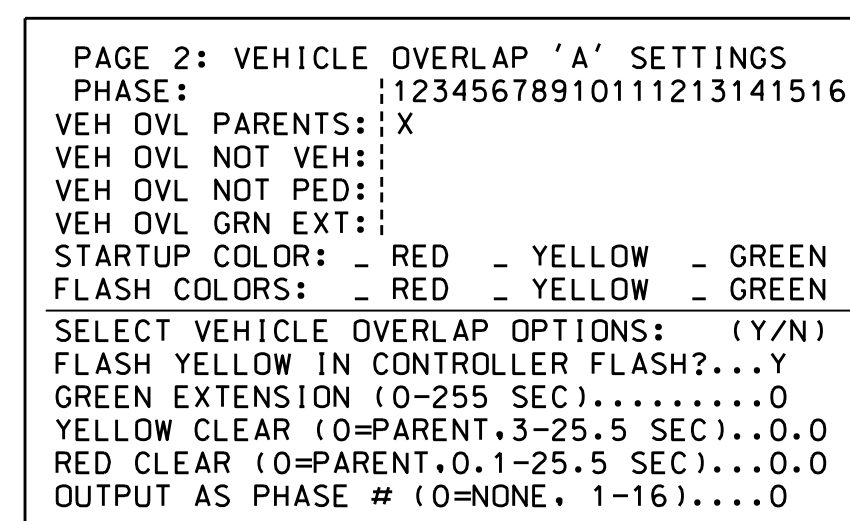
OVERLAP PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING**

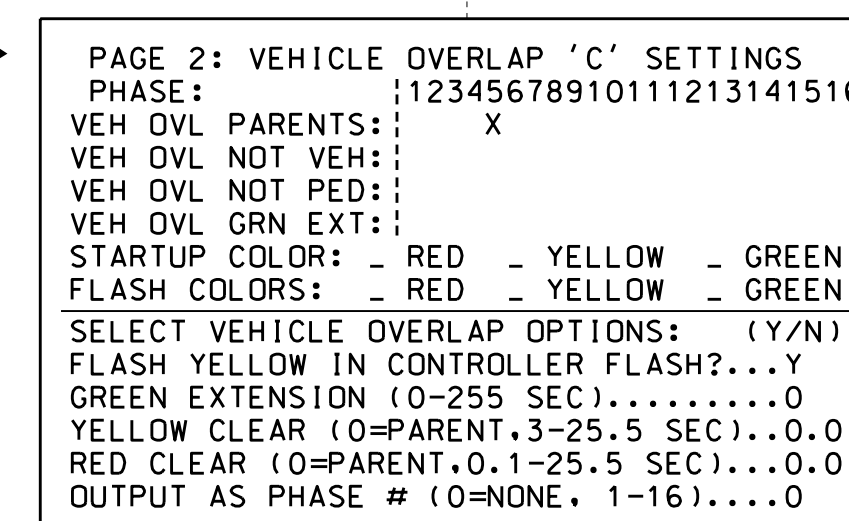
(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

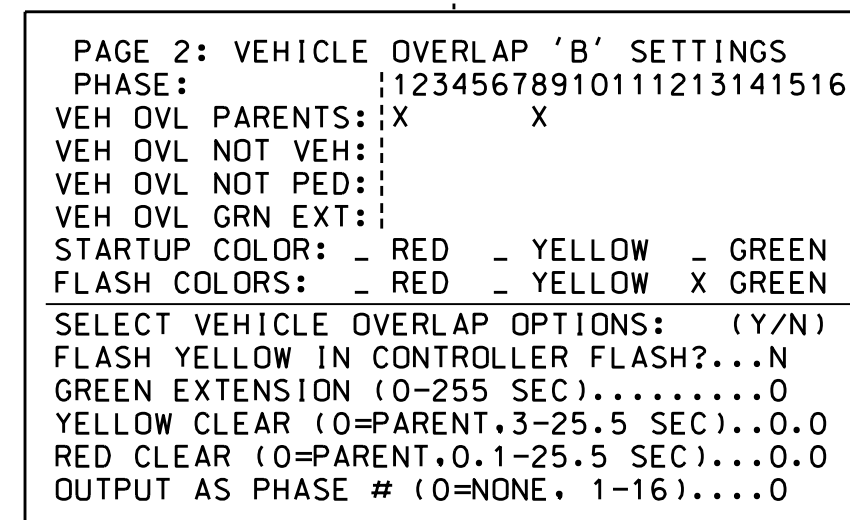
NOTICE PAGE 2 →



NOTICE PAGE 2 →

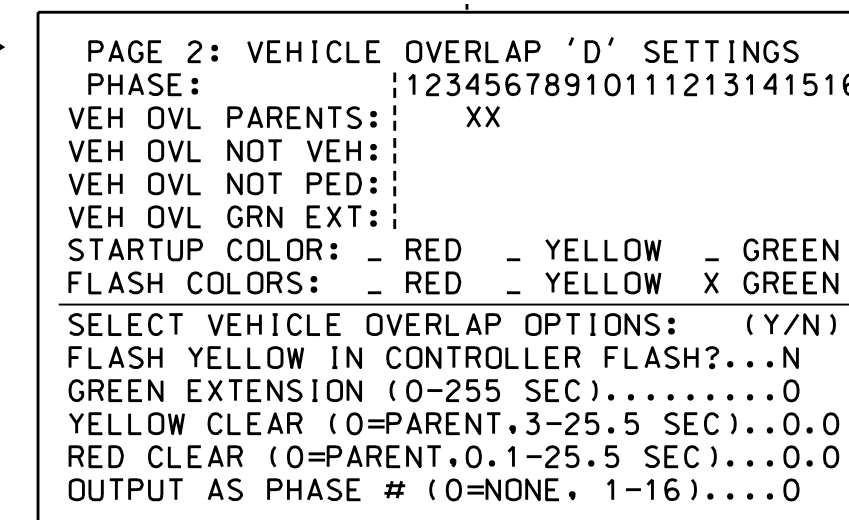


NOTICE PAGE 2 →



← NOTICE GREEN FLASH

NOTICE PAGE 2 →



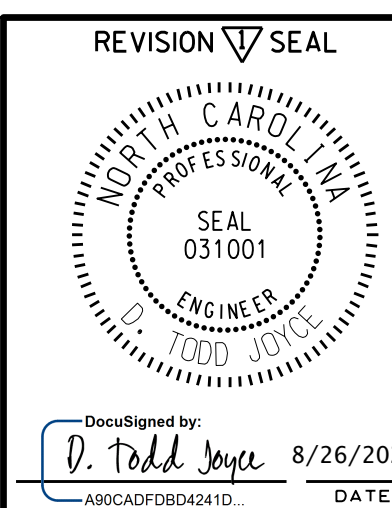
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

Electrical Detail - Sheet 2 of 5  
 Signal Upgrade  
 Final Design

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1027  
 DESIGNED: February 2018  
 SEALED: 8-1-18  
 REVISED: 8/26/2020



ELECTRICAL AND PROGRAMMING DETAILS FOR:	
Prepared for:	US 17 (Market Street) at SR 2734 (Marsh Oaks Drive) / SR 2290 (Mendenhall Drive)
Division 03	New Hanover Co. Wilmington
PLAN DATE: February 2018	REVIEWED BY: A.D. Klinskies
PREPARED BY: A.H. Thornburg	REVIEWED BY: N.R. Simmons
REVISIONS	INIT. DATE
Remove all phases from 'Startup Ped Collis'. CES	(Signature) 8/26/20

SEAL  
 Not a certified document. This document originally issued and sealed by Natasha M. Rodevick, PE, #031464 on 08/01/2018. This document shall not be considered a certified document.  
 SIGNATURE DATE  
 SIG. INVENTORY NO. 03-1027