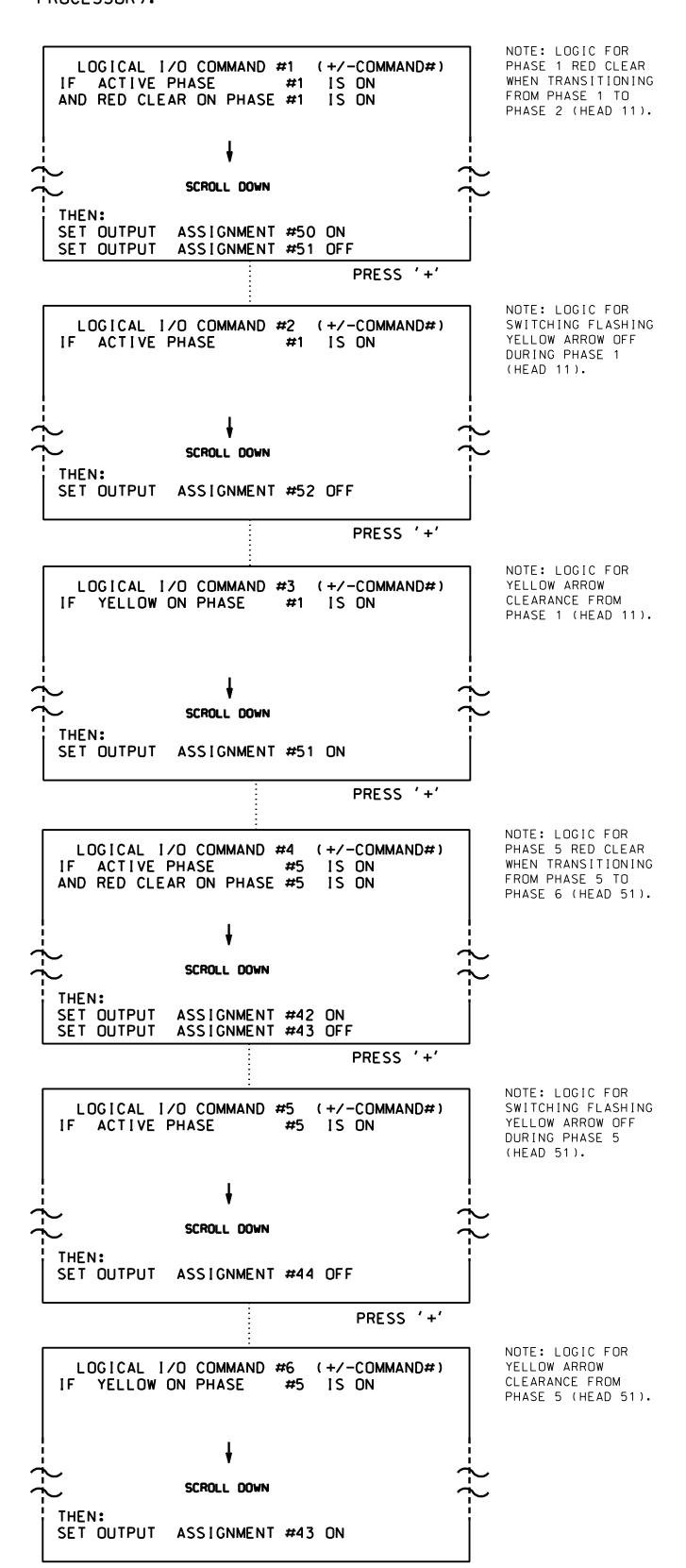
### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

# TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, and 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

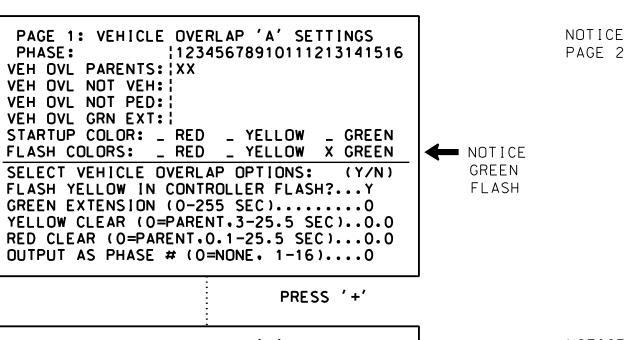
PROJECT REFERENCE NO. Sig. 11

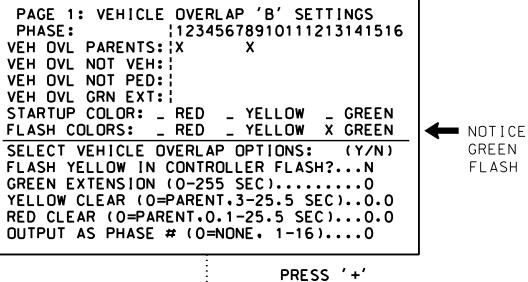
U-4902D

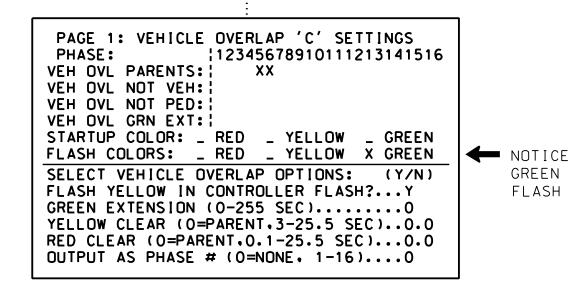
## OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).





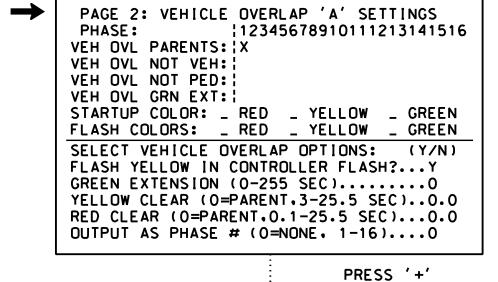


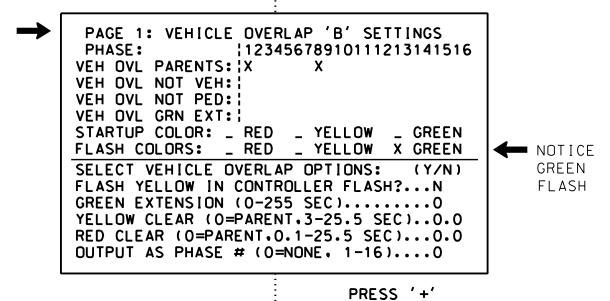
OVERLAP PROGRAMMING COMPLETE

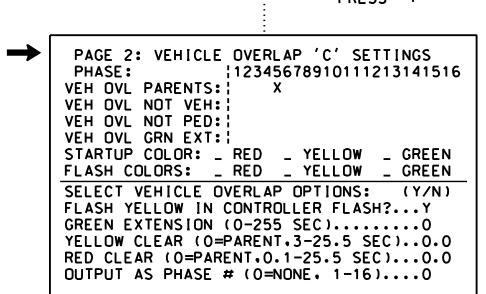
## OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.







OVERLAP PROGRAMMING COMPLETE

#### OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-1027T2 DESIGNED: February 2018 SEALED: 8-1-18

REVISED: N/A

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Electrical Detail - Sheet 2 of 5

|Signal Upgrade Temporary Design 2

ELECTRICAL AND PROGRAMMING

PAGE 2

US 17 (Market Street) at



SR 2734 (Marsh Oaks Drive)/ SR 2290 (Mendenhall Drive)

Division 03 New Hanover Co. February 2018 REVIEWED BY: A.D. Klinksiek PLAN DATE: PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons REVISIONS

INIT. DATE

SIG. INVENTORY NO. 03-1027

TH CAROL

COFESSION.

031464

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED**