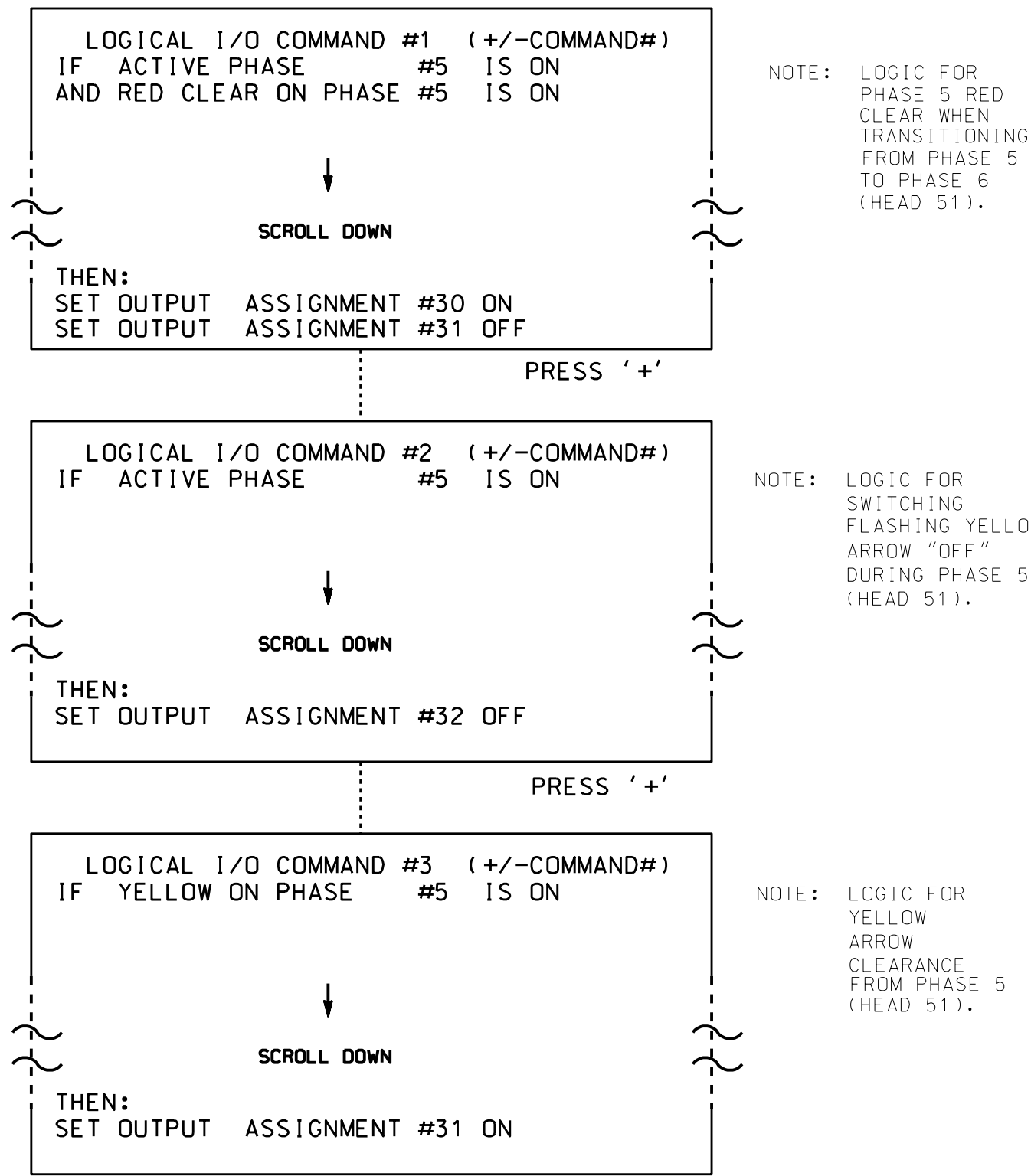


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 and 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 30 =	Overlap C Red
OUTPUT 31 =	Overlap C Yellow
OUTPUT 32 =	Overlap C Green
OUTPUT 34 =	Phase 5 Green

Note: All outputs shown above have been remapped. See sheet 3 of this electrical detail.

**OVERLAP PROGRAMMING DETAIL
FOR DEFAULT PHASING**

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).
PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL
FOR ALTERNATE PHASING**

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).
PRESS 'NEXT' TO ADVANCE TO PAGE 2.

PRESS '+' TWICE

NOTICE →
PAGE 2

```

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 09-0186T3
DESIGNED: November 2017
SEALED: 12-11-17
REVISED: N/A

16-1116-2018 13:20
 S:\IT\SS\115\SIGNAL\work\hgr\oups\sig_man\peter.peterson\090186_snc.ele_20171220.dgn
 J.peterson

Electrical Detail - Temp. 3 (TMP Phase I, Detail 3) - Sheet 2 of 5		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED	
ELECTRICAL AND PROGRAMMING DETAILS FOR:		SEAL	
Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529		SR 2456 (N. Liberty Street) at Glenn Avenue Division 9 Forsyth County Winston-Salem PLAN DATE: December 2017 REVIEWED BY: PREPARED BY: James Peterson REVIEWED BY: REVISIONS INIT. DATE DocuSigned by: Keith M. Mims 12/20/2017 2F80798E6C02465 DATE SIG. INVENTORY NO. 09-0186T3	