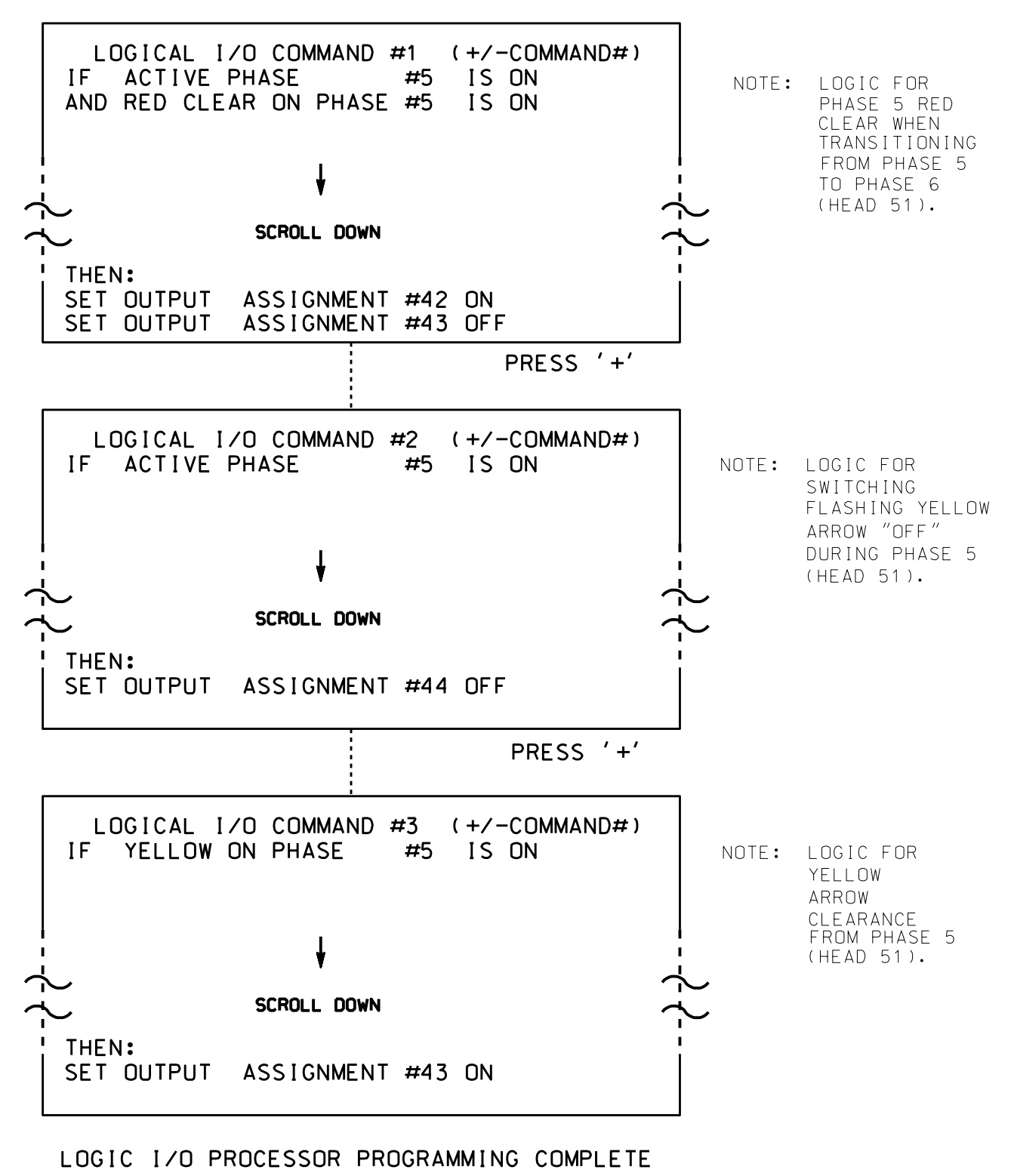


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

*(program controller as shown below)*

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



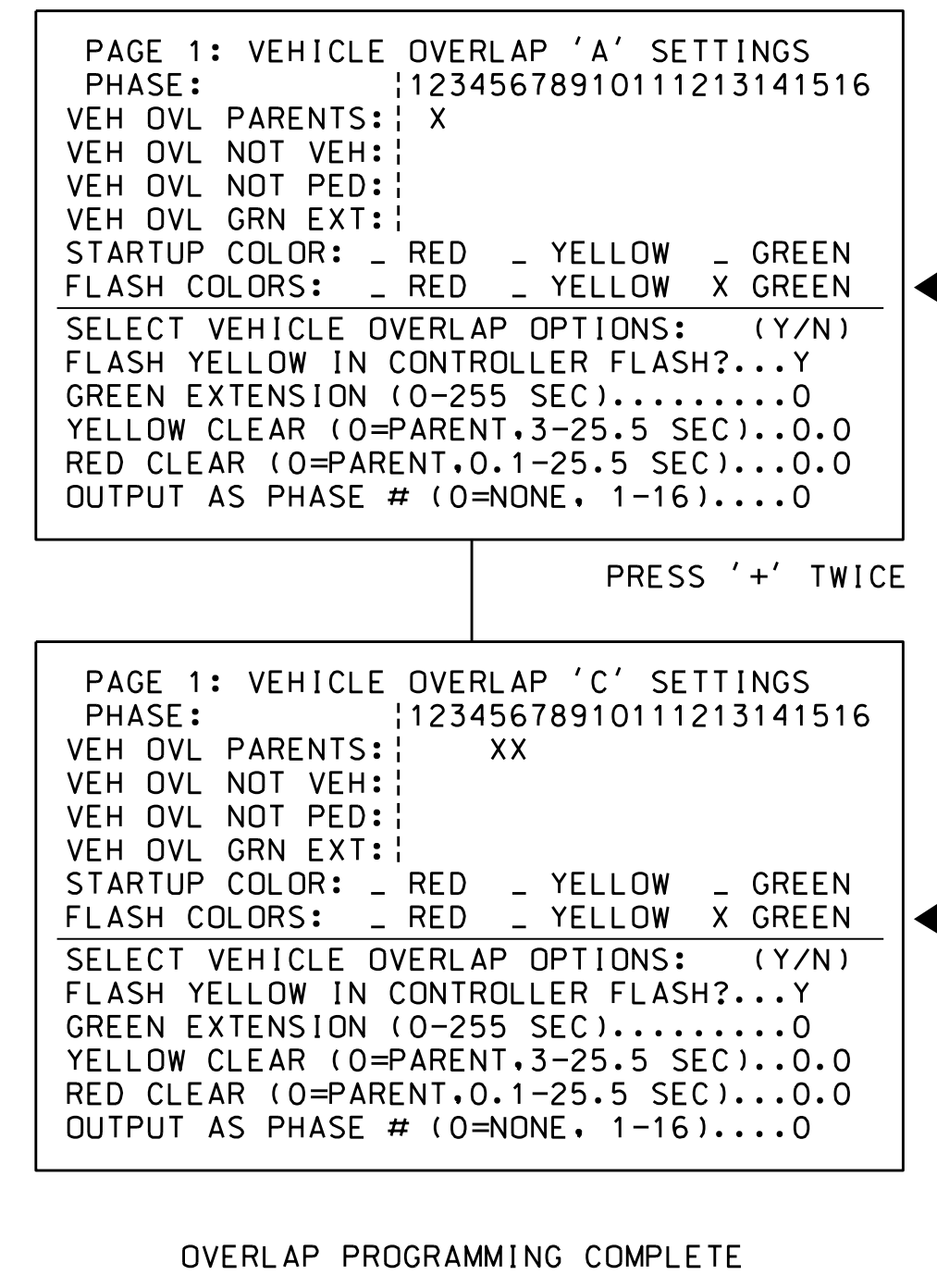
**OUTPUT REFERENCE SCHEDULE**

OUTPUT 42	= Overlap C Red
OUTPUT 43	= Overlap C Yellow
OUTPUT 44	= Overlap C Green

**OVERLAP PROGRAMMING DETAIL  
FOR DEFAULT PHASING**

*(program controller as shown below)*

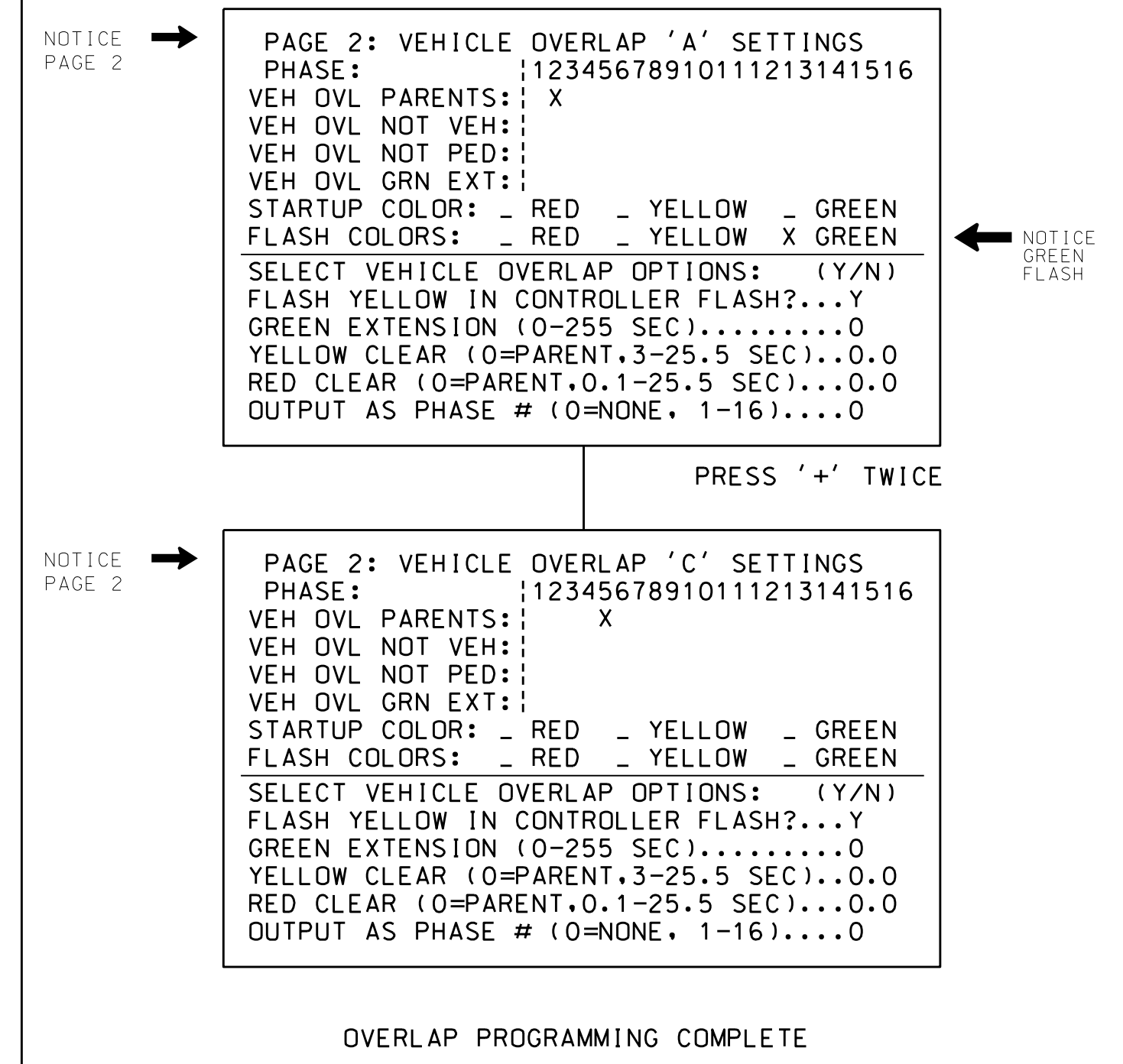
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



**OVERLAP PROGRAMMING DETAIL  
FOR ALTERNATE PHASING**

*(program controller as shown below)*

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0016  
DESIGNED: November 2017  
SEALED: 12-07-17  
REVISED: N/A

Electrical Detail - Sheet 2 of 4

ELECTRICAL AND PROGRAMMING DETAILS FOR:  
Prepared In the Offices of:  
750 N. Greenfield Pkwy, Garner, NC 27529

SR 2264 (Akron Drive) at Glenn Avenue

Division 9 Forsyth County Winston-Salem

PLAN DATE: November 2017 REVIEWED BY:  
PREPARED BY: James Peterson REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSigned by:  
Keith M. Mins 12/19/2017  
2F8078E8CD3425 DATE

SIG. INVENTORY NO. 09-0016

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
SEAL 036880  
KEITH M. MINS

16-1114-2016\_13.cdr  
S:\1114\1114\1114\_Signals\work\hgr\output\sig\_Minh\Peter.sch\090016\_sch.ele\_20171219.dgn  
J. Peterson