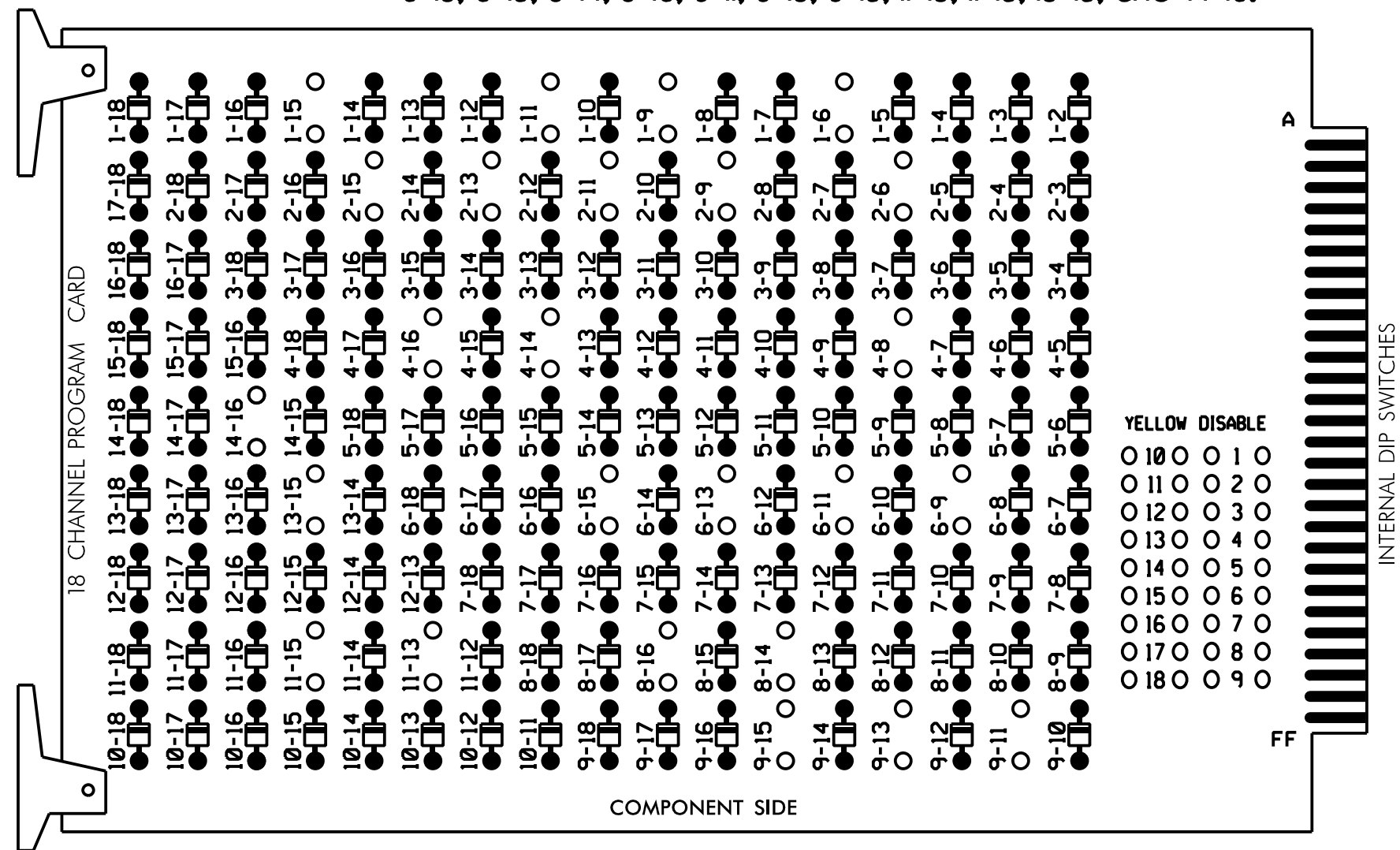


EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

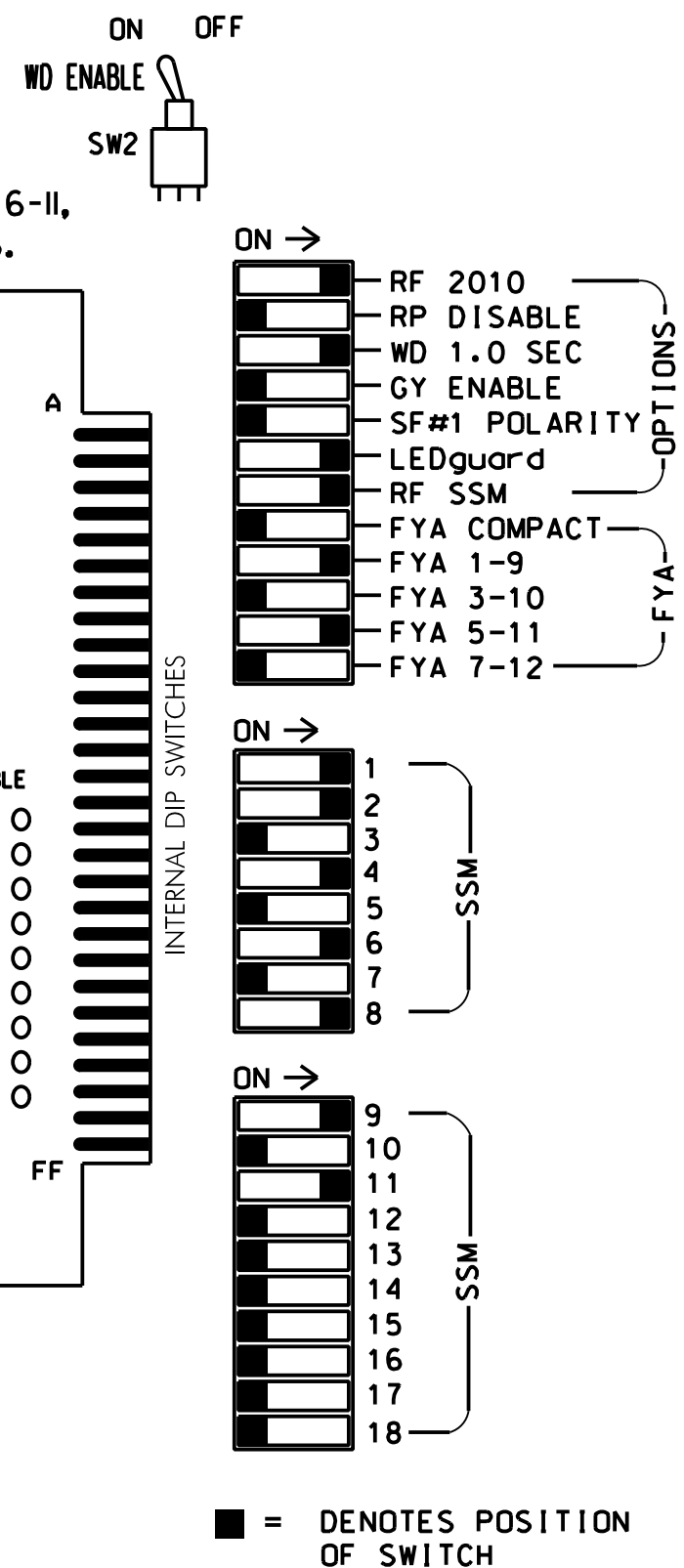
REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 1-15, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-14, 4-16, 6-9, 6-11, 6-13, 6-15, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 13-15, and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Ensure that jumper SEL9 is removed from the monitor board.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 3,5,7,10,12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2, 4, 6 and 8 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Gilead Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....EXISTING 332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S2P,S4,S4P,S6,S6P,S8,S8P,S9,S12
 PHASES USED.....1,2,2 PED,4,4 PED,6,6 PED,8,8 PED
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....6
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	9	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	22,23	P21, P22	NU	41,42	P41, P42	NU	61,62	P61, P62	NU	81,82	P81, P82	11	NU	NU	21	NU	NU
RED	*	128			101			134			107								
YELLOW		129			102			135			108								
GREEN		130			103			136			109								
RED ARROW														A121			A114		
YELLOW ARROW		126												A122			A115		
FLASHING YELLOW ARROW														A123			A116		
GREEN ARROW	127	127																	
Hand					113		104		119		110								
Person					115		105		121		112								

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 2	S	∅ 4	S	S	SYS. DET. S39	S	S	∅ 2 PED	∅ 6 PED	FS
L	NOT USED	NOT USED	∅ 2	NOT USED	Y	NOT USED	Y	Y	SYS. DET. S40	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
U	S	∅ 6	S	W	S	∅ 8	S	S	SYS. DET. S41	S	S	S	S	S
L	Y	NOT USED	Y	W	Y	NOT USED	Y	Y	NOT USED	Y	Y	Y	Y	Y

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

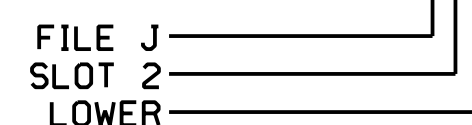
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
		J4U	48	10	26	6	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
2B	TB2-11,12	I3L	76	38	42	2	Y	Y			
2C	TB4-1,2	I4U	47	9	22	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			5
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
* S39	TB6-9,10	I9U	60	22	11	SYS					
* S40	TB6-11,12	I9L	62	24	13	SYS					
* S41	TB7-9,10	J9U	59	21	15	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

- Add jumper from I1-W to J4-W, on rear of input file.
- SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

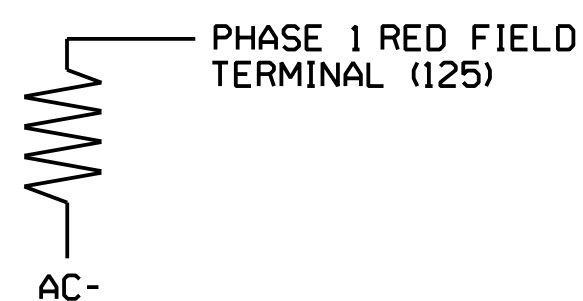
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

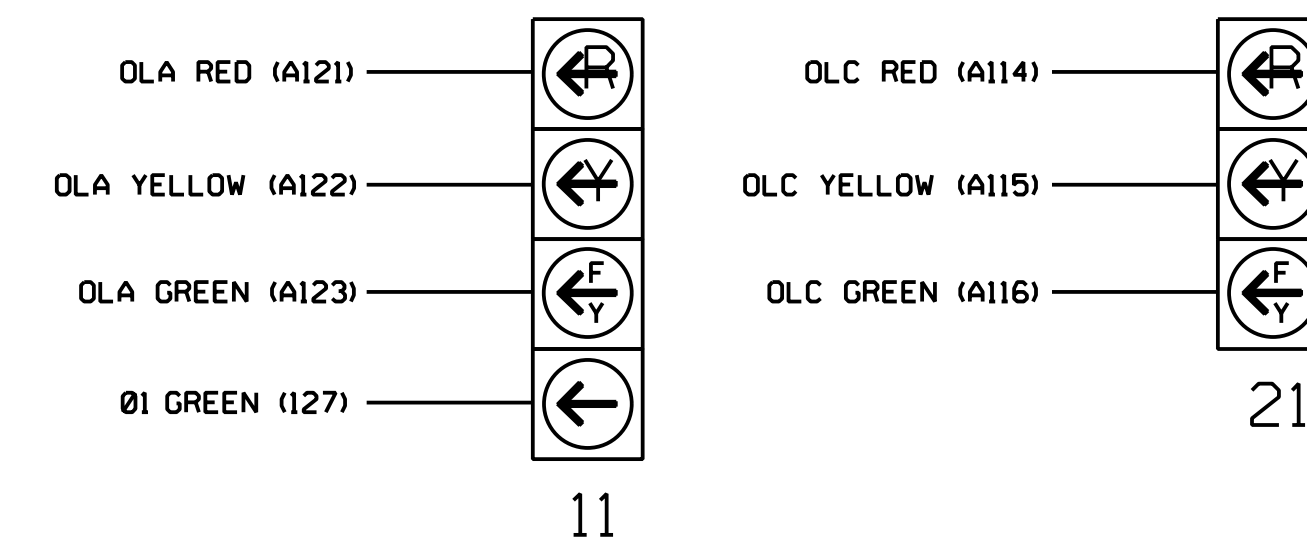
(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-1752
 DESIGNED: December 2017
 SEALED: 04-23-2018
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

	SR 2136 (Gilead Road) at SR 2138 (McCoy Road)		
	Division 10 Mecklenburg Co. Huntersville PLAN DATE: December 2017 PREPARED BY: J.T. Thibault	REVIEWED BY: A.D. Klinksiek REVIEWED BY: N.R. Simmons	
Revisions Table:		DocuSigned by: Natasha R. Simmons DATE: 4/23/2018	SIG. INVENTORY NO. 10-1752