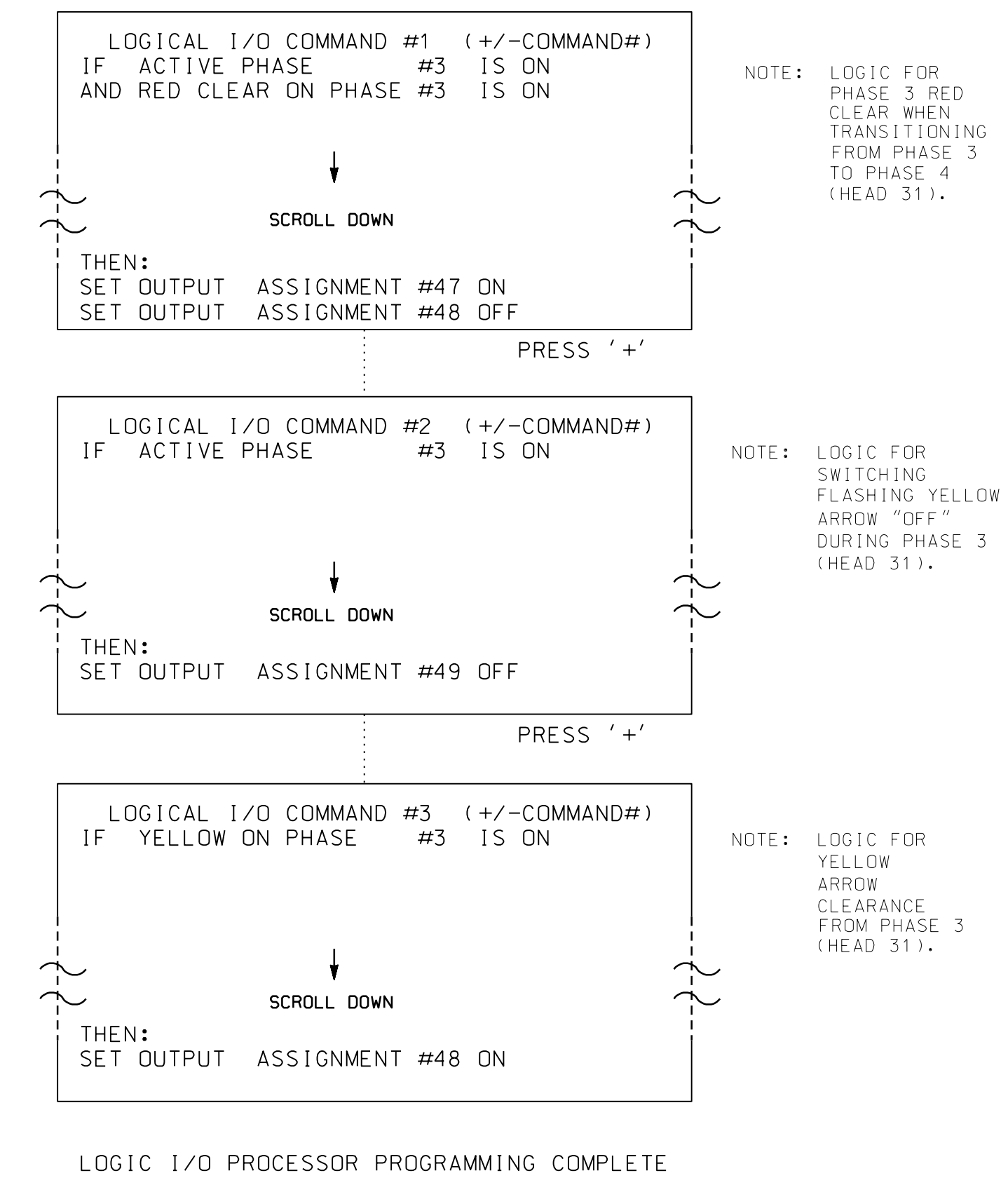


LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

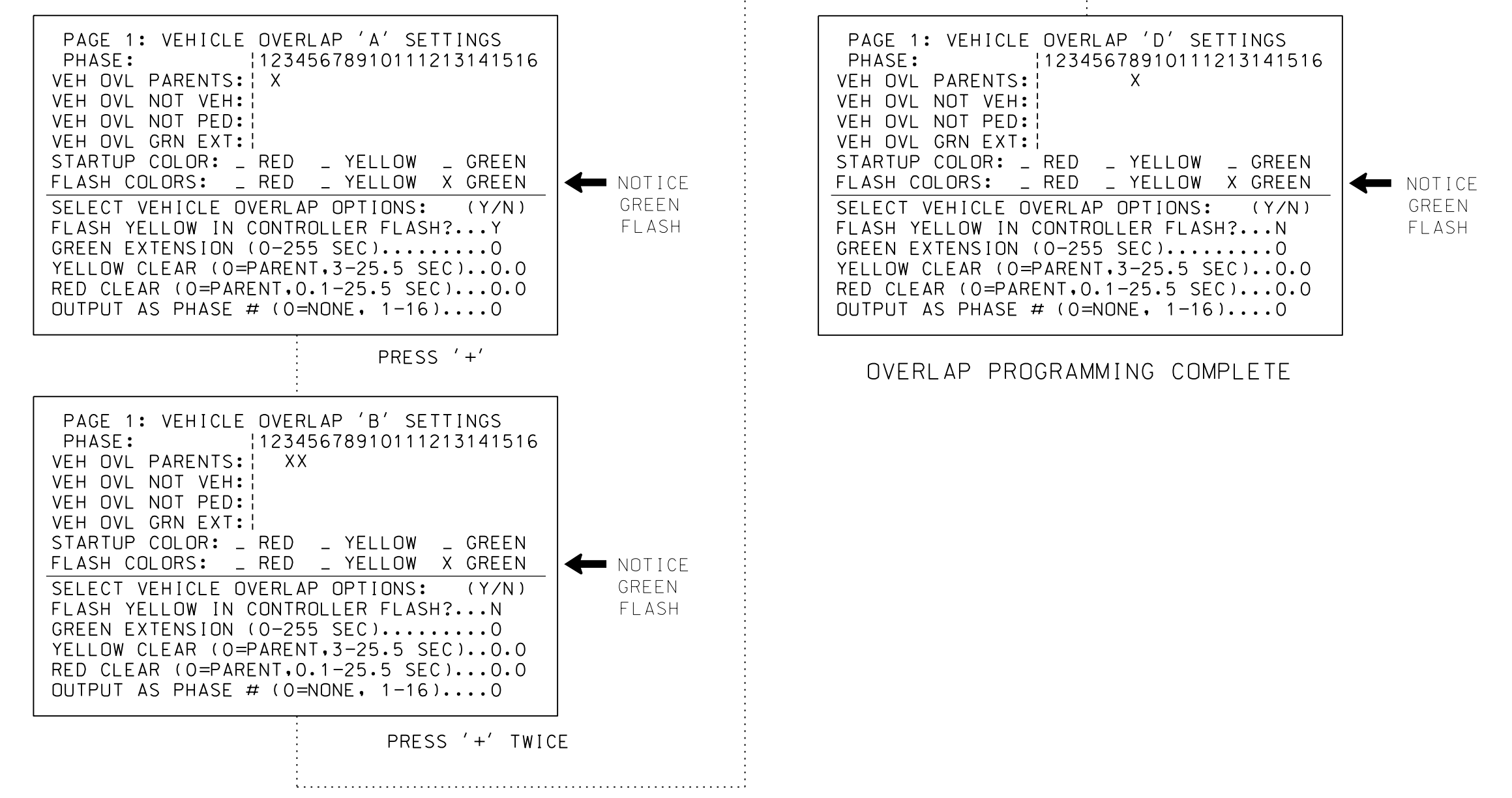
OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 47	= Overlap B Red
OUTPUT 48	= Overlap B Yellow
OUTPUT 49	= Overlap B Green

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

OVERLAP PROGRAMMING DETAIL (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0874
DESIGNED: January 2018
SEALED: 5/25/2018
REVISED: N/A

5/25/2018 L:\Morrisville\1106\00818R3825B\Traffic\Signal\Electrical\04-0874\ref-00-192.dgn

Final Design
Electrical Detail - Sheet 2 of 2



ELECTRICAL AND PROGRAMMING DETAILS FOR:		NC 42 at SR 1003 (Buffalo Road)		
Prepared for the Offices of: 		Division 4 Johnston County Clayton		
PLAN DATE: January 2018	REVIEWED BY: J O Deaton	PREPARED BY: M W Yalch	REVIEWED BY:	DocuSigned by: 40FFBAC430B040F SIG. INVENTORY NO. 04-0874
REVISIONS		INIT.	DATE	

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED