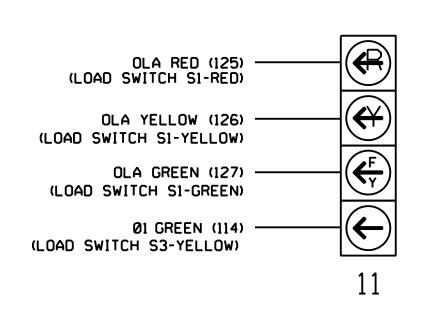


NU = Not Used

- * Denotes install load resistor. See load resistor installation detail this sheet.
- ★ See pictorial of head wiring in detail this sheet. NOTE: Load switches S1 and S3 require output remapping. See Sheet 3 of this electrical detail for instructions.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



<u>NOTE</u>

The sequence display for signal head 11 requires special logic and output remapping. See sheets 2-3 for programming instructions.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-1106T2 DESIGNED: December 2017 SEALED: 04-23-2018 REVISED: N/A

Electrical Detail - Sheet 1 of 3

Signal Upgrade Temporary Design 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:

I-77 SB Ramps

Division 10 Mecklenburg Co. Huntersville December 2017 REVIEWED BY: A.D. Klinksiek PLAN DATE: PREPARED BY: A.H. Thornburg REVIEWED BY: N.R. Simmons REVISIONS

SR 2136 (Gilead Road)

ORIGINESS 1012 031464 MOINEER. INIT. DATE

SIG. INVENTORY NO. 10-1106

NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- 2. Enable Simultaneous Gap-Out for all Phases.
- 3. Program phases 2 and 6 for Startup In Green.
- 4. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- 5. The cabinet and controller are part of the Gilead Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E SOFTWARE.....ECONOLITE OASIS CABINET MOUNT.....POLE OUTPUT FILE POSITIONS...12 LOAD SWITCHES USED.....S1,S2,S3,S5,S8 OVERLAP "A".....1+2 OVERLAP "B".....NOT USED OVERLAP "C".....NOT USED OVERLAP "D".....NOT USED

= DENOTES POSITION

OF SWITCH

-RF 2010

⊩RP DISABLE

₩D 1.0 SEC CY ENABLE

SF#1 POLARITY

FYA COMPACT—

─ LEDguard

⊢RF SSM

₽- FYA 1-9

FYA 5-11 FYA 7-12

FYA 3-10

WD ENABLE 🕥

1. Card is provided with all diode jumpers in place. Removal

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

COMPONENT SIDE

REMOVE JUMPERS AS SHOWN

3. Ensure that Red Enable is active at all times during normal operation.

EDI MODEL 2018ECLip-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

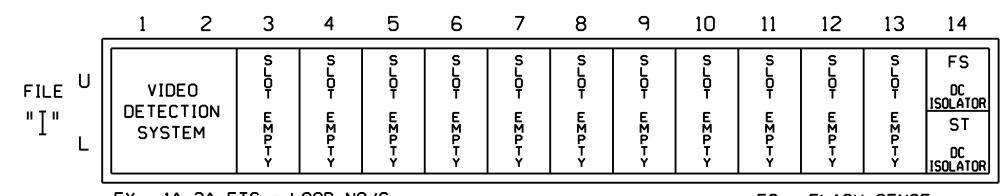
REMOVE DIODE JUMPERS 1-2, 1-6, 2-6, and 6-9.

4. Special cabinet wiring is required to utilize FYA COMPACT mode. See Ped Yellow Conflict Monitor Wiring Detail on this sheet.

of any jumper allows its channels to run concurrently.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

NOTES:

FS = FLASH SENSE ST = STOP TIME

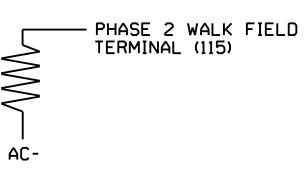
SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K 25W (min) 2.0K - 3.0K 10W (min)



PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode with the 2018ECLip-NC Monitor, the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to chan. 9 green (monitor pin 13).

Follow the instructions below to make the appropriate connections: STEP 1: Fold down rear panel of output file.

STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).

STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:

CMU-13 — 2PY (term. 114)

NOTE: Some cabinet manufacturers use keyed connectors to accomplish this wiring configuration. If connectors are used, fold down the rear panel of the output file and find the set of 3 keyed connectors and connect them as shown below:

3-6PY

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