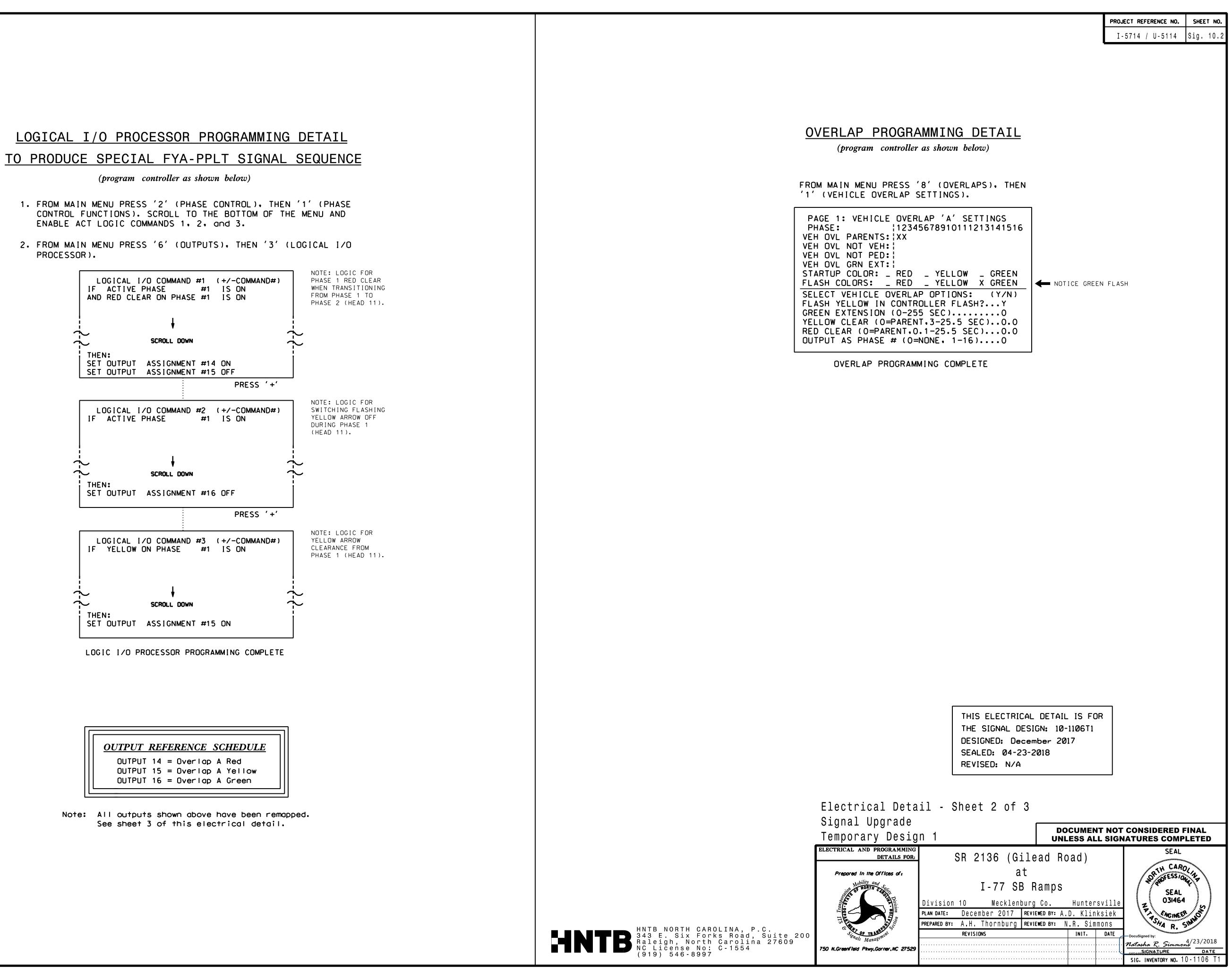
- ENABLE ACT LOGIC COMMANDS 1, 2, and 3.
- PROCESSOR).



<u>0</u>	UTPUT	<u>REF</u>	E	RENCE	<u>sc</u>	HEDULI
	OUTPUT	14	=	Overlop	A	Red
	OUTPUT	15	=	Overlap	Α	Yellow
	OUTPUT	16	=	Overlap	Α	Green

THIS ELECTRICAL DETAIL IS FOR							
THE SIGNAL DESIGN: 10-1106T1							
DESIGNED: December 2017							
SEALED: 04-23-2018							
REVISED: N/A							

nal unorade			
porary Desig	n 1		CONSIDERED FINAL ATURES COMPLETED
ICAL AND PROGRAMMING DETAILS FOR:	SR 2136 (Gile	ead Road)	SEAL
repared in the Offices of:	àt	OR THESSION	
Mubility and Charles Do	I-77 SB F Division 10 Mecklenburg	SEAL 031464	
lion work	PLAN DATE: December 2017 REVI	ewed By: A.D. Klinksiek	PA MOINEER S
	PREPARED BY: A.H. Thornburg REVI	ewed by: N.R. Simmons	JAA R. SIMM
Signals Management	REVISIONS	INIT. DATE	-DocuSigned by: Natasha R. Simmons
reenfield Pkwy.Garner.NC 27529			Matasha K. Simmons DATE
			SIG. INVENTORY NO. 10-1106 T1