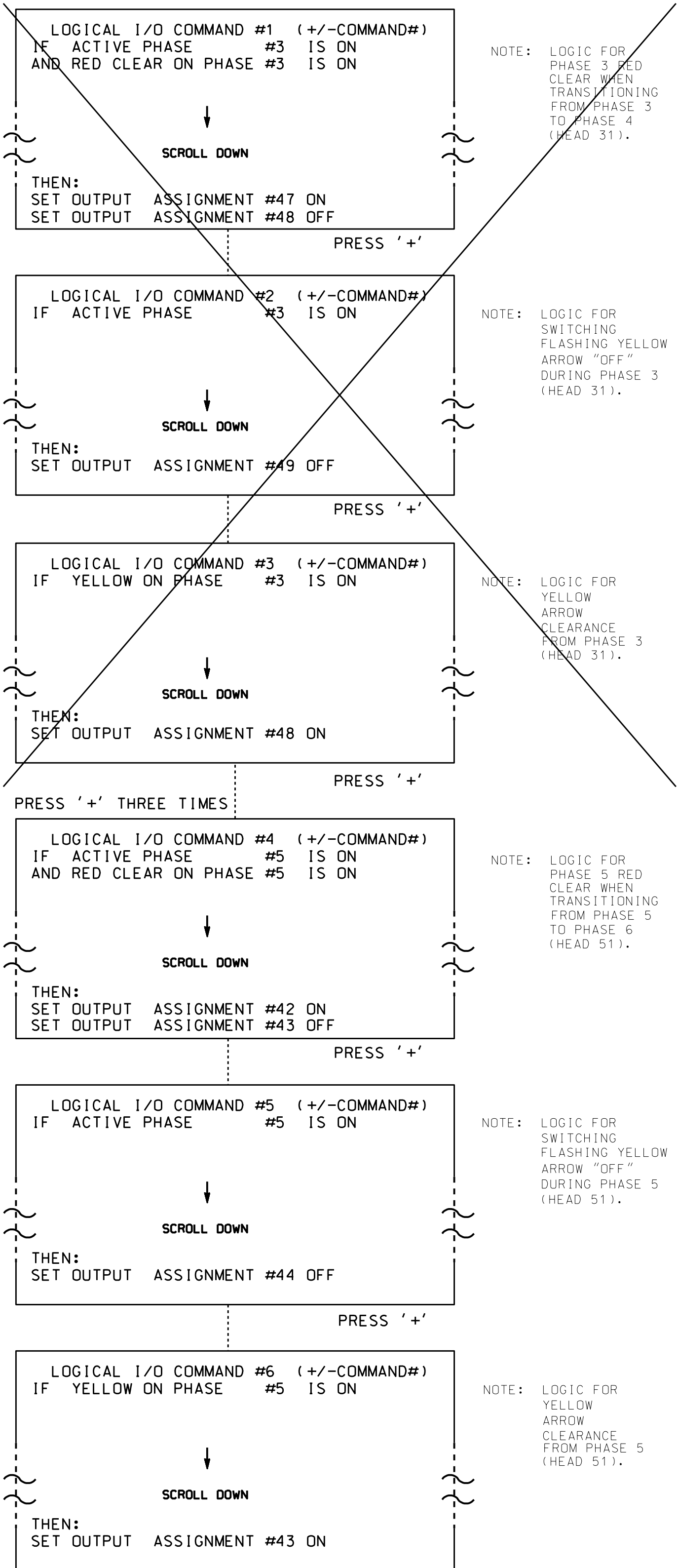


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 4, 5 AND 6.
- IMPORTANT:** 1. If present, remove ACT LOGIC COMMANDS 1, 2 and 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

DYNAMIC BACK-UP CONTROL PROGRAMMING

(program controller as shown below)

- 1. From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Scroll to the bottom of the menu and enable Dynamic/Backup Control Function 1.
- 2. From Phase Control Functions Menu press '2' (Dynamic/Backup Control Functions).

```

DYNAMIC/BACKUP CONTROL FUNCTION #01
OVERLAPS: | ABCDEF GHIJKLMN OP
IF OVERLAPS ARE ACTIVE |
OR PHASES: | 12345678910111213141516
IF PHASES ARE ON: | X
OMIT PHASES | X
CALL PHASES |
    
```

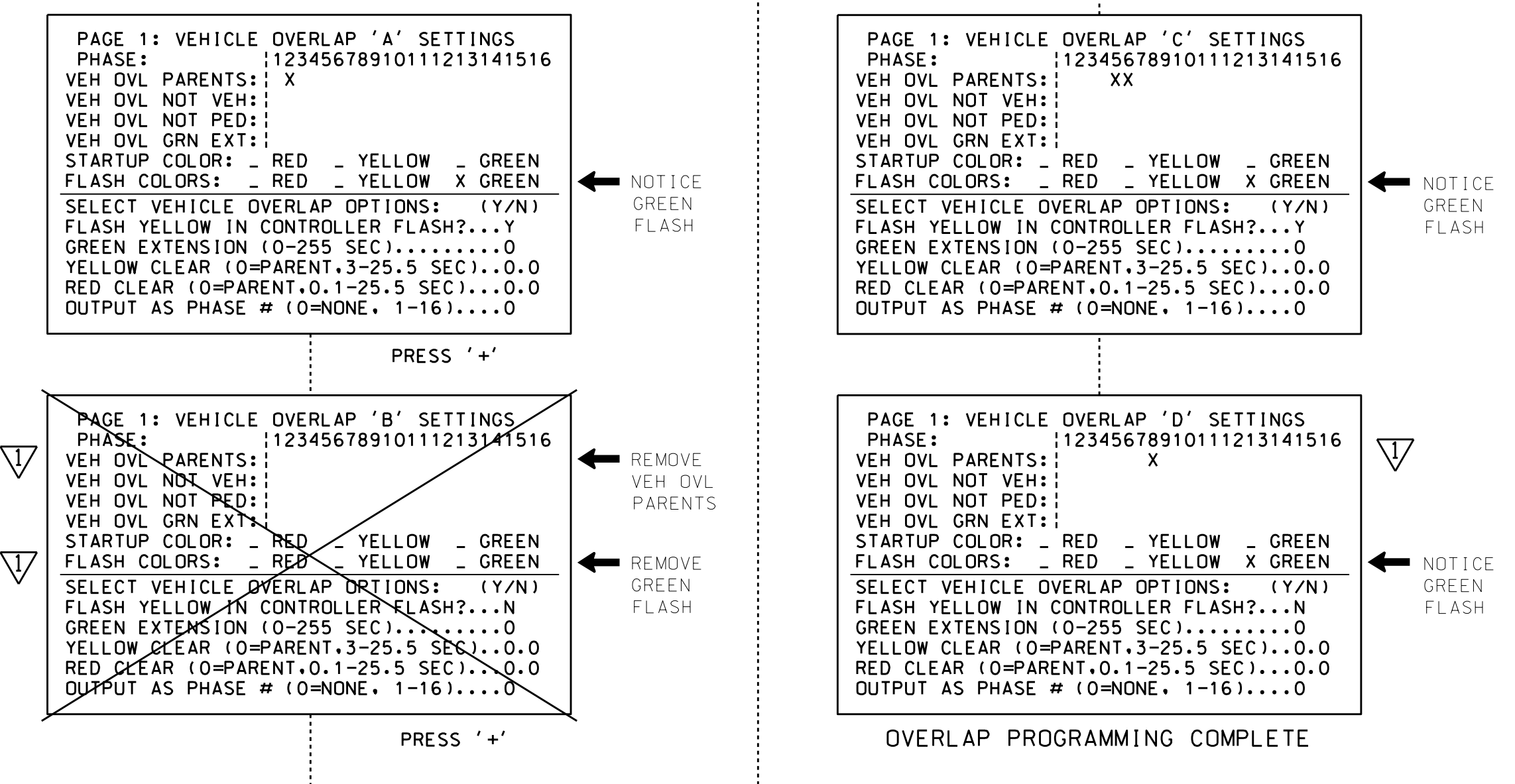
BACKUP PROTECTION PROGRAMMING COMPLETE

- IMPORTANT:** 1. If present, remove all functions from 'Dynamic/Backup'.
- IMPORTANT:** 2. If present, remove all phases from all 'Dynamic/Backup Control Functions'.

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT REFERENCE SCHEDULE
USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1432
DESIGNED: January 2018
SEALED: 5/25/2018
REVISED: 9/14/2020

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

REVISION SEAL 	ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	NC 42 at Flowers Parkway		SEAL Not a certified document as to the Original Document but only as to the Revisions - This document originally issued and sealed by James Deaton, 07438, on 5/25/2018. This document is only certified as to the revisions. SIG. INVENTORY NO. 04-1432
		Division 4 PLAN DATE: January 2018 PREPARED BY: M W Vaich	Johnston County REVIEWED BY: J O Deaton REVIEWED BY:	

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