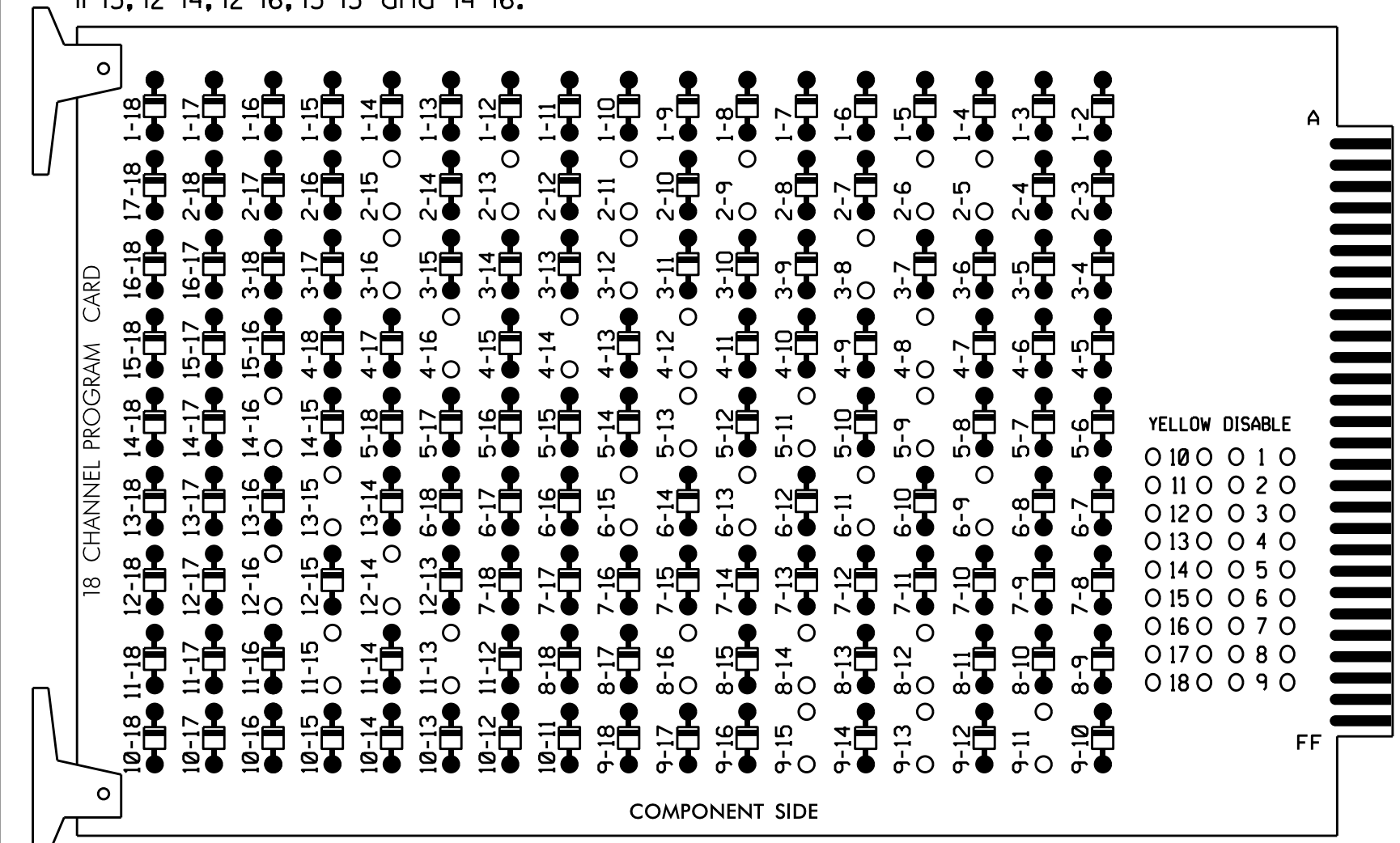


EDI MODEL 2018ECL-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-8, 3-12, 3-16, 4-8, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phase 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2, 4, 6, and 8 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the NC 42 (East of Clayton) Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S3,S4,S5,S6,S7,S8,S9,S11,
 S12,AUX S1,AUX S4,AUX S5
 PHASES USED.....2,3,4,5,6,8,2PED,4PED,6PED,
 8PED
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....8

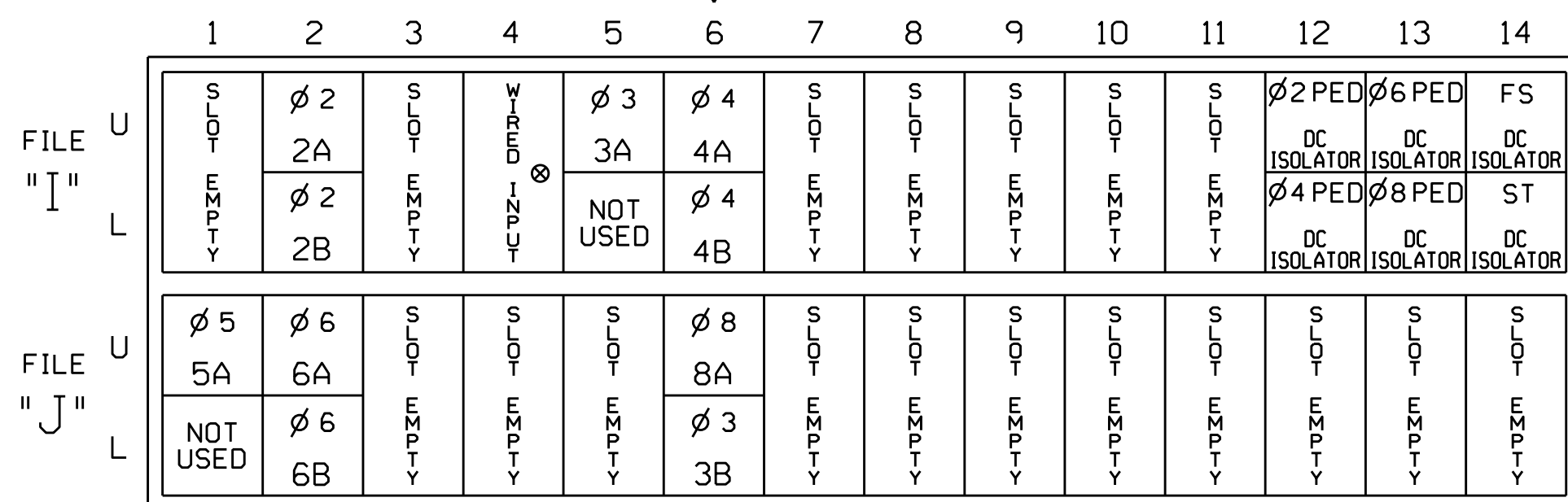
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	21,22	P21, P22	22	31,32	42,43	P41, P42	51	62,63	P61, P62	NU	81,82	P81, P82	61	NU	NU	51	41	NU
RED		128			101			134		107									
YELLOW		129			102		*	135		108									
GREEN		130			103			136		109									
RED ARROW					116								A121			A114	A101		
YELLOW ARROW					117	117							A122			A115	A102		
FLASHING YELLOW ARROW													A123			A116	A103		
GREEN ARROW					118	118		133											
Hand					113			104		119		110							
Walking					115			106		121		112							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

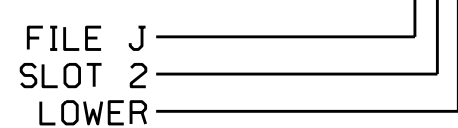
(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE POSITION LEGEND: J2L



INPUT FILE CONNECTION & PROGRAMMING CHART

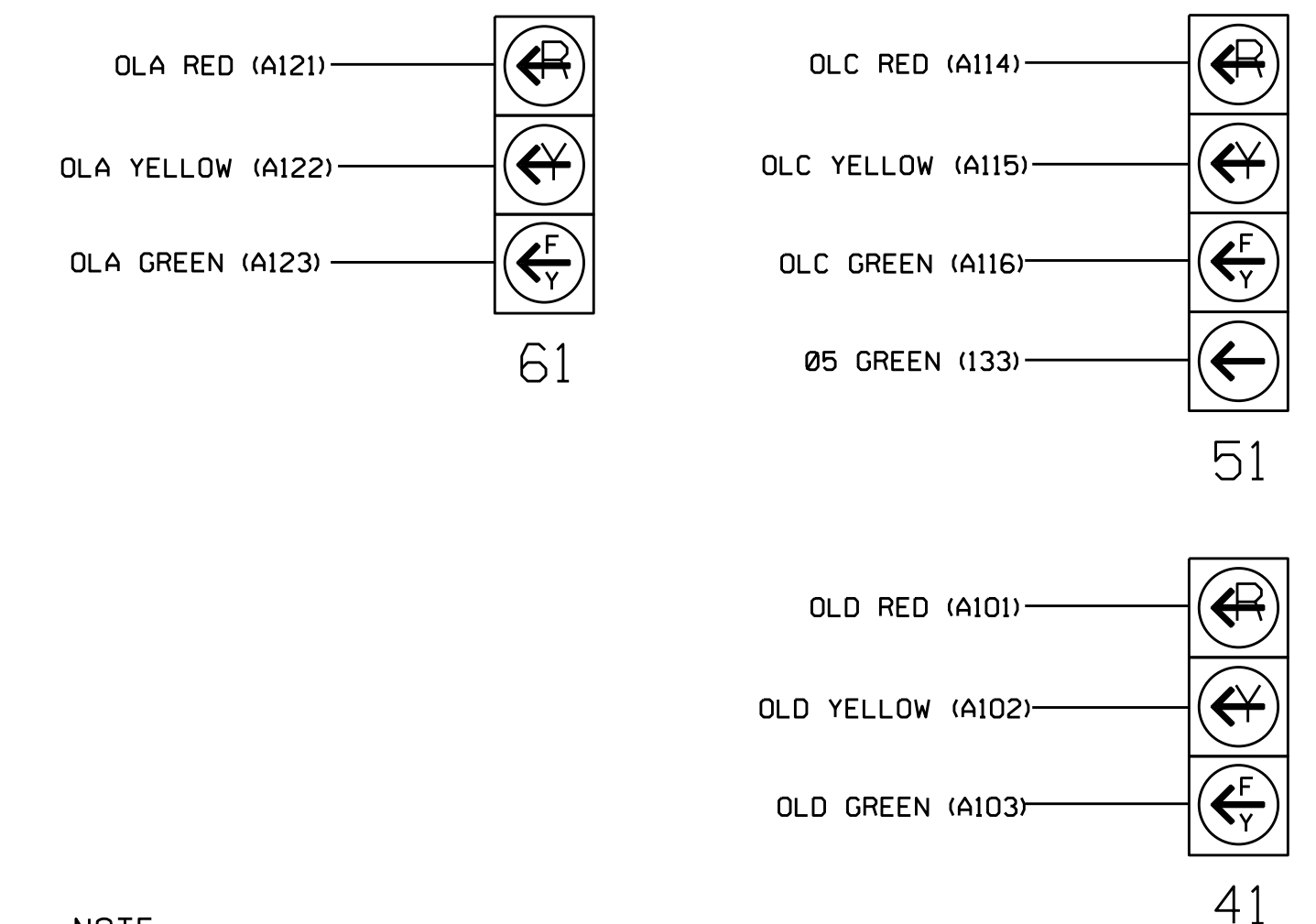
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			
3B	TB5-11,12	J6L	46	8	18	3	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
5A ¹	TB3-1,2	J1U	55	17	5	5	Y	Y			15
		I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29		PED 2	2	PED			
P41,P42	TB8-5,6	I12L	69	31		PED 4	4	PED			
P61,P62	TB8-7,9	I13U	68	30		PED 6	6	PED			
P81,P82	TB8-8,9	I13L	70	32		PED 8	8	PED			

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- ¹Add jumper from J1-W to 14-W, on rear of input file.
- ! If present, remove jumper from 15-W to J8-W, on rear of input file.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

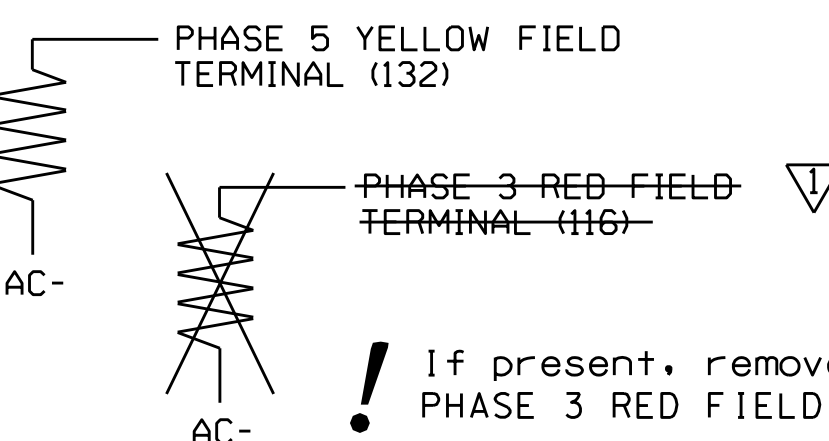
- The sequence display for signal head 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1432
 DESIGNED: January 2018
 SEALED: 5/25/2018
 REVISED: 9/14/2020

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



! If present, remove load resistor PHASE 3 RED FIELD TERMINAL (116)

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

REVISION SEAL

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

NC 42 at Flowers Parkway

Division 4 Johnston County Clayton

PLAN DATE: January 2018 REVIEWED BY: J O Deaton

PREPARED BY: M W Valch REVIEWED BY:

REVISIONS

NO.	DESCRIPTION	INIT.	DATE
1	Modify FYA signal heads. Add signal heads 31,32.		
2	Modify Input File and CM. CES.		

9/16/2020

SEAL

Not a certified document as to the Original Document but Only as to the Revisions - This document originally issued and sealed by James Deaton, 07438, on 5/25/2018. This document is only certified as to the revisions.

SIG. INVENTORY NO. 04-1432

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