LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE      #1  IS ON
AND RED CLEAR ON PHASE #1  IS ON

            ↓
       SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #50 ON
SET OUTPUT  ASSIGNMENT #51 OFF
```
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE      #1  IS ON

            ↓
       SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #52 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE    #1  IS ON

            ↓
       SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #51 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  ACTIVE PHASE      #5  IS ON
AND RED CLEAR ON PHASE #5  IS ON

            ↓
       SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #42 ON
SET OUTPUT  ASSIGNMENT #43 OFF
```
NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #5  (+/-COMMAND#)
IF  ACTIVE PHASE      #5  IS ON

            ↓
       SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #44 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #6  (+/-COMMAND#)
IF  YELLOW ON PHASE    #5  IS ON

            ↓
       SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #43 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE
USE TO INTERPRET LOGIC PROCESSOR

```
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green
```

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
  PHASE:          :12345678910111213141516
VEH OVL PARENTS::XX
VEH OVL NOT VEH::
VEH OVL NOT PED::
VEH OVL GRN EXT::
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  x GREEN    ← NOTICE GREEN FLASH
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE. 1-16)....0
```

PRESS '+' TWICE

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
  PHASE:          :12345678910111213141516
VEH OVL PARENTS::    XX
VEH OVL NOT VEH::
VEH OVL NOT PED::
VEH OVL GRN EXT::
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  x GREEN    ← NOTICE GREEN FLASH
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE. 1-16)....0
```
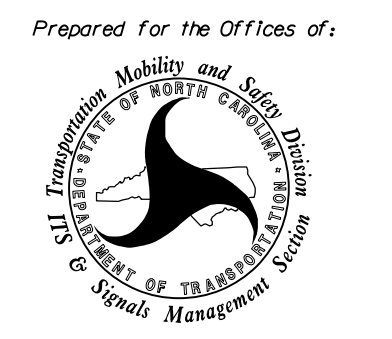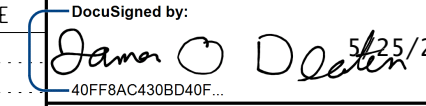
OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-1241
DESIGNED: January 2018
SEALED: 5/25/2018
REVISED: N/A

Final Design
Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 42
at
SR 1705 (Castleberry Road)

Division 4	Johnston County	Clayton
PLAN DATE: January 2018	REVIEWED BY: J O Deaton	
PREPARED BY: M W Yalch	REVIEWED BY:	

SEAL 07438

REVISIONS	INIT.	DATE

SIG. INVENTORY NO. 04-1241