

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **8. LOGIC PROCESSOR**
- From LOGIC PROCESSOR Submenu select **2. LOGIC STATEMENTS**

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 1 COPY FROM: 1 ACTIVE: M
IF DET 52 IS ON

THEN LP SET LOGIC FLAG 1 ON

ELSE

```

IF RR1 PREEMPT (REMAPPED AS DET 52) INPUT IS ACTIVE, SET LOGIC FLAG 1 ON.

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 2 COPY FROM: 2 ACTIVE: M
IF LP FLAG 1 IS ON

THEN PMT CALL PMT SEQ 2 ON

ELSE

```

IF LOGIC FLAG 1 IS ON, THEN INITIATE PREEMPT 2 SEQUENCE. THE PREEMPT MAY OR MAY NOT ACTUALLY BE SERVED DEPENDING ON THE STATE OF THE OTHER RR PREEMPT INPUT.

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 3 COPY FROM: 3 ACTIVE: M
IF DET 54 IS ON

THEN LP SET LOGIC FLAG 2 ON

ELSE

```

IF RR2 PREEMPT (REMAPPED AS DET 54) INPUT IS ACTIVE, SET LOGIC FLAG 2 ON.

ENTER A "4" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 4 COPY FROM: 4 ACTIVE: M
IF LP FLAG 2 IS ON

THEN PMT CALL PMT SEQ 4 ON

ELSE

```

IF LOGIC FLAG 2 IS ON, THEN INITIATE PREEMPT 4 SEQUENCE. THE PREEMPT MAY OR MAY NOT ACTUALLY BE SERVED DEPENDING ON THE STATE OF THE OTHER RR PREEMPT INPUT.

ENTER A "5" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

```

LP#: 5 COPY FROM: 5 ACTIVE: M
IF DET 52 IS OFF
AND DET 54 IS OFF

THEN LP SET LOGIC FLAG 1 OFF
THEN LP SET LOGIC FLAG 2 OFF

ELSE

```

WHEN BOTH PREEMPT INPUTS GO INACTIVE, THIS LOGIC RESETS THE LOGIC FLAG THAT IS HOLDING THE ACTIVE PREEMPT ACTIVE, AND RESETS THE OTHER LOGIC FLAG TO PREVENT IT FROM CALLING THE OTHER PREEMPT.

END PROGRAMMING

- From Main Menu select **1. CONFIGURATION**
- From CONFIGURATION Submenu select **8. LOGIC PROCESSOR**
- From LOGIC PROCESSOR Submenu select **1. LOGIC STATEMENT CONTROL**

ENABLE LOGIC PROCESSOR STATEMENTS 1-5 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

LOGIC STATEMENT CONTROL	
	1 2 3 4 5 6 7 8 9 0 1 2 3 4 5
LP 1-15	E E E E E
LP 16-30
LP 31-45
LP 46-60
LP 61-75
LP 76-90

END PROGRAMMING

ECONOLITE ASC/3-2070 I/O PIN REMAPPING FOR RR1 AND RR2 PREEMPT INPUTS

The ASC/3 Configurator utility program must be used to remap the I/O pins as shown below. Consult the ASC/3 Configurator User Guide for specific instructions on software use.

- Run the Configurator utility. Load a file as the Current DB.
- Choose the C1-in tab to change the I/O mapping as needed. Use the drop down list within the program to select the assigned function for the pins shown below.
- Save the database file and download it to the controller.

C1 PIN #	DEFAULT FUNCTION	ASSIGNED FUNCTION
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PIN 51-PREEMPT 1 CALL →

PIN 52-PREEMPT 2 CALL →

NOTE: PREEMPT INPUTS REMAPPED AS DETECTORS

NOTE: The steps below can be used to view changes to I/O pins within the controller. Any I/O pins that have been remapped will display and show their default function in addition to the current assigned function.

- From Main Menu select **7. STATUS DISPLAY**
- From STATUS DISPLAY Submenu select **8. INPUTS/OUTPUTS**
- From INPUT/OUTPUT Submenu select **9. I/O DIFFERENCES**

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR REMAPPED DETECTORS

(program controller as shown)

The preempt inputs remapped as detectors that are to be used by the logic processor are assigned to a dummy phase 9 as shown in the detector setup programming below.

- From Main Menu select **6. DETECTORS**
- From DETECTOR Submenu select **2. VEHICLE DETECTOR SETUP**

- Place cursor in VEH DETECTOR [] position and enter "52".

DISABLE TS2 DETECTOR	→	VEH DETECTOR [52] VEH DET PLAN [1]
ASSIGN PHASE 9	→	TYPE: S-STANDARD TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 52 9 EXTEND TIME... 0.0 DELAY TIME... 0.0 USE ADDED INITIAL . CROSS SWITCH PH.. 0 LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [] position and enter "54".

DISABLE TS2 DETECTOR	→	VEH DETECTOR [54] VEH DET PLAN [1]
ASSIGN PHASE 9	→	TYPE: S-STANDARD TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 54 9 EXTEND TIME... 0.0 DELAY TIME... 0.0 USE ADDED INITIAL . CROSS SWITCH PH.. 0 LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0054
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

Final Design
Electrical Detail - Sheet 5 of 6

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:

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US 401 Business (Raeford Road)
at
McPherson Church Road/
Owen Drive
Division 6 Cumberland County Fayetteville

PLAN DATE: March 2018 REVIEWED BY: L Overn
PREPARED BY: G B Spell REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA
PROFESSIONAL ENGINEER
LAWRENCE E. OVERN
045933
3/29/2018
DATE
SIG. INVENTORY NO. 06-0054

DATE: U:\Projects\Signal\Signal\4405\Signal\4405_Sig_el.e_06-0054.dgn User: rmluncey

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