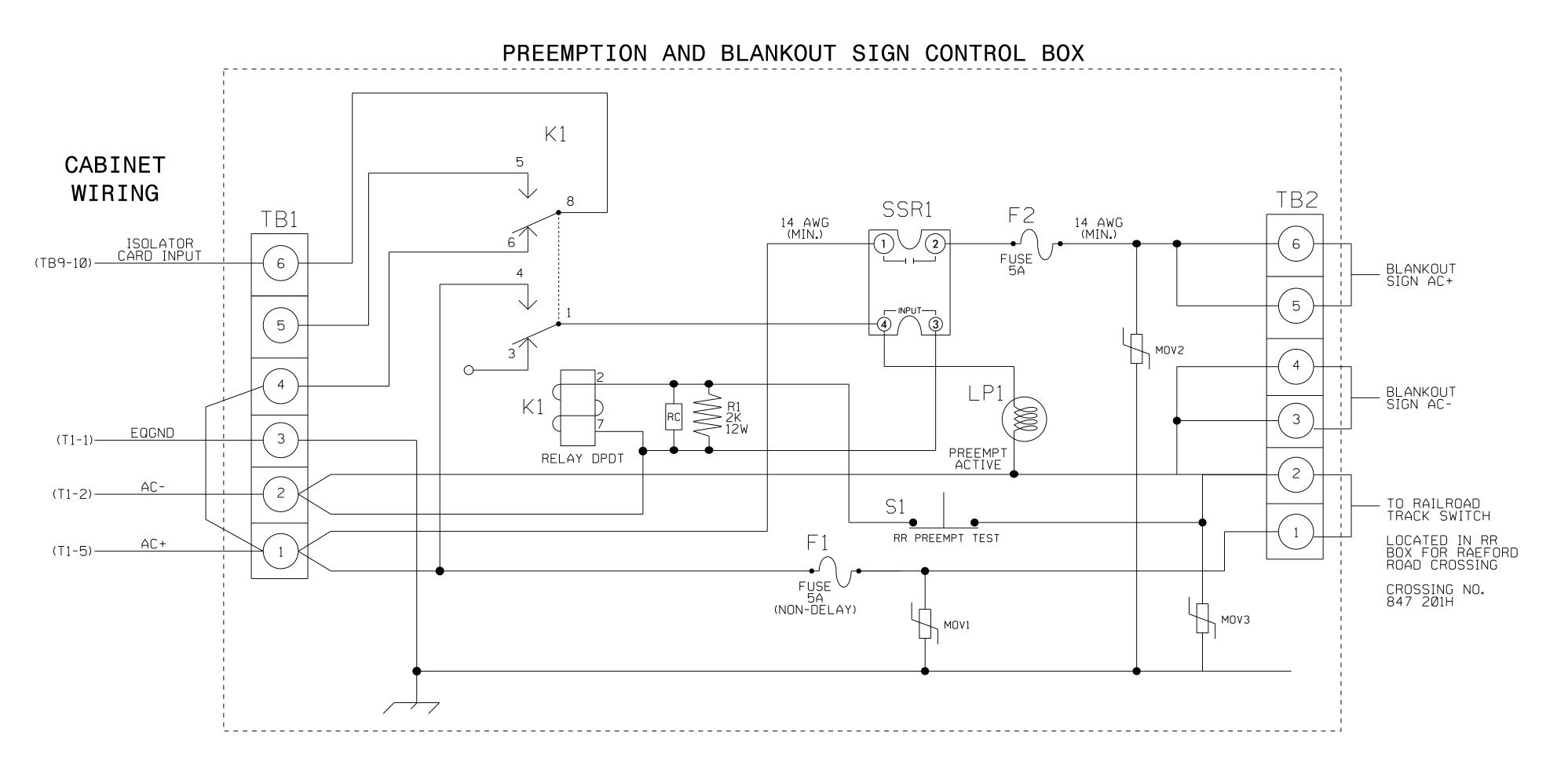
U-4405 SIG-68

## RAILROAD PREEMPTION WIRING DETAIL FOR

RR1 (LINKED RR PREEMPTS 1 & 2)

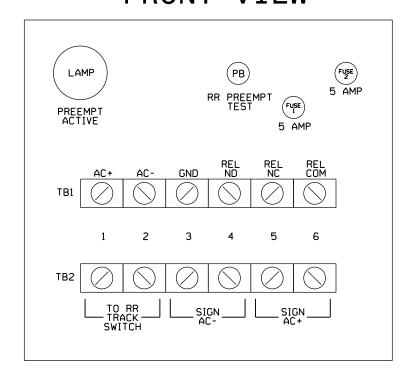
(wire as shown below)



## NOTES

- Relay Klis shown in the energized (Preempt <u>not</u> active) normal operation state.
- 2. Relay Klis a DPDT with 120VAC coilwith octalbase.
- Relay SSRI is a SPST (normally open) Solid State Relay with AC input and AC (25 amp) output.
- 4. AC Isolator Card shall activate preemption upon removal of AC+ from the input (as shown above). To accomplish this set invert dip switch on AC Isolator Card. See AC Isolator Output Programming Detailon Sheet I.
- IMPORTANT!! A jumper must be added between input file terminals JI4-E and JI4-K if not already present. Also, terminal TB9-I2 (on input panel) shall be connected to AC neutral (jumper may have to be added).

## FRONT VIEW



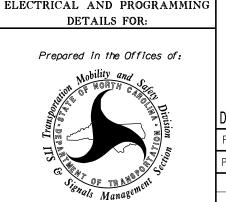
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: Ø6-ØØ54T3
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

Temporary Design 3 - TMP Phase III Electrical Detail - Sheet 2 of 5



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## Prepared In the Offices of: MCPherson Church Road Owen Drive

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Division 6 Cumberland County Fayetteville

PLAN DATE: March 2018 REVIEWED BY: L Overn
PREPARED BY: G B Spell REVIEWED BY:

REVISIONS INIT. DATE

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:rmuncey