ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

(program controller as shown)

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu select | 8. UTILITIES |
- 2. From UTILITIES Submenu select | 1. COPY/CLEAR
- 3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN..... > TIMING PLAN..... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select | 6. DETECTORS |
- 5. From DETECTOR Submenu select 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [] position and enter "2".
- Place cursor in VEH DETECTOR [] position and enter "5". - Set delay time to "0".

VEH DETECTOR [5] VEH DET PLAN [2] TYPE: S-STANDARD TS2 DETECTOR.... ECPI LOG.... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... 0.0 DELAY TIME... 0.0

ENSURE DELAY USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [] position and enter "22". - Set assigned phase to "0".

DET PLAN 2

VEH DETECTOR [22] VEH DET PLAN [2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR..... ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO "O" EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

END PROGRAMMING

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

TOGGLE TWO TIMES OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O

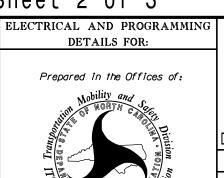
END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0334T1 DESIGNED: March 2018 SEALED: 03-29-2018 REVISED: N/A

Temporary Design 1 - TMP Phase I Electrical Detail - Sheet 2 of 3



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Division 6 Cumberland County Fayetteville PLAN DATE: March 2018 REVIEWED BY: PREPARED BY: R M Muncey REVIEWED BY:

SIG. INVENTORY NO. 06-03341

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L Overn REVISIONS INIT. DATE