PROJECT REFERENCE NO. U-4405 SIG-36.

ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select | 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	1	COPY	FROM:	1	ACTIVE:	М	FALSE	
ΙF	CTR	PHASE	TMING		1	IS	ON	
THEN	SIG	SET O	DLP GRE	ΕN	1	I	OFF	
ELSE								

LOGIC FOR TURNING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEADS 83,84)

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	2	COPY FRO	M: 2	ACTIVE:	М	FALSE
ΙF	VEH	YELLOW O	IN PH	1	ΙS	ON
THEN	SIG	SET OLP	YELLOW	1		ON
ELSE						

LOGIC FOR YELLOW ARROW CLEARANCE WHEN LEAVING PHASE 1 (HEADS 83,84)

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	3	COPY FROM:	3	ACTIVE:	М	FALSE	
		PHASE TIMING RED ON PHASE		1 1	IS IS		
THEN	SIG	SET OLP RED		1		ON	
FISE							

LOGIC FOR RED SIGNAL CLEARANCE WHEN LEAVING PHASE 1 (HEADS 83,84)

ENTER A "4" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	4	COPY	FROM	: 4	ACTIVE:	М	FALSE
ΙF	VEH	OVERL	AP		7	IS	ON
THEN	SIG	SET C)VLP (GREEN	2		OFF

LOGIC FOR TURNING FLASHING YELLOW ARROW OFF DURING OLG, WHICH HAS PARENT PHASE 7 (HEAD 64)

ENTER A "5" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	5	COPY FRO	M: 5	ACTIVE:	М	FALSE
ΙF	VEH	OVERLAP	YLW	7	ΙS	ON
THEN	SIG	SET OLP	YELLOW	2		ON
ELSE						

LOGIC FOR YELLOW ARROW CLEARANCE WHEN LEAVING OLG, WHICH HAS PARENT PHASE 7 (HEAD 64)

ENTER A "6" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	6	COPY FRO	:MC	6	ACTIVE:	М	FALSE
		OVERLAP OVERLAP	RED		•	IS IS	
THEN	SIG	SET OLP	RED		2		ON
ELSE							

LOGIC FOR RED SIGNAL CLEARANCE WHEN LEAVING OLG, WHICH HAS PARENT PHASE 7 (HEAD 64) ENTER A "7" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	7 COPY FROM: 7	ACTIVE:	M FALSE
ΙF	CTR PHASE TMING	5	IS ON
THEN	SIG SET OLP GREEN	3	OFF
ELSE			

LOGIC FOR TURNING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEADS 43,44)

ENTER A "8" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	8 COPY FROM: 8	ACTIVE:	M FALSE
ΙF	VEH YELLOW ON PH	5	IS ON
THEN	SIG SET OLP YELLOW	3	ON
ELSE			

LOGIC FOR YELLOW ARROW CLEARANCE WHEN LEAVING PHASE 5 (HEADS 43,44)

ENTER A "9" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	9	COPY FROM:	9	ACTIVE:	М	FALS
_	_	PHASE TIMING RED ON PHASE		5 5	IS IS	ON ON
THEN	SIG	SET OLP RED		3		ON
ELSE						

LOGIC FOR RED SIGNAL CLEARANCE WHEN LEAVING PHASE 5 (HEADS 43,44)

ENTER A "10" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	10	COPY FROM	: 10	ACTIVE:	М	FALSE
ΙF	VEH	OVERLAP		8	IS	ON
THEN	SIG	SET OVLP	GREEN	4		OFF
ELSE						

LOGIC FOR TURNING FLASHING YELLOW ARROW OFF DURING OLH, WHICH HAS PARENT PHASE 3 (HEAD 23)

ENTER A "11" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	11	COPY FROM: 11	ACTIVE:	М	FALSE
ΙF	VEH	OVERLAP YLW	8	ΙS	ON
THEN	SIG	SET OLP YELLOW	4		ON
ELSE					

LOGIC FOR YELLOW ARROW CLEARANCE WHEN LEAVING OLH, WHICH HAS PARENT PHASE 3 (HEAD 23)

ENTER A "12" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#:	12	COPY FRO	:MC	12	ACTIVE:	М	FALSE
_		OVERLAP OVERLAP	RED		8 8		ON ON
THEN	SIG	SET OLP	RED		4		ON
ELSE							

SIGNAL CLEARANCE WHEN LEAVING OLH, WHICH HAS PARENT PHASE 3 (HEAD 23)

LOGIC FOR RED

4. From LOGIC PROCESSOR Submenu select | 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-12 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

LOGIC STA	СО	CONTROL														
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
LP 1-15	Ε	Ε	Ε	Ε	Ε	Ε	Ε	Ε	Ε	Ε	Ε	Ε	•	•	•	
LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 46-60	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

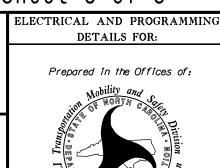
END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0155T1 DESIGNED: March 2018 SEALED: 03-29-2018 REVISED: N/A

Temporary Design 1 - TMP Phase I Electrical Detail - Sheet 3 of 3



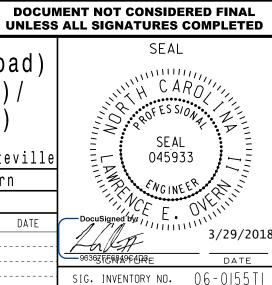
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750 N.Greenfield Pkwy,Garner,NC 27529

US 401 Business (Raeford Road) at NC 59 (Hope Mills Road)/ SR 1592 (Glensford Drive)

Division 6 Cumberland County Fayetteville March 2018 REVIEWED BY: PLAN DATE: L Overn PREPARED BY: G B Spell REVIEWED BY: REVISIONS INIT. DATE



END PROGRAMMING

ELSE