

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select 2. CONTROLLER
- From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

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TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT.....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
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Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

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TMG VEH OVLP...[C] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6



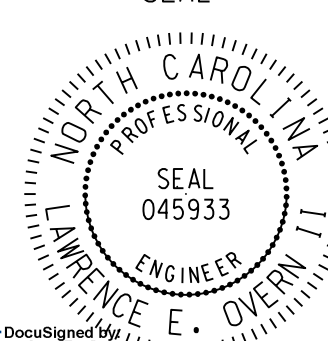
FLASHING ARROW OUTPUT.....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0
    
```

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 06-0491T1
 DESIGNED: March 2018
 SEALED: 03-29-2018
 REVISED: N/A

Temporary Design 1 - TMP Phase I
 Electrical Detail - Sheet 2 of 2

**DOCUMENT NOT CONSIDERED FINAL
 UNLESS ALL SIGNATURES COMPLETED**

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	PLAN DATE: March 2018 PREPARED BY: R M Muncey	REVIEWED BY: L Overn REVIEWED BY:	REVISIONS INIT. DATE