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PROJECT REFERENCE NO. SHEET NO. SIG-6.2

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS
- 3. Toggle Once

OVERLAP B

Select TMG VEH OVLP [B] and 'OTHER/ECONOLITE'

Toggle Twice

OVERLAP D

Select TMG VEH OVLP [D] and 'PPLT FYA'

TMG VEH OVLP...[D] TYPE: PPLT FYA

PROTECTED LEFT TURN.... PHASE 7

OPPOSING THROUGH..... PHASE 8

FLASHING ARROW OUTPUT....CH12 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE......

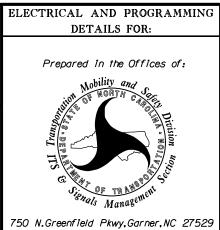
END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø6-Ø767 DESIGNED: March 2018 SEALED: 03-29-2018 REVISED: N/A

Final Design Electrical Detail - Sheet 2 of 2

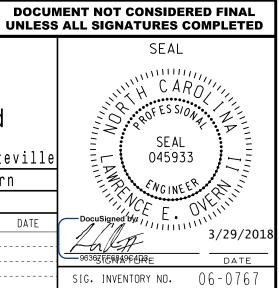


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US 401 (Raeford Rd.) at Chilton Dr / Rayconda Rd

Division 6 Cumberland County Fayetteville
PLAN DATE: March 2018 REVIEWED BY: L Overn
PREPARED BY: R M Muncey REVIEWED BY:
REVISIONS INIT. DATE



part. U:*Traffic*Signals*Design*Electrical Details*Final Signals*U-4405_sig_ele_06-(