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PROJECT REFERENCE NO. U-4405

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH9 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... O Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH11 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... O

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0845T1 DESIGNED: March 2018 SEALED: 03-29-2018 REVISED: N/A

Tempoary Design 1 - TMP Phase II Electrical Detail - Sheet 2 of 2

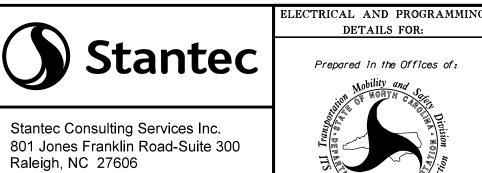
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US 401 (South Raeford Road) SR 3569 (Raeford Rd-

Old US 401)/Bentridge Ln. March 2018 REVIEWED BY: L Overn

Division 6 Cumberland County Fayetteville PREPARED BY: R M Muncey REVIEWED BY: REVISIONS INIT. DATE

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