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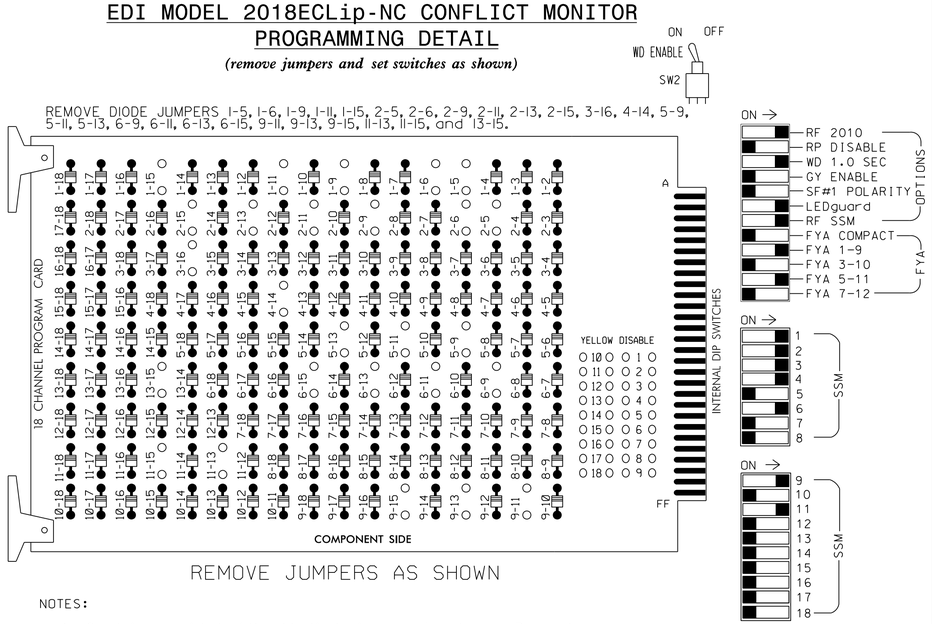
EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program controller for start up in Phase 2 WALK and Phase 6 WALK.
3. The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 W/AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8, S9,S12,AUX S1,AUX S4
PHASES USED.....1,2,2PED,3,4,4PED,5,6, 6PED,3PED
OVERLAP A.....*
OVERLAP B.....NOT USED
OVERLAP C.....*
OVERLAP D.....NOT USED

* See Overlap Programming Detail on Sheet 2

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4		S5		S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3		4		14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	4	2	2 PED	3	3	4	4	4 PED	5	6	6 PED	7	8	3 PED	OLA	OLB	OLE	OLC	OLD	OLF
SIGNAL HEAD NO.	11★	42	21,22	P21, P22	31	32	41	42	P41, P42	51★	61,62, 63	P61, P62	NU	NU	P31, P32	11★	NU	NU	51★	NU	NU
RED	*		128		116	116	101	101			134										
YELLOW			129		117	117	102	102		*	135										
GREEN			130		118	118	103	103			136										
RED ARROW																A121			A114		
YELLOW ARROW		126														A122			A115		
FLASHING YELLOW ARROW																A123			A116		
GREEN ARROW	127	127			118		103			133											
✋				113					104			119			110						
🚶				115					106			121			112						

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail below.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø1 1B	Ø2 2B	WIRED INPUT ⊗	Ø3 3A	Ø3 3B	Ø4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	Ø6 PED DC ISOLATOR	FS DC ISOLATOR
	L	NOT USED	Ø2 2A	Ø2 2C		NOT USED	NOT USED	Ø4 4B					Ø4 PED DC ISOLATOR	Ø3 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6 6A	Ø6 6C	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	Ø6 6B	Ø6 6D											

EX.: 1A, 2A, ETC. = LOOP NO.'S
⊗ Wired Input - Do not populate slot with detector card
FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A[1]	TB2-1,2	I1U	56	1★	1	YES		15		S
	-	J4U	48	26★	6	YES				S
1B	TB2-5,6	I2U	39	2	1	YES		15		S
2A	TB2-7,8	I2L	43	12	2	YES				S
2B	TB2-9,10	I3U	63	32	2	YES				S
2C	TB2-11,12	I3L	76	42	2	YES				S
3A	TB4-5,6	I5U	58	3	3	YES		3		S
3B	TB4-9,10	I6U	41	4	3	YES		10		S
4A	TB6-1,2	I7U	65	34	4	YES		3		S
4B	TB6-3,4	I7L	78	44	4	YES				S
5A[2]	TB3-1,2	J1U	55	5★	5	YES		15		S
	-	I4U	47	22★	2	YES				S
6A	TB3-5,6	J2U	40	6	6	YES				S
6B	TB3-7,8	J2L	44	16	6	YES				S
6C	TB3-9,10	J3U	64	36	6	YES				S
6D	TB3-11,12	J3L	77	46	6	YES				S
PED PUSH BUTTONS										
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED					
P31,P32	TB8-8,9	I13L	70	PED 8	3 PED					
P41,P42	TB8-5,6	I12L	69	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED					

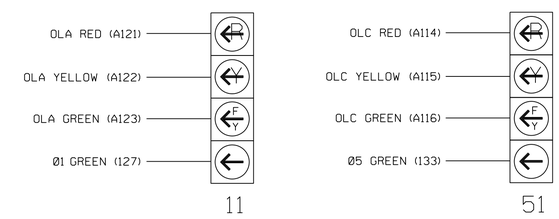
NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

[1] Add jumper from I1-W to J4-W. on rear of input file.
[2] Add jumper from J1-W to I4-W. on rear of input file.
★ See Vehicle Detector Setup Programming Detail for alternate phasing on Sheet 3.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

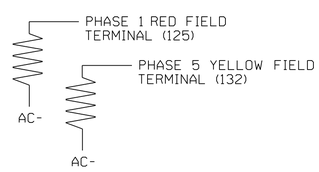
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0224
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

Final Design
Electrical Detail - Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Stantec
Stantec Consulting Services Inc.
801 Jones Franklin Road-Suite 300
Raleigh, NC 27606
Tel. (919) 851-6866
Fax. (919) 851-7024
www.stantec.com
License No. F-0672

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA
DEPARTMENT OF TRANSPORTATION
ITS & Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

US 401 Business (Raeford Road)
at
Purdue Drive/
SteinMart Driveway

Division 6 Cumberland County Fayetteville

PLAN DATE: March 2018	REVIEWED BY: L Overn
PREPARED BY: G B Spell	REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 045933 ENGINEER LAWRENCE E. OVERN
DocuSigned by:
3/29/2018
DESIGN ENGINEER DATE
SIG. INVENTORY NO. 06-0224

DATE:
USER:
Workspace:C:\Users\dsigns\AppData\Local\Temp\wp0a0d0a.tmp
Workspace:C:\Users\dsigns\Design\ME\Electrical Details\Final Signals\U-4405_sig_ele_06-0224.dgn