SIG-75.

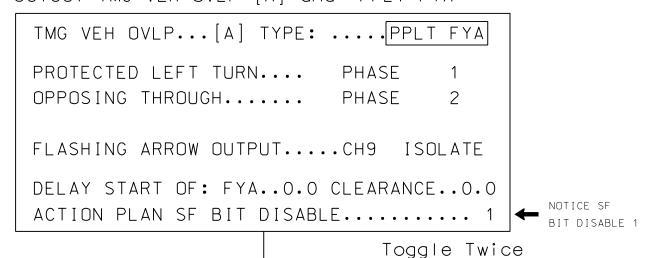
ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

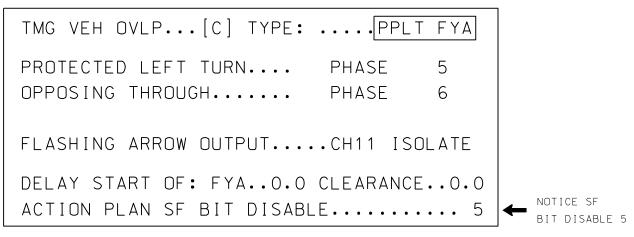
OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'



OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

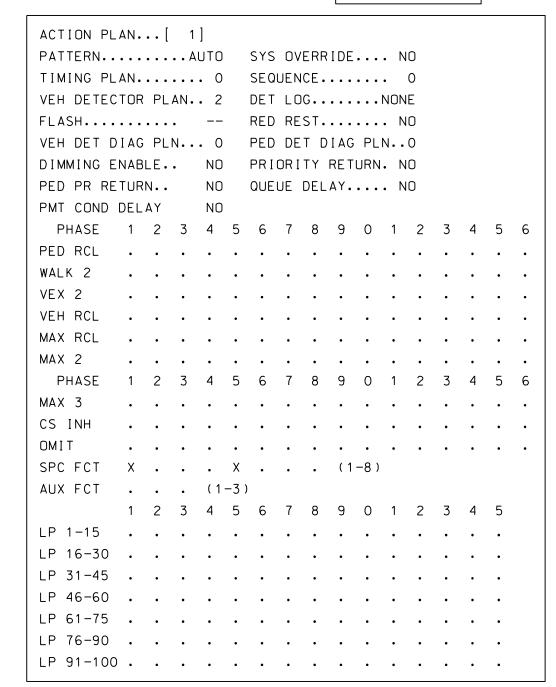


END PROGRAMMING

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

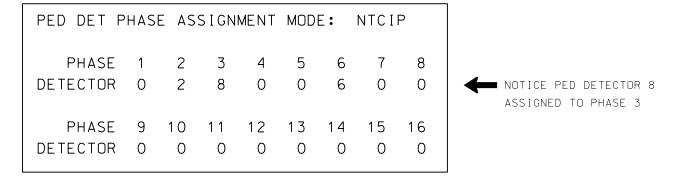
2. From TIME BASE Submenu select | 2. ACTION PLAN |



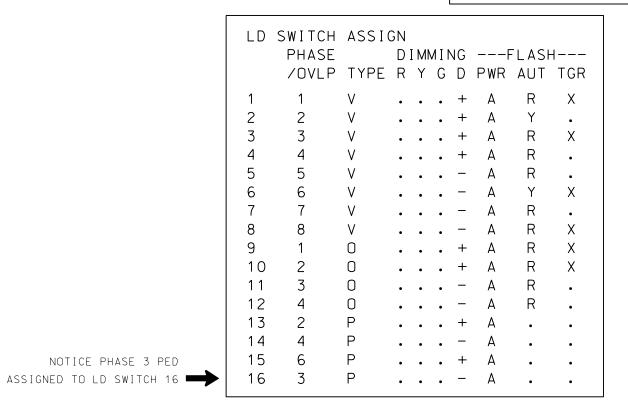
ECONOLITE ASC/3-2070 PED 3 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select | 3. PED DETECTOR INPUT ASSIGNMENT



- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1, 5

IMPORTANT: IF ALT, PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

Modifies overlap parent phases for heads 11 and 51 to

run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.

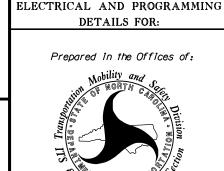
> Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

> > THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0224T2 DESIGNED: March 2018 SEALED: 03-29-2018 REVISED: N/A

Temporary Design 2 - TMP Phase II Electrical Detail - Sheet 2 of 3

Stantec

Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



ELECTRICAL AND PROGRAMMING US 401 Business (Raeford Road) Purdue Drive/

SteinMart Driveway

Division 6 Cumberland County Fayetteville PLAN DATE: March 2018 REVIEWED BY: L Overn PREPARED BY: G B Spell REVIEWED BY: REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED