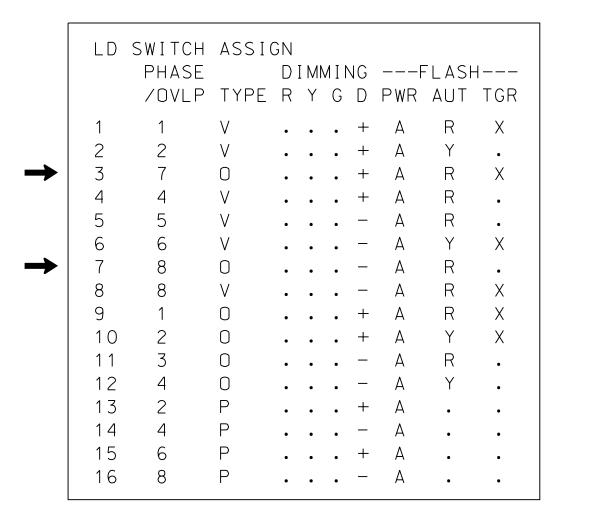
ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S4 and S10 as OLG and OLH, program LD SWITCH 3 as OVLP '7' TYPE '0' and LD SWITCH 7 as OVLP '8' TYPE '0' as shown below.

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



FLASHER CIRCUIT MODIFICATION DETAIL

In order to ensure that signals flash concurrently on the Same approach, make the following flasher circuit changes:

- 1. On rear of PDA remove wire from Term. T2-4 and terminate on T2-2.
- 2. On rear of PDA remove wire from Term. T2-5 and terminate on T2-3.
- 3. Remove flasher unit 2.

The changes listed above ties all phases and overlaps to flasher unit 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0155T1 DESIGNED: March 2018 SEALED: 03-29-2018 REVISED: N/A

Temporary Design 1 - TMP Phase I Electrical Detail - Sheet 2 of 3

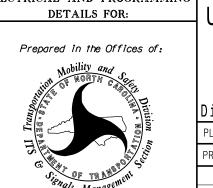
Stantec

Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300

Raleigh, NC 27606

Tel. (919) 851-6866

Fax. (919) 851-7024 www.stantec.com License No. F-0672

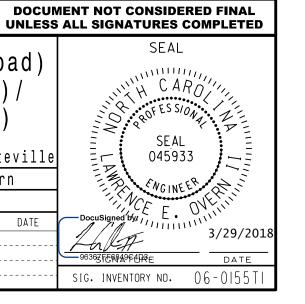


ELECTRICAL AND PROGRAMMING US 401 Business (Raeford Road) at NC 59 (Hope Mills Road)/

REVISIONS

SR 1592 (Glensford Drive) Division 6 Cumberland County Fayetteville March 2018 REVIEWED BY: L Overn PREPARED BY: R M Muncey REVIEWED BY:

INIT. DATE



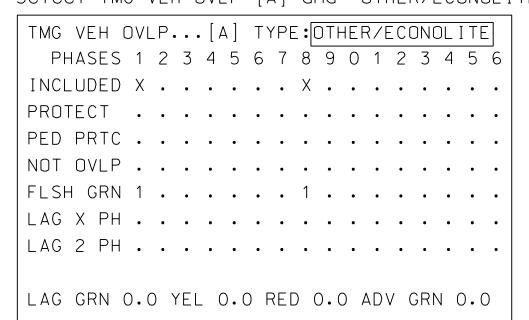
ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL (program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

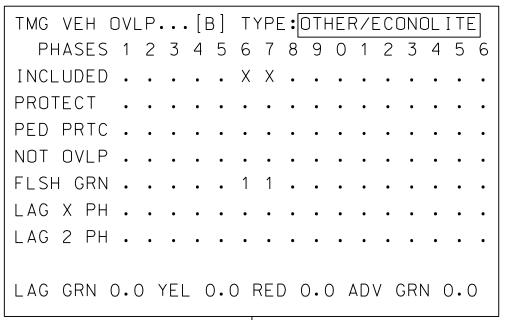
OVERLAP A

Select TMG VEH OVLP [A] and 'OTHER/ECONOLITE'



Toggle Once OVERLAP B

Select TMG VEH OVLP [B] and 'OTHER/ECONOLITE'



Toggle Once

Select TMG VEH OVLP [C] and 'OTHER/ECONOLITE'

OVERLAP C

TMG VEH OVLP[C]								T١	TYPE: OTHER/ECONOL I TE										
PH	SAF	ES	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	_ 6	
INCL	.UC)ED	•	•	•	Χ	Χ	•	•	•	•	•		•	•	•	•	•	
PR01	EC	T	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
PED	PF	RTC	•	•	•	•	•	•	•	•		•		•	•	•	•	•	
TOV	0	LP	•	•	•	•	•	•				•	•	•	•	•	•	•	
FLSH	H (RN	•	•	•	1	1	•	•	•		•	•	•	•	•	•	•	
_AG	Χ	РН	•	•	•	•	•	•	•	•		•		•	•	•	•	•	
_AG	2	РΗ		•	•		•	•	•	•	•	•	•	•	•		•	•	
_AG	GF	N	0.0	Э,	ΥΕL	_ ().() F	RED) ().() /	4D'	/ (GRI	/ ().()	
_AG	GF	<u> </u>	0.0		ΥEι	_ () . (C F	RED) ().() <i>A</i>		V (Γος					

OVERLAP D

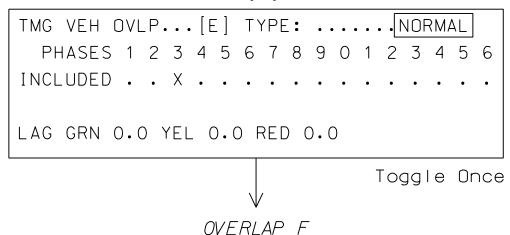
Salaat TMC VEH OVER [D] and 'OTHER/ECONOLITE'

Select ———	I MG	VEH	H ()VL	.P)]	ar	nd	' ()Tł	HEF	₹/[<u> </u>	אכ)L :
TMG VI	EH O\	VLP.		[[]	ΤY	'ΡΕ	:[)TF	HEF	R / E	ECC	ONC	DL :	ΙTΕ	=
PHA	SES ′	1 2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
INCLU	DED .	. X	Χ	•	•	•	•	•	•	•	•	•	•	•	•	•
PROTE	CT .		•	•	•	•	•	•	•	•	•	•	•	•	•	•
PED PI	RTC .		•	•	•	•		•	•	•	•	•	•	•	•	•
NOT O	VLP .		•	•	•	•	•	•	•	•	•	•	•	•	•	•
FLSH	GRN .	. 1	1	•	•	•		•	•	•	•	•	•	•	•	•
LAG X	PH .		•	•	•		•	•	•	•		•	•		•	
LAG 2	PH .		•		•	•	•	•	•	•		•	•	•	•	
LAG G	RN 0.	. 0 `	YEL	. C	. () F	REC) ().() /	ΔD/	/ (GRI	1 ().()

Toggle Once

OVERLAP E

Select TMG VEH OVLP [E] and 'NORMAL'

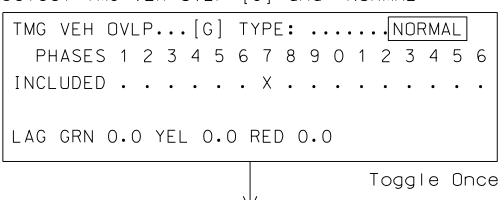


Select TMG VFH OVIP [F] and 'NORMAL'

SOLOGI IMO VEIL OVEI [1] GIIG HOKMAE													
TMG VEH OVLP[F] TYPE:NORMAL													
PHASES 1 2 3 4	5 6 7	8 9	0 1	2	3	4	5	6					
INCLUDED	X			•	•	•	•						
LAG GRN 0.0 YEL 0.0 RED 0.0													
	Τος	gg	lе	Or	nce								
\bigvee													

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'



OVERLAP H

Select TMG VEH OVLP [H] and 'NORMAL'

- The very even first and workman																
TMG VEH	TYPE:NORMAL															
PHASE	ES 1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
INCLUDE	ED .	•	Χ	•	•	•	•	•	•	•	•	•	•	•	•	•
LAG GRN 0.0 YEL 0.0 RED 0.0																

END PROGRAMMING