

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER
2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle Until Positioned
on Overlap G

OVERLAP G

Select TMG VEH OVLP [G] and 'NORMAL'

```

TMG VEH OVLP...[G] TYPE: .....NORMAL
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED . . . . . X . . . . .
LAG GRN 0.0 YEL 0.0 RED 0.0

```

↓

Toggle Until Positioned
on Overlap A

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```

TMG VEH OVLP...[A] TYPE: .....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... OVERLAP G

FLASHING ARROW OUTPUT.....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 0

```

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE
SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0274T3
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

Temporary Design 3 - TMP Phase III
Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED

 Stantec <small>Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672</small>	<small>ELECTRICAL AND PROGRAMMING DETAILS FOR:</small> <small>Prepared in the Offices of: North Carolina Department of Transportation 750 N. Greenfield Pkwy, Garner, NC 27529</small>	US 401 (Raeford Road) at NC 162 (Bunce Road) / SR 1411 (Bunce Road) <small>Division 6 Cumberland County Fayetteville</small>	<small>SEAL</small> <small>PROFESSIONAL ENGINEER LAWRENCE E. OVERN 045933</small>								
	<small>PLAN DATE: March 2018</small> <small>REVIEWED BY: L Overn</small> <small>PREPARED BY: G B Spell</small> <small>REVIEWED BY:</small>	<table border="1" style="width: 100%; border-collapse: collapse; font-size: x-small;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table>	REVISIONS	INIT.	DATE						
REVISIONS	INIT.	DATE									

DATE: 03/29/2018 10:45:12 AM
 User: rfmancey
 C:\Users\rfmancey\Documents\Electrical\Detail\Signal\Phase 3\U-4405\SigEle_06-0274T3.dgn