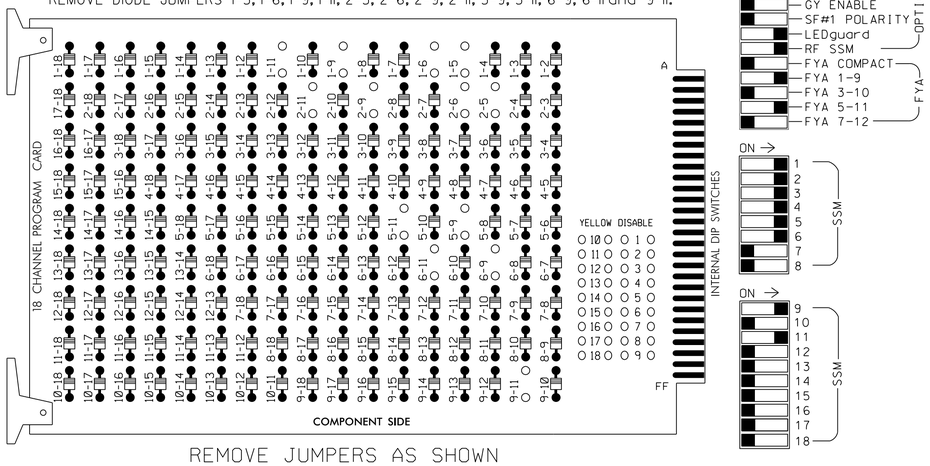
EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS I-5, I-6, I-9, I-11, 2-5, 2-6, 2-9, 2-11, 5-9, 5-11, 6-9, 6-11 and 9-11.



■ = DENOTES POSITION OF SWITCH

REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø 1 1A	Ø 1 1B	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	Ø 4 4A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS DC ISOLATOR
	L	NOT USED	NOT USED				NOT USED								ST DC ISOLATOR
FILE "J"	U	Ø 5 5A	SLOT EMPTY	SLOT EMPTY	WIRED INPUT ⊗	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED													

EX.: 1A, 2A, ETC. = LOOP NO.'S

⊗ Wired Input - Do not populate slot with detector card

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A[1]	-	I1U	56	1	1	YES		15		S
	-	J4U	48	26	6	YES		3		G
1B	TB2-5,6	I2U	39	2	2	YES				S
4A	TB4-9,10	I6U	41	4	4	YES				S
5A[2]	-	J1U	55	5	5	YES		15		S
	-	I4U	47	22	2	YES		3		G

[1] Add jumper from I1-W to J4-W, on rear of input file.
[2] Add jumper from J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program controller to start up in phase 2 Green and 6 Green.
3. The cabinet and controller are part of the Fayetteville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 W/AUX
SOFTWARE.....ECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8 AUX S1,AUX S4
PHASES USED.....1,2,3,4,5,6
OVERLAP "A".....*
OVERLAP "B".....NOT USED
OVERLAP "C".....*
OVERLAP "D".....NOT USED

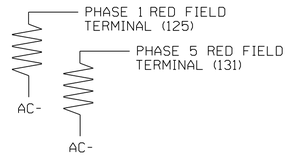
* See overlap programming detail on sheet 2

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



DETECTOR NOTES

1. For all loops, install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
2. For loops 1A and 5A detector card placements and slots reserved for wired inputs are typical for a NCDOT installation.

PROJECT REFERENCE NO.	SHEET NO.
U-4405	SIG-2.1

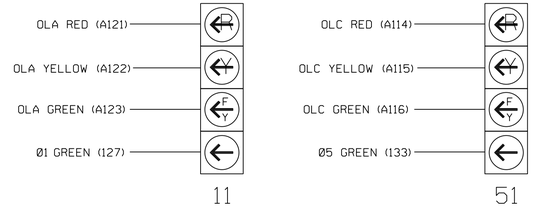
SIGNAL HEAD HOOK-UP CHART

	S1		S2	S3	S4			S5			S6	S7		S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3			4			14	5		6	15	7	8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	3			4			4 PED	5		6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	42	21,22	NU	31	32	33	41	42	22	NU	51★	33	61,62	NU	NU	NU	NU	11★	NU	NU	51★	NU	NU
RED	✳		128			116	116	101	101			✳		134										
YELLOW			129			117	117	102	102					135										
GREEN			130			118	118	103	103					136										
RED ARROW					116														A121			A114		
YELLOW ARROW		126			117					102			132						A122			A115		
FLASHING YELLOW ARROW																			A123			A116		
GREEN ARROW	127	127			118	118		103		103		133	133											

NU = Not Used
✳ Denotes install load resistor. See load resistor installation detail this sheet.
★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0845T1
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

Temporary Design 1 - TMP Phase II
Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:
Traffic Engineering Mobility and Safety Division
DEPARTMENT OF TRANSPORTATION
Traffic & Signals Management Section
750 N.Greenfield Pkwy.Garner,NC 27529

US 401 (South Raeford Road)
at
SR 3569 (Raeford Rd-Old US 401))/Bentridge Ln.

Division 6 | Cumberland County | Fayetteville

PLAN DATE:	March 2018	REVIEWED BY:	L Overn
PREPARED BY:	R M Muncey	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 045933 ENGINEER LAWRENCE E. OVERN II
DocuSigned by: 3/29/2018
SIGNATURE DATE

SIG. INVENTORY NO. 06-0845T1

DATE:
User:rmuncey
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