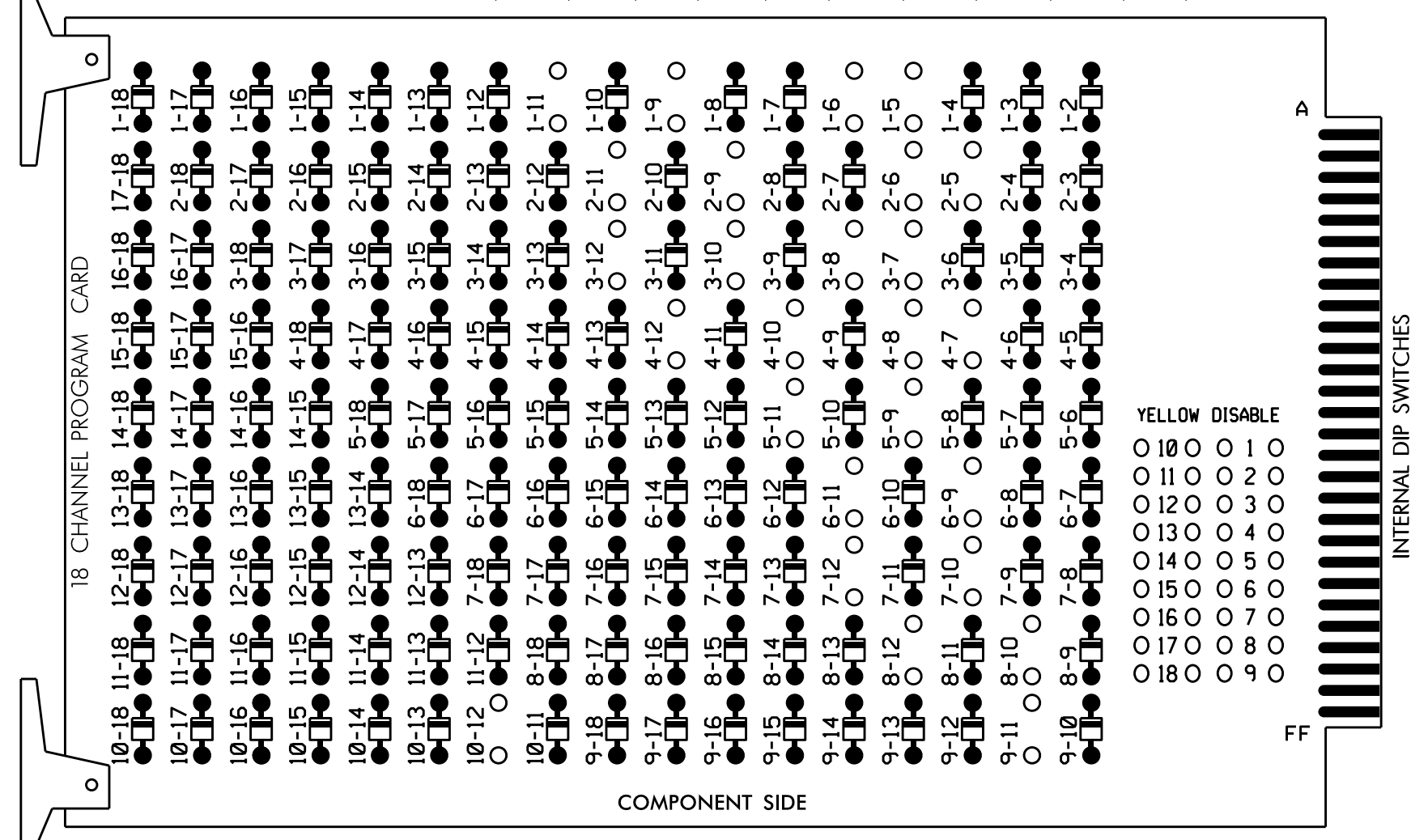


**EDI MODEL 2018ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

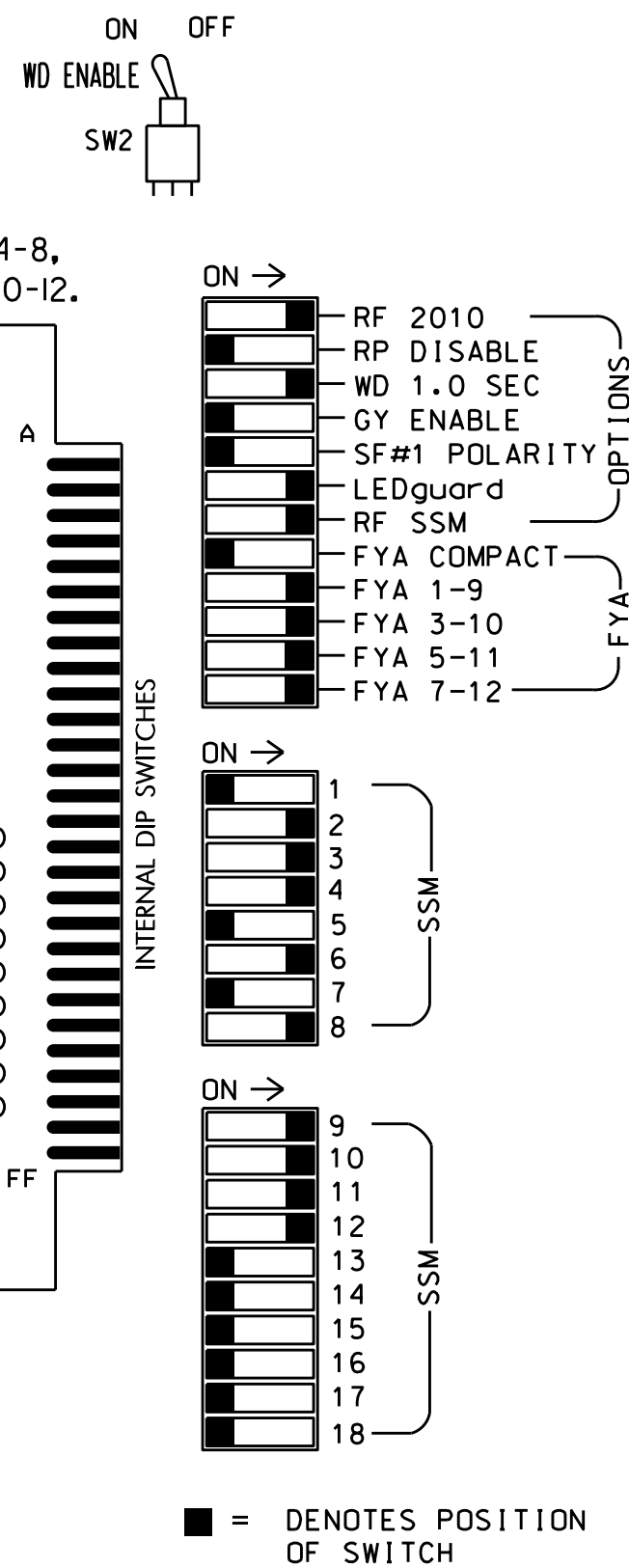
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11, and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Startup In Green.
6. Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,AUX S1,
 AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

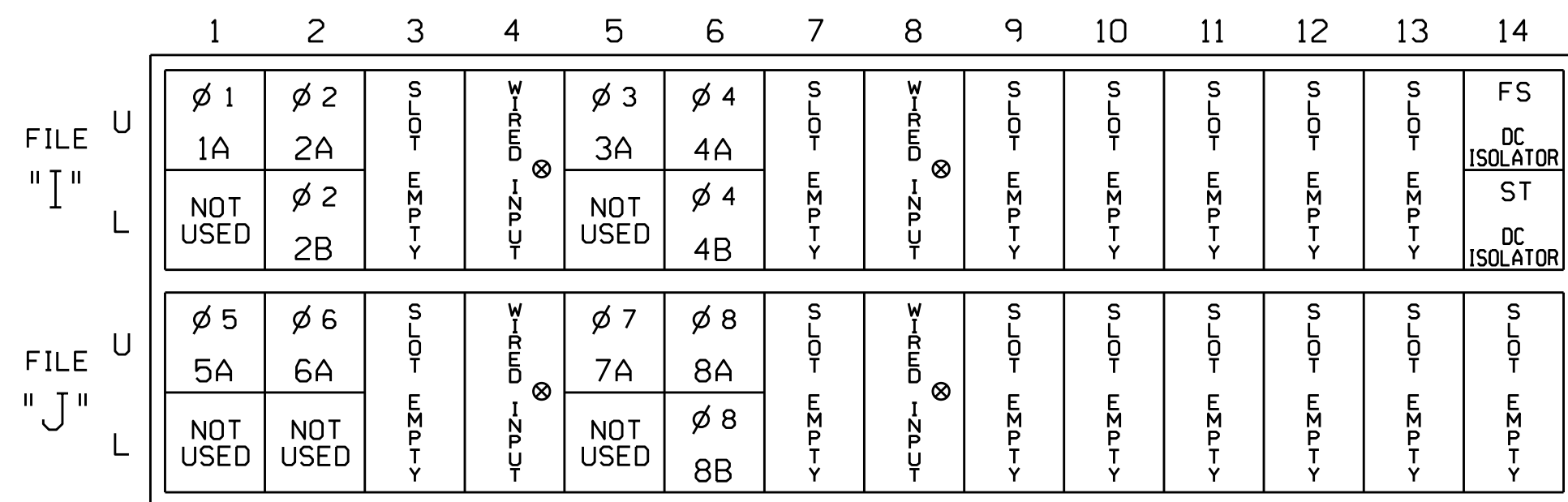
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	NU	22	31	41,42 43	NU	51	61,62	NU	71	81,82 83	NU	11	31	NU	51	71	NU
RED		128		*	101			134			107								
YELLOW	*	129			102		*	135		*	108								
GREEN		130			103			136			109								
RED ARROW													A121	A124		A114	A101		
YELLOW ARROW				117									A122	A125		A115	A102		
FLASHING YELLOW ARROW													A123	A126		A116	A103		
GREEN ARROW	127			118	118			133			124								

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

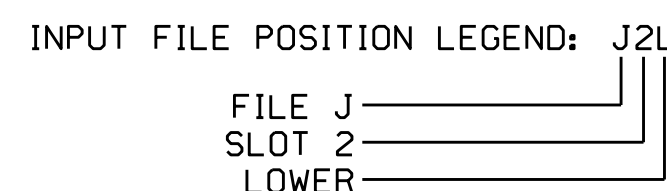


EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A ²	TB4-5,6	I5U	58	20	3	3	Y	Y			15
	-	J8U	50	12	28	8	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y		2.5	
	TB4-11,12	I6L	45	7	14	4	Y	Y			
5A ³	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
	TB5-5,6	J5U	57	19	7	7	Y	Y			15
7A ⁴	-	I8U	49	11	24	4	Y	Y			
	TB5-9,10	J6U	42	4	8	8	Y	Y		2.5	
8A	TB5-11,12	J6L	46	8	18	8	Y	Y			

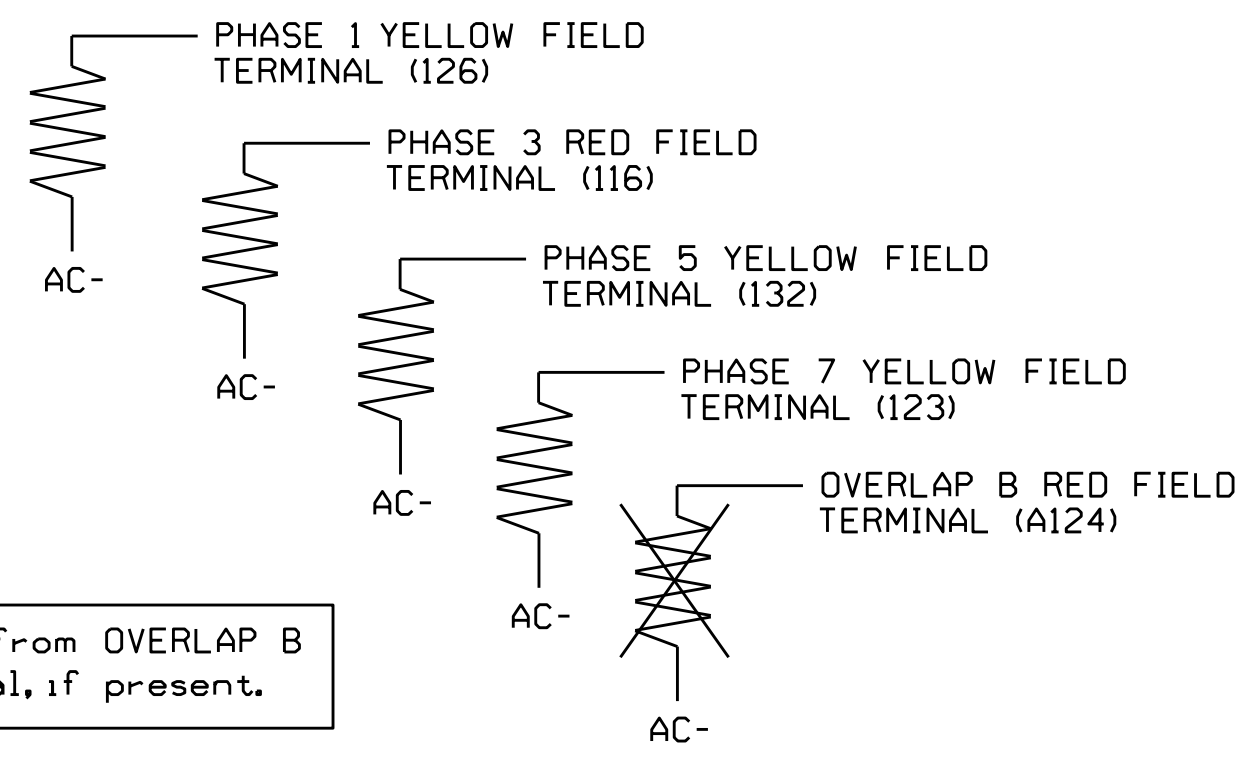
- 1 Add jumper from I1-W to J4-W, on rear of input file.
- 2 Add jumper from I5-W to J8-W, on rear of input file.
- 3 Add jumper from J1-W to I4-W, on rear of input file.
- 4 Add jumper from J5-W to I8-W, on rear of input file.



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

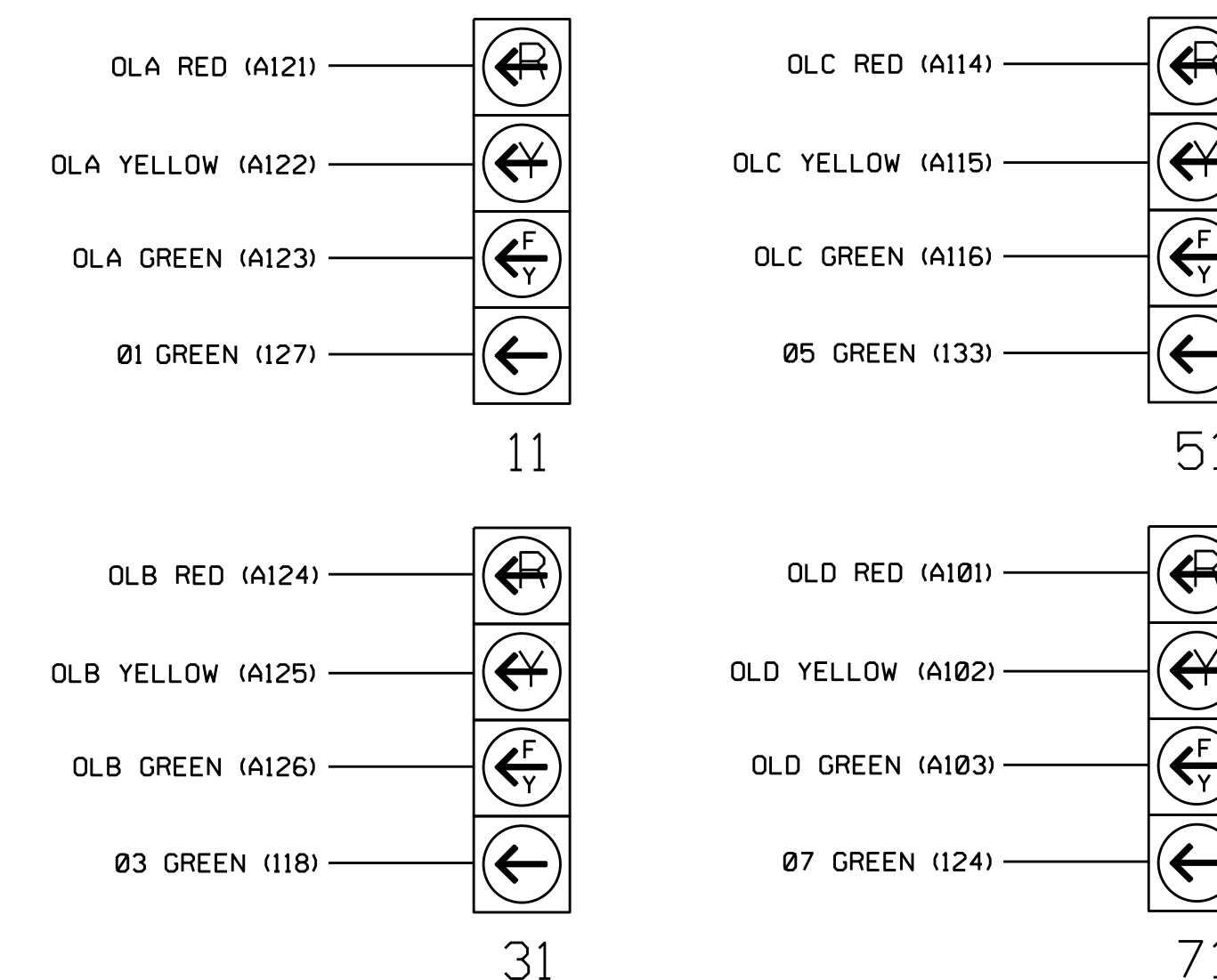
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



IMPORTANT! Remove resistor from OVERLAP B Red field terminal, if present.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

The sequence display for signal heads 11, 31, 51, and 71 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1555
 DESIGNED: March 2018
 SEALED: 4/5/2018
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

Prepared In the Offices of:
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Division 7 Alamance County Mebane

PLAN DATE: April 2018 REVIEWED BY:
 PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: Keith M. Mims 4/9/2018

SIG. INVENTORY NO. 07-1555

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED