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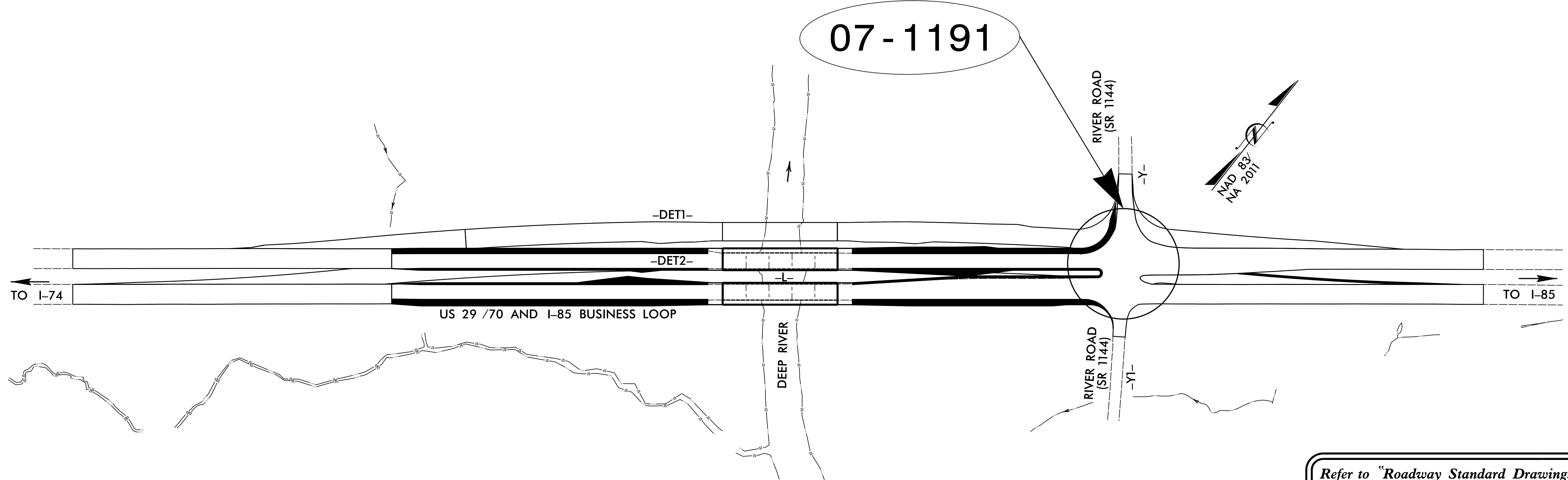
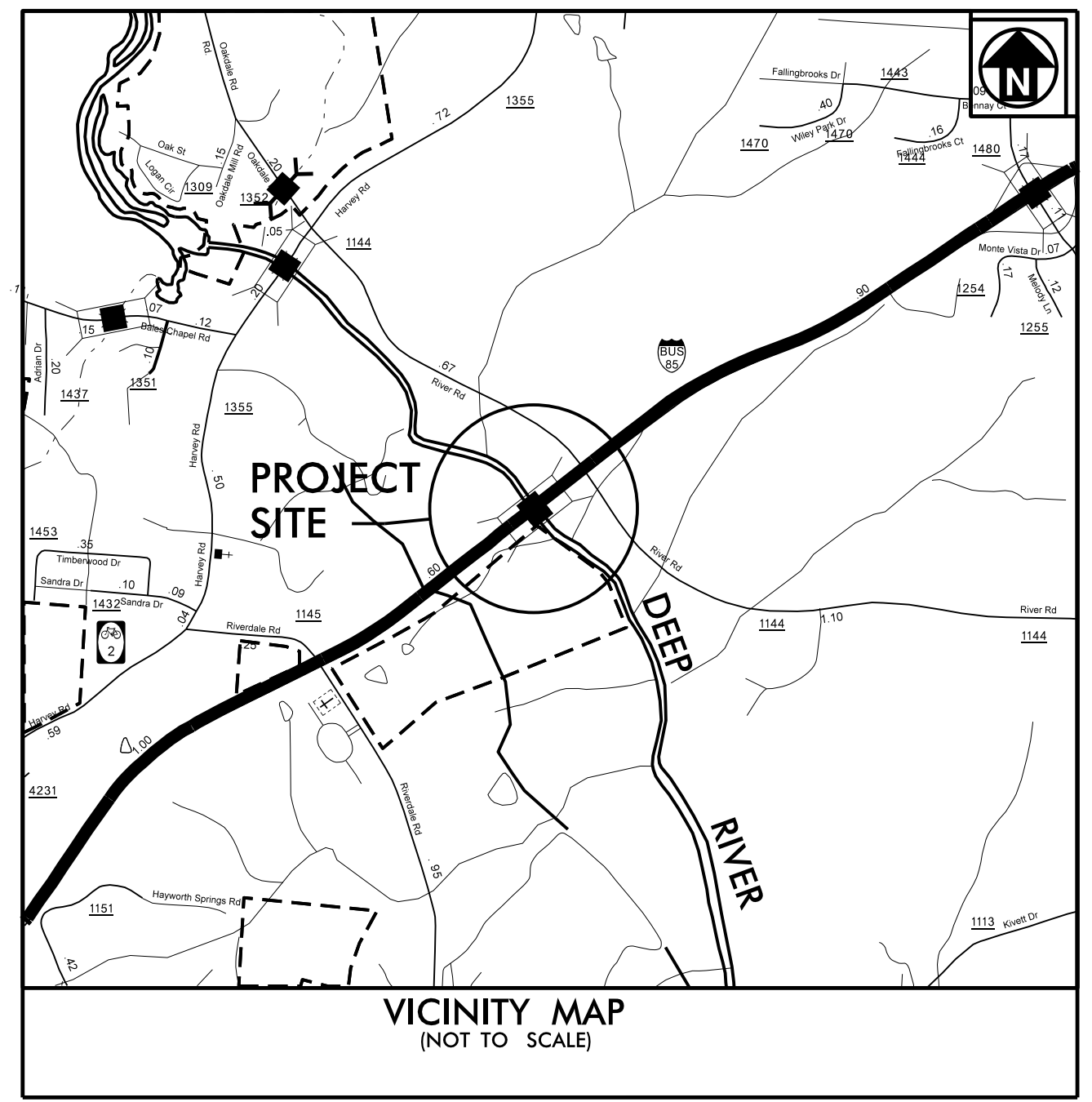
STATE OF NORTH CAROLINA  
DIVISION OF HIGHWAYS

# GUILFORD COUNTY

**LOCATION: REPLACE BRIDGE NO. 242 AND BRIDGE NO. 237 OVER DEEP RIVER  
IN HIGH POINT ON I-85 BUSINESS/US 29-70**

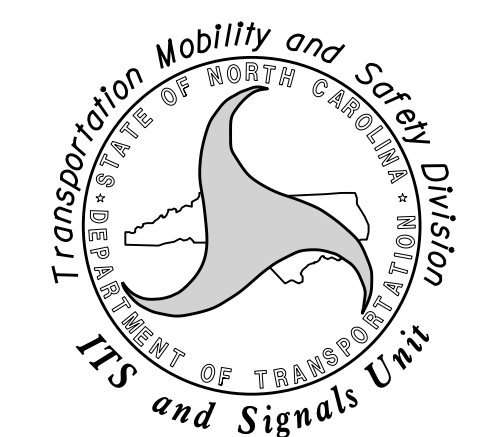
**TYPE OF WORK: SIGNALS**

**TIP PROJECT: B-5351**



Refer to "Roadway Standard Drawings  
NCDOT" dated January 2018 and  
"Standard Specifications for Roads  
and Structures" dated January 2018.

Prepared in the Office of:  
DIVISION OF HIGHWAYS  
TRANSPORTATION MOBILITY AND SAFETY  
DIVISION



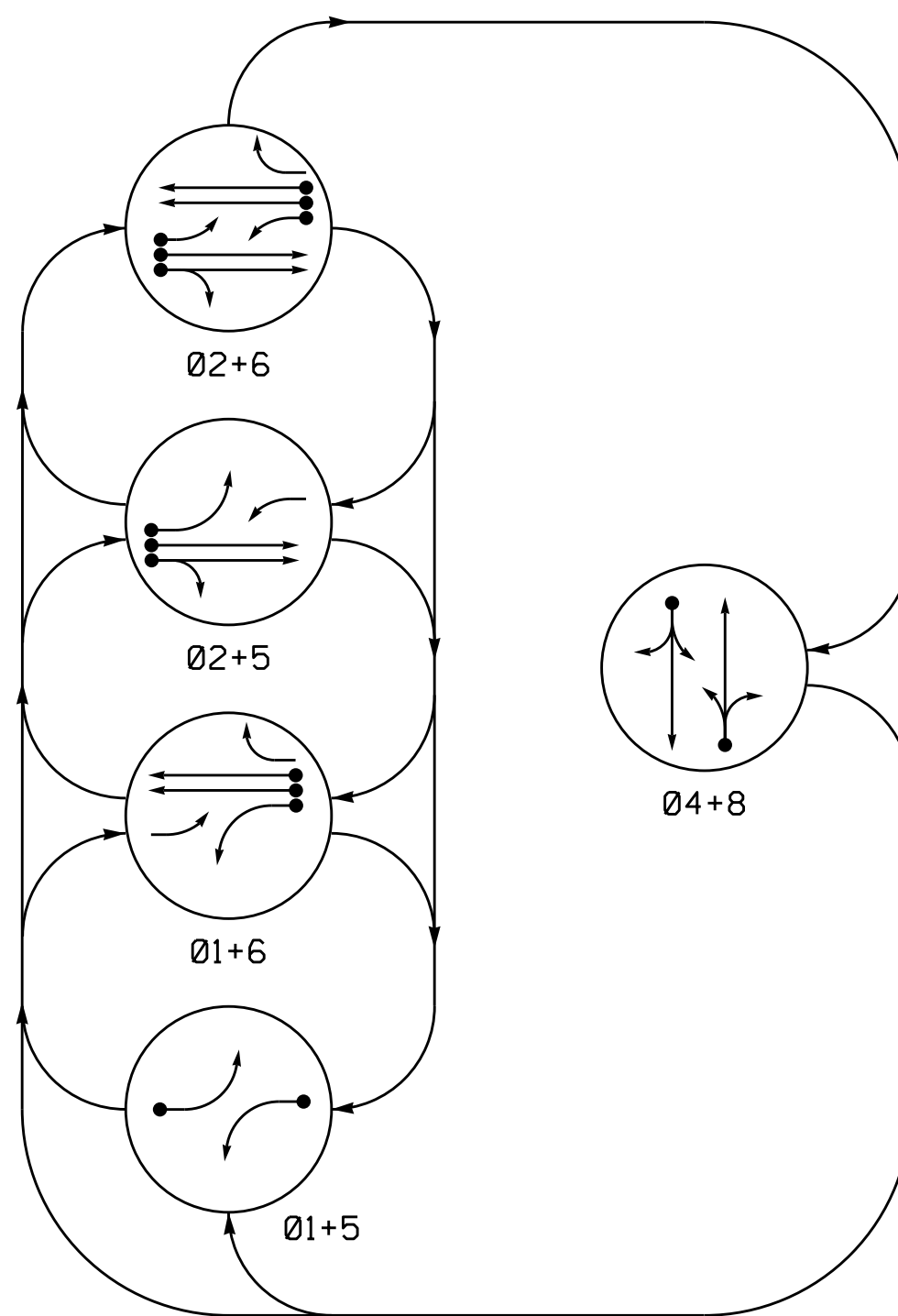
Index of Plans		
Sheet #	Reference #	Location/Description
Sig. 1	-----	Title Sheet
Sig. 2.0-6.8	07-1191	I-85 Bus./US 29-70 at SR 1144 (River Road)
Sig. 7.0-7.1	-----	Standard Plate Sheets
M1-M8	-----	Standard Drawing For Metal Poles

**INTELLIGENT TRANSPORTATION AND SIGNALS UNIT**

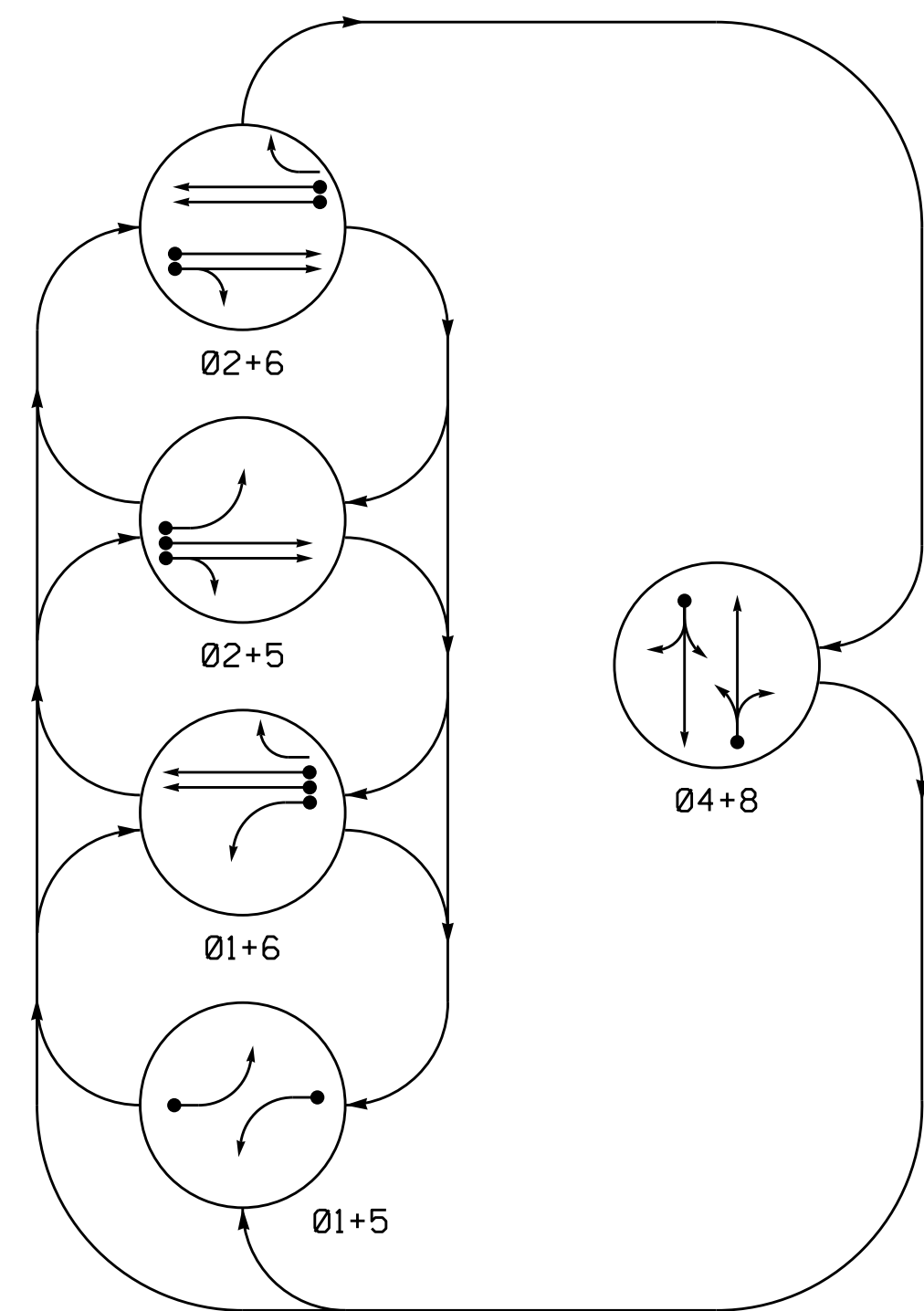
Robert J. Ziemba, PE - Central Region Signals Engineer  
Keith M. Mims, PE - Signal Equipment Project Engineer

**CONTRACT: C204100**

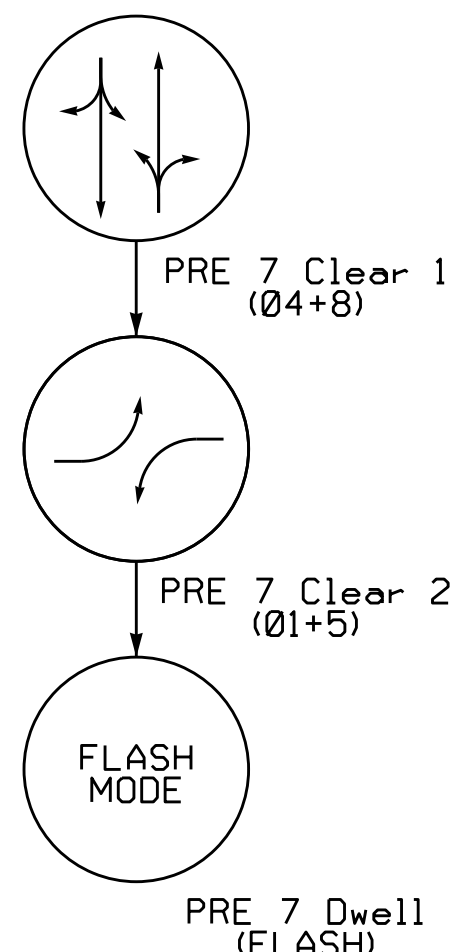
**DEFAULT PHASING DIAGRAM**



**ALTERNATE PHASING DIAGRAM**



**LONG VEHICLE EXTENSION FAILURE PREEMPT PHASES**

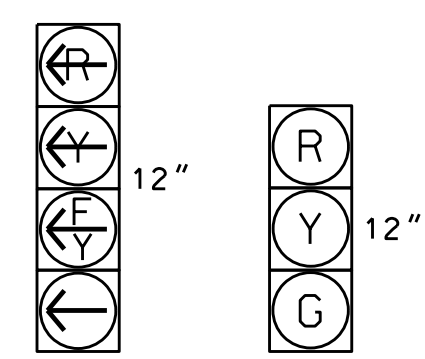


**DEFAULT PHASING TABLE OF OPERATION**

SIGNAL FACE	PHASE											
	01+5	01+6	01+7	02+5	02+6	02+7	04+8	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7
11	-	-	-	-	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	R	Y	Y			
41, 42, 43	R	R	R	R	G	G	R	R	R			
51	-	-	-	-	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	R	Y	Y			
81, 82, 83	R	R	R	R	G	G	R	R	R			

**SIGNAL FACE I.D.**

All Heads L.E.D.



**OASIS 2070 LOOP & DETECTOR INSTALLATION CHART**

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING							
					PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM	NEW CARD
1A	6X40	0	2-4-2	Y	1	Y	Y	-	-	15 *	-	Y
2A	6X6	420	5	-	2	Y	Y	-	-	-	-	Y
2B	6X6	420	5	-	2	Y	Y	-	-	-	-	Y
4A	6X40	0	2-4-2	Y	4	Y	Y	-	-	10	-	Y
5A	6X60	0	2-4-2	-	5	Y	Y	-	-	15 *	-	Y
6A	6X6	420	5	Y	6	Y	Y	-	-	-	-	Y
6B	6X6	420	5	Y	6	Y	Y	-	-	-	-	Y
8A	6X40	0	2-4-2	-	8	Y	Y	-	-	10	-	Y

\* Disable Delay During Alternate Phasing Operation.  
# Disable Phase Call For Loop During Alternate Phasing Operation.

**ALTERNATE PHASING TABLE OF OPERATION**

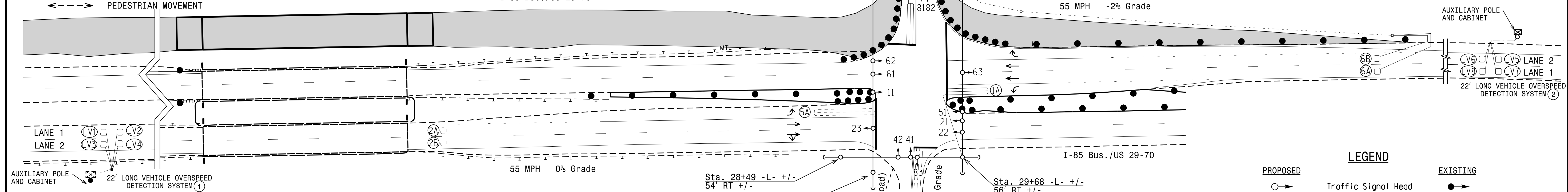
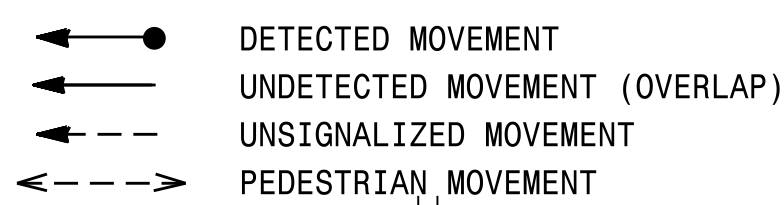
SIGNAL FACE	PHASE											
	01+5	01+6	02+5	02+6	02+7	04+8	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7
11	-	-	-	-	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	R	Y	Y			
41, 42, 43	R	R	R	R	G	G	R	R	R			
51	-	-	-	-	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	R	Y	Y			
81, 82, 83	R	R	R	R	G	G	R	R	R			

**5 Phase Fully Actuated W/ Long Vehicle Detection (Isolated)**

**NOTES**

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Set all detector units to presence mode.
- Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.
- Pavement markings are existing.

**PHASING DIAGRAM DETECTION LEGEND**



**LONG VEHICLE EXTENSION FAILURE PREEMPT**

FUNCTION	PRE 7
Interval 1 - Green Clear	15
Interval 1 - Yellow Clear	0.0*
Interval 1 - Red Clear	0.0*
Interval 2 - Green Clear	10
Interval 2 - Yellow Clear	0.0*
Interval 2 - Red Clear	0.0*
Interval 3 - Dwell Green	255
Interval 3 - Dwell Yellow	0.0*
Interval 3 - Dwell Red	0.0*
Interval 4 - Exit Green	1
Interval 4 - Yellow	0.0
Interval 4 - Red	0.0
Exit Phase(s)	2+6
Priority	-
Delay Time	0
Min Green Before Pre	14
Ped Clear Before Pre	0
Yellow Clear Before Pre	0.0*
Red Clear Before Pre	0.0*
Dwell Min Time	14
Flash Dwell Interval?	Y
Enable Backup Protection	N
Ped Clear Through Yellow	N
Omit Overlaps	-

**OASIS 2070 TIMING CHART**

FEATURE	PHASE							
	1	2	4	5	6	8		
Min Green 1*	7	14	7	7	14	7		
Extension 1*	2.0	6.0	3.0	2.0	6.0	3.0		
Max Green 1*	20	120	25	25	120	25		
Yellow Clearance	3.0	5.4	4.3	3.0	5.4	3.6		
Red Clearance	2.3	1.0	1.9	2.8	1.0	2.0		
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0		
Walk 1*	-	-	-	-	-	-		
Don't Walk 1	-	-	-	-	-	-		
Seconds Per Actuation*	-	1.5	-	-	1.5	-		
Max Variable Initial*	-	46	-	-	46	-		
Time Before Reduction*	-	20	-	-	20	-		
Time To Reduce*	-	50	-	-	50	-		
Minimum Gap	-	3.4	-	-	3.4	-		
Recall Mode	-	MIN RECALL	-	-	MIN RECALL	-		
Vehicle Call Memory	-	YELLOW	-	-	YELLOW	-		
Dual Entry	-	-	ON	-	-	ON		
Simultaneous Gap	ON	ON	ON	ON	ON	ON		

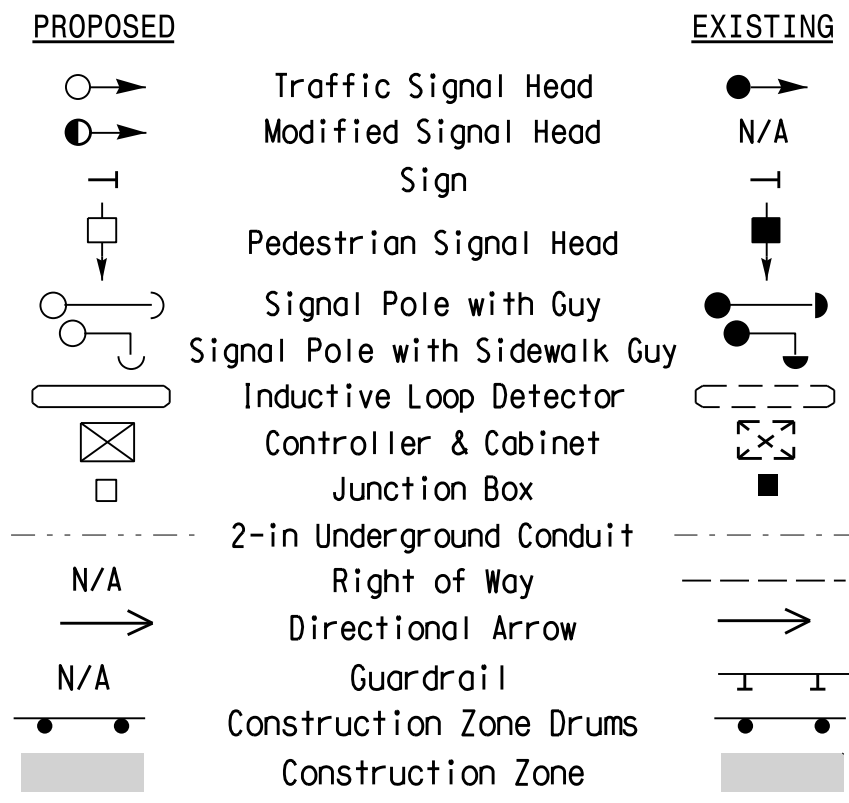
\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

**LONG VEHICLE OVERSPEED DETECTION SYSTEM LOOP & DETECTOR INSTALLATION CHART**

LOOP NO.	SIZE (ft)	TURNS	DIST. FROM STOPBAR (ft)	NEW	EXISTING	INDUCTIVE LOOP	DETECTOR UNITS	NEMA PHASE	TIMING		PLACE CALL DURING PHASE	INHIBIT DELAY DURING GREEN?
									FEATURE	TIME		
LV1	6X6	4	1015	X		1	1	2*	NONE	- SEC.	ALL	NO
LV2	6X6	4	999	X		1	1	2*	NONE	- SEC.	ALL	NO
LV3	6X6	4	1015	X		2	2	2*	NONE	- SEC.	ALL	NO
LV4	6X6	4	999	X		2	2	2*	NONE	- SEC.	ALL	NO
LV5	6X6	4	1015	X		1	1	1	NONE	- SEC.	ALL	NO
LV6	6X6	4	999	X		1	1	2	NONE	- SEC.	ALL	NO
LV7	6X6	4	1015	X		2	2	1	NONE	- SEC.	ALL	NO
LV8	6X6	4	999	X		2	2	2	NONE	- SEC.	ALL	NO
LVDS THRESHOLD SPEED (MPH)	55						2	6				
LVDS EXTEND TIME	12 SEC.						2	6				

\*Phase hold output to controller

**LEGEND**



**Signal Upgrade Temporary Design 1 (TMP Phase I)**

Prepared in the Offices of:  
  
 750 N. Greenfield Pkwy, Garner, NC 27529  
 SCALE: 0 50 1"=50'

**I-85 Bus. /US 29-70 at SR 1144 (River Road)**

Division 7 Guilford County Jamestown  
 PLAN DATE: January 2018 REVIEWED BY:  
 PREPARED BY: I. O. Umozurike REVIEWED BY:

REVISIONS: INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
 ROBERT J. ZEMBA  
 ENGINEER  
 026486  
 2/7/2018  
 SIG. INVENTORY NO. 07-119111

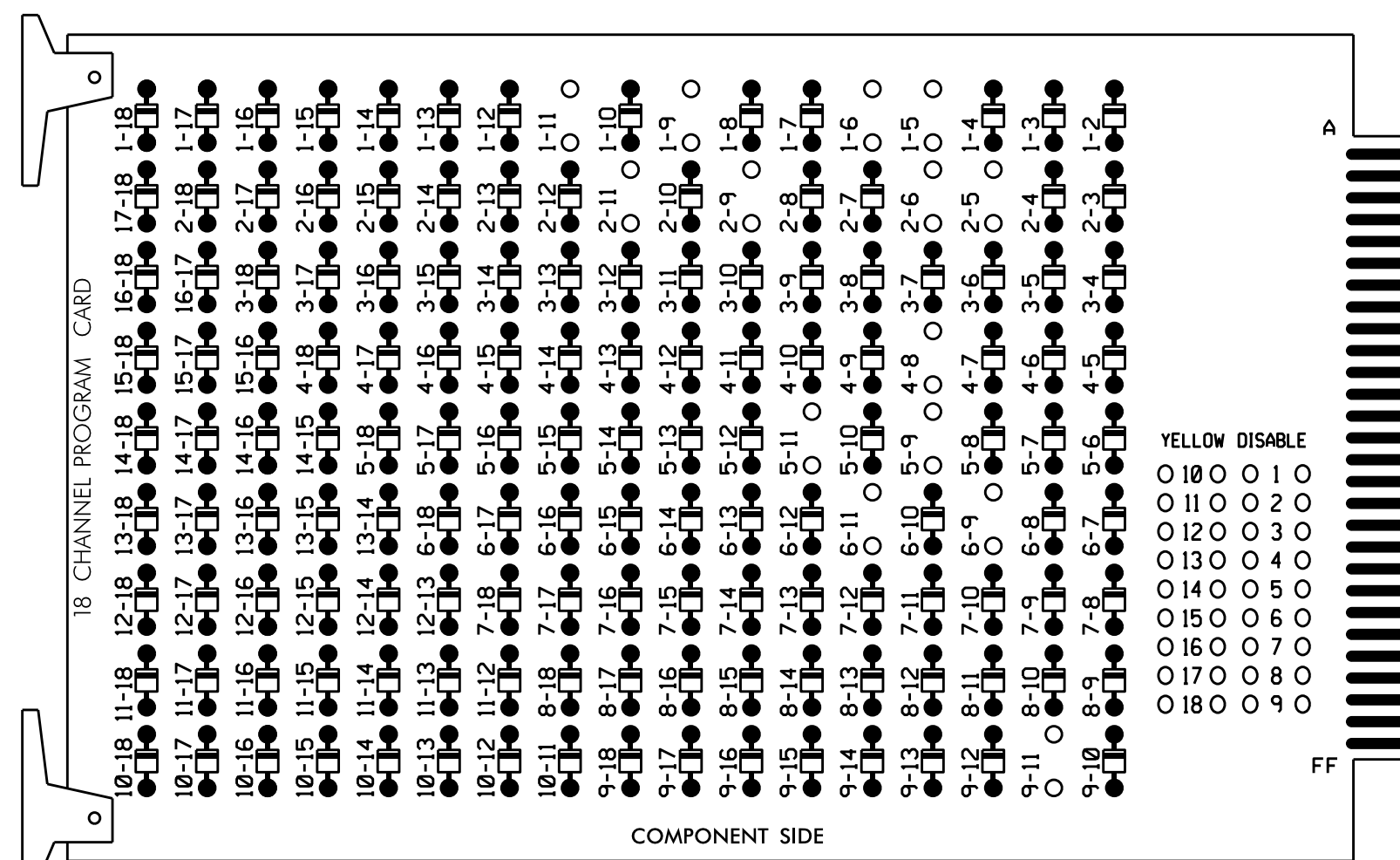
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 cz.lendo

\* Time defaults to time used for phase during normal operation

**EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

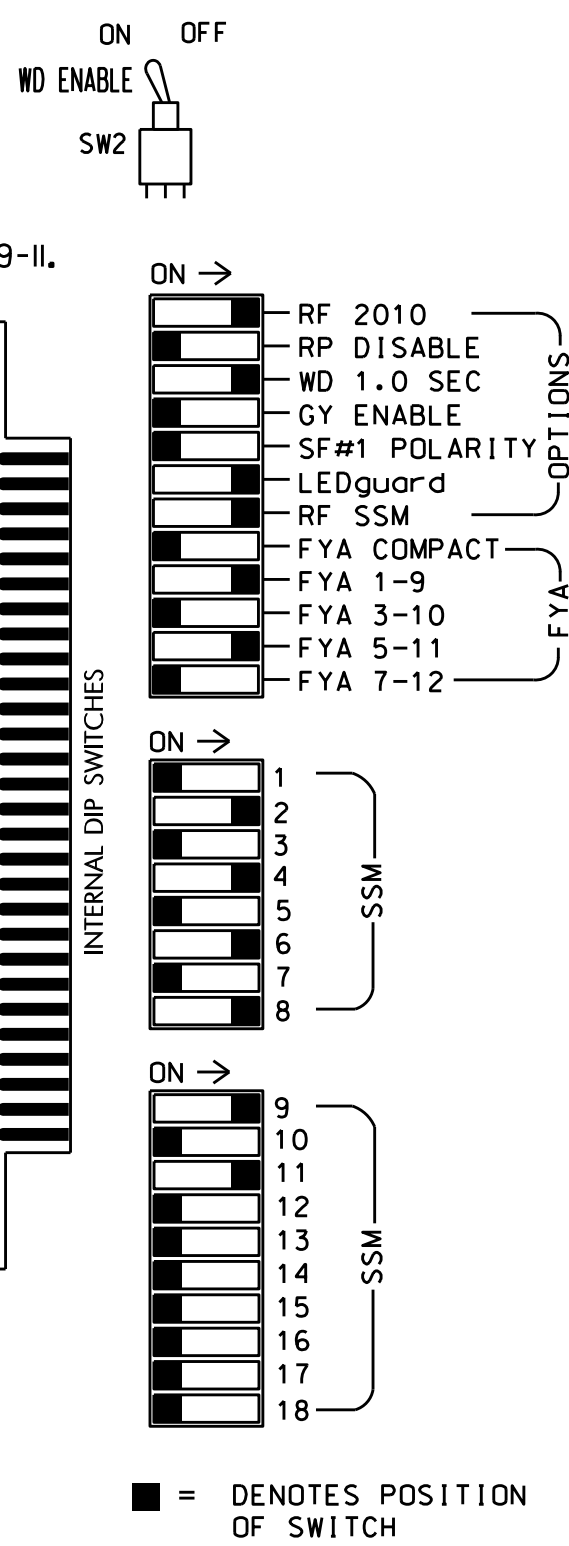
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUX S1,AUX S4  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22,23	NU	NU	41,42,43	NU	51	61,62,63	NU	NU	81,82,83	NU	11	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127							133										

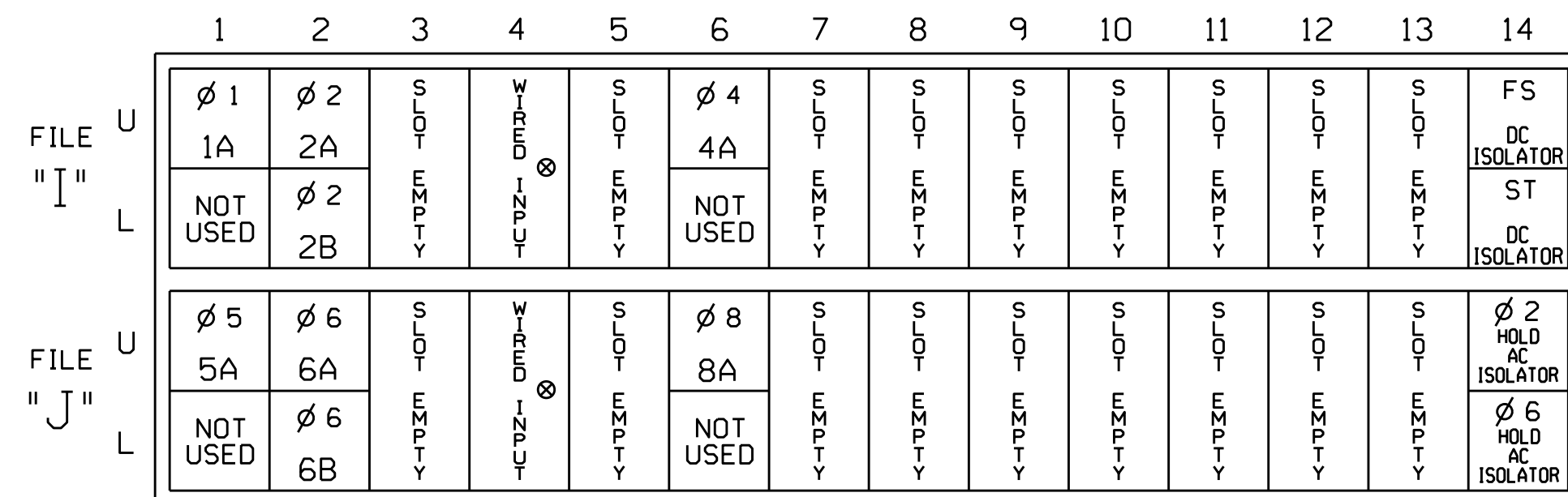
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

See AC Isolator programming detail on sheet 6.

**INPUT FILE CONNECTION & PROGRAMMING CHART**

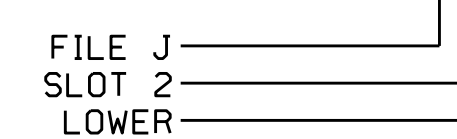
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9★	22	2	Y	Y	Y		3
	-	J1U	55	17★	55	5	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

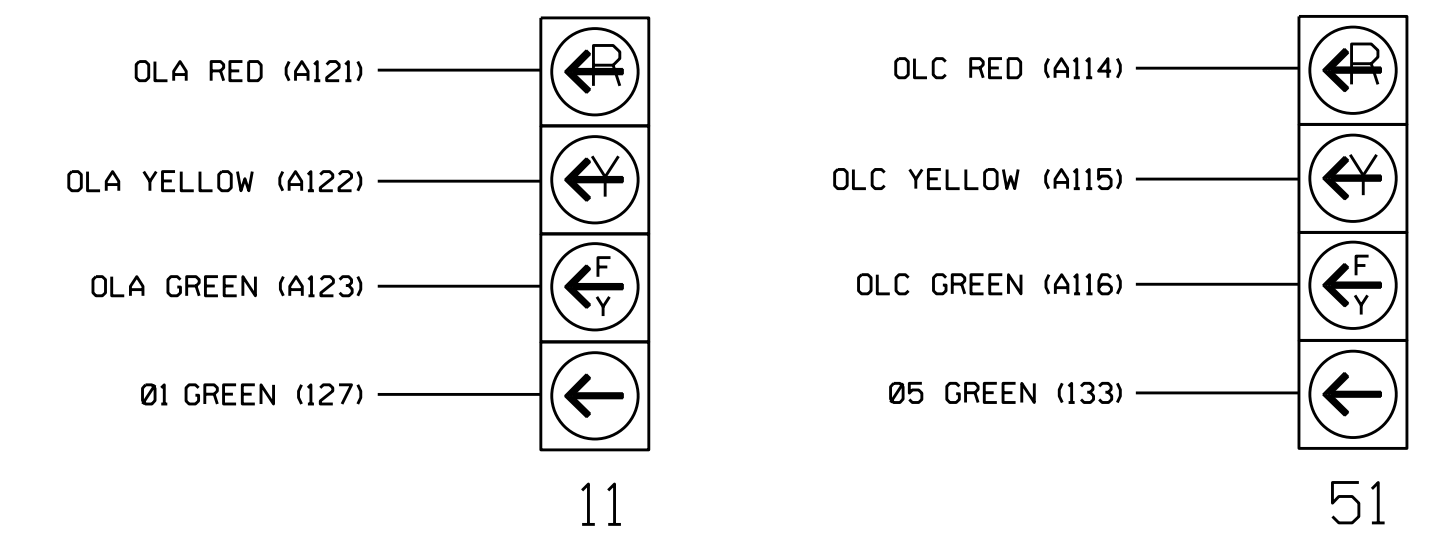
★ See Input Page Assignment programming details on sheets 3 and 4.

**INPUT FILE POSITION LEGEND: J2L**



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

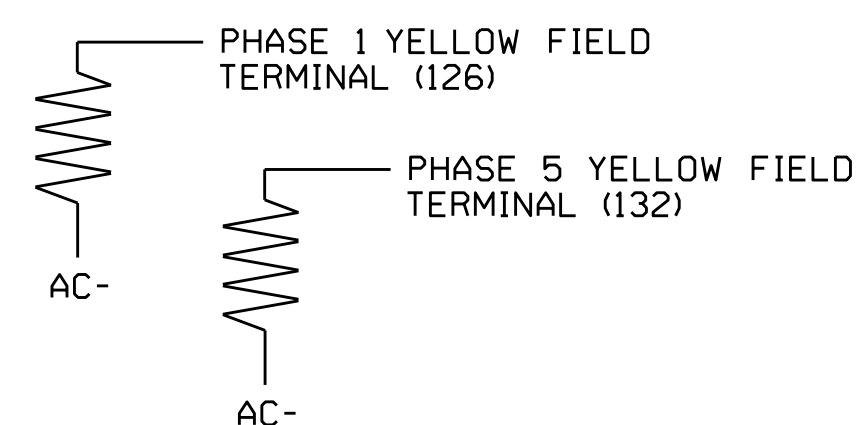
The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T1  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

**LOAD RESISTOR INSTALLATION DETAIL**

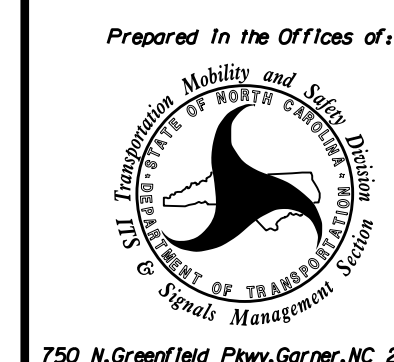
(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Temporary Design 1 (TMP Phase I)  
 Sheet 1 of 8

ELECTRICAL AND PROGRAMMING DETAILS FOR:



I-85 Bus. / US 29-70  
 at  
 SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY:

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
 NORTH CAROLINA PROFESSIONAL ENGINEER  
 KEITH M. MINNS  
 036880  
 2/8/2018  
 DATE  
 SIG. INVENTORY NO. 07-1191T1

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SEQUENCE, AND TO FLASH INTERSECTION IF LVOD SYSTEMS FAIL**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, AND 7.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)  
IF INPUT ASSIGNMENT #13 IS ON  
OR INPUT ASSIGNMENT #14 IS ON

↓  
SCROLL DOWN

THEN:  
DELAY FOR 240.0 SECONDS  
SET INPUT ASSIGNMENT #64 ON

NOTE: INPUT 64 (PREEMPT 7) WILL INITIATE CONTROLLER FLASH IF EITHER LVODS INPUT IS "STUCK ON" FOR 4 CONSECUTIVE MINUTES.

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**I/O REFERENCE SCHEDULE**

- INPUT 13 = Input from LVODS #1
- INPUT 14 = Input from LVODS #2
- INPUT 64 = Preempt 7
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

INPUTS 13, 14, AND 64 HAVE BEEN REASSIGNED. SEE SHEET 6 FOR PROGRAMMING DETAILS.

**OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS), PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

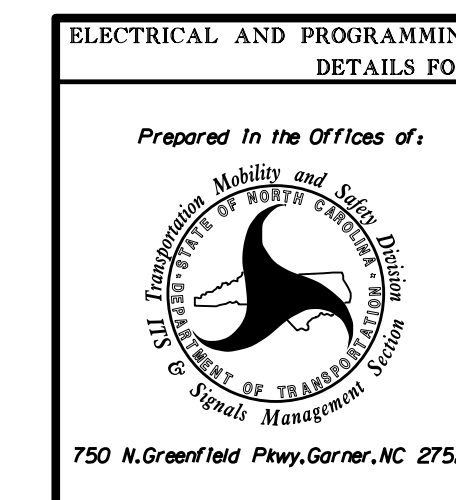
NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T1  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 1 (TMP Phase I)  
Sheet 2 of 8



Division 7	Guilford County	Jamestown
PLAN DATE: February 2018	REVIEWED BY:	
PREPARED BY: S. Armstrong	REVIEWED BY:	
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SEAL 036880

KEITH M. MINS

DocuSigned by: Keith M. Mins 2/8/2018

SIG. INVENTORY NO. 07-1191T1

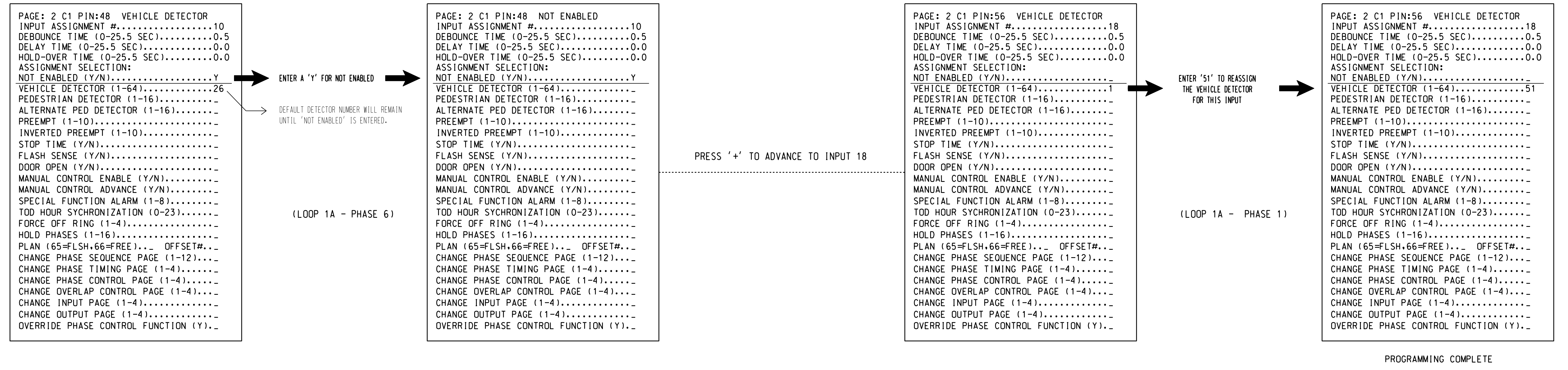
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sarmstrong

**INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A**

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

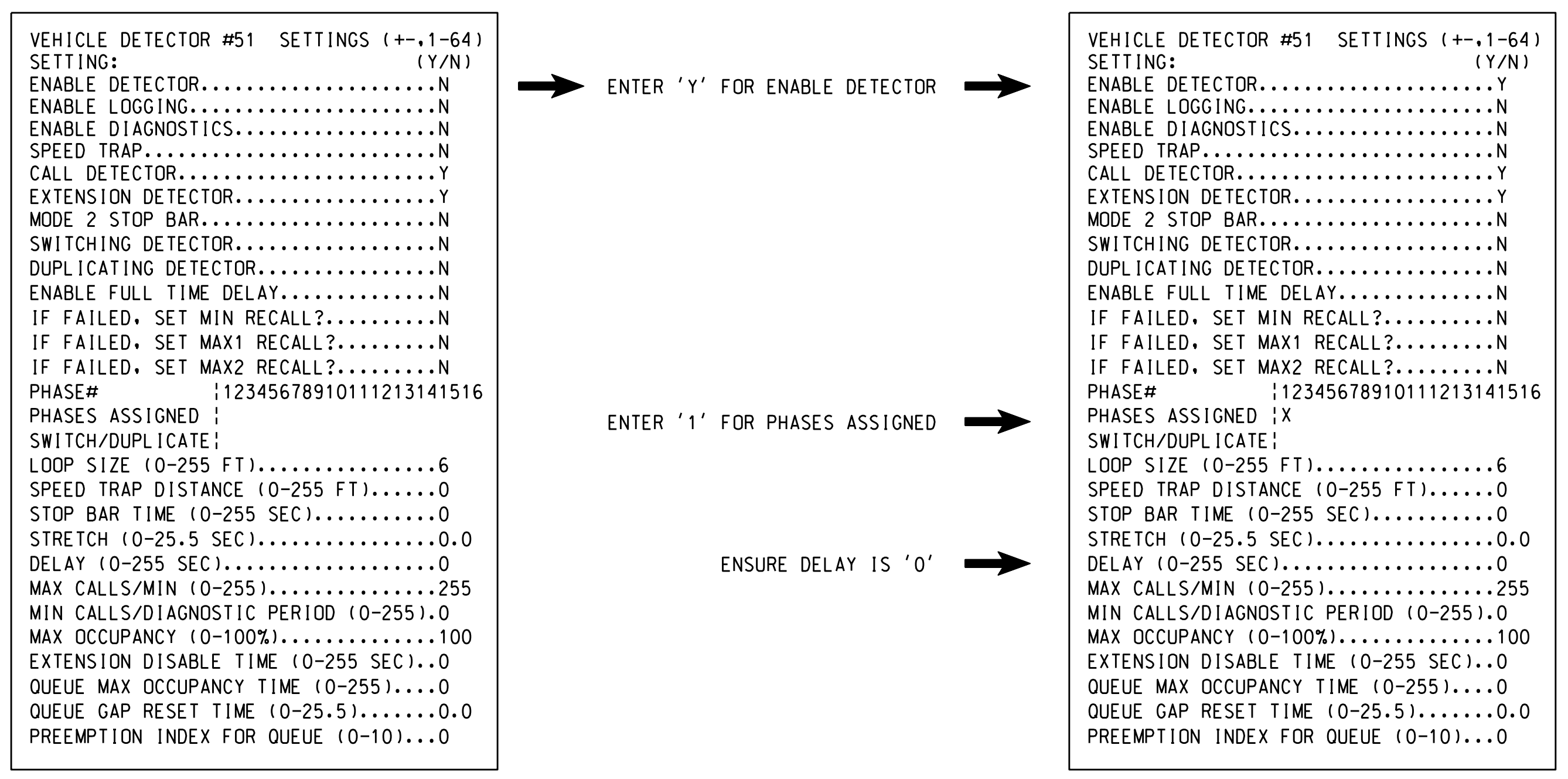
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.



**SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)**

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-11911I  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

08-FEB-2018 11:08 C:\MITSAS\15\_Signal\work\hgr\oups\sig\_mph\armstrong\071191\_1sm\_elec\_xxx.dgn sarmstrong

Electrical Detail - Temporary Design 1 (TMP Phase I)  
Sheet 3 of 8

	I-85 Bus. / US 29-70 at SR 1144 (River Road)		
	Prepared In the Offices of: Guilford County 750 N. Greenfield Pkwy, Garner, NC 27529	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	

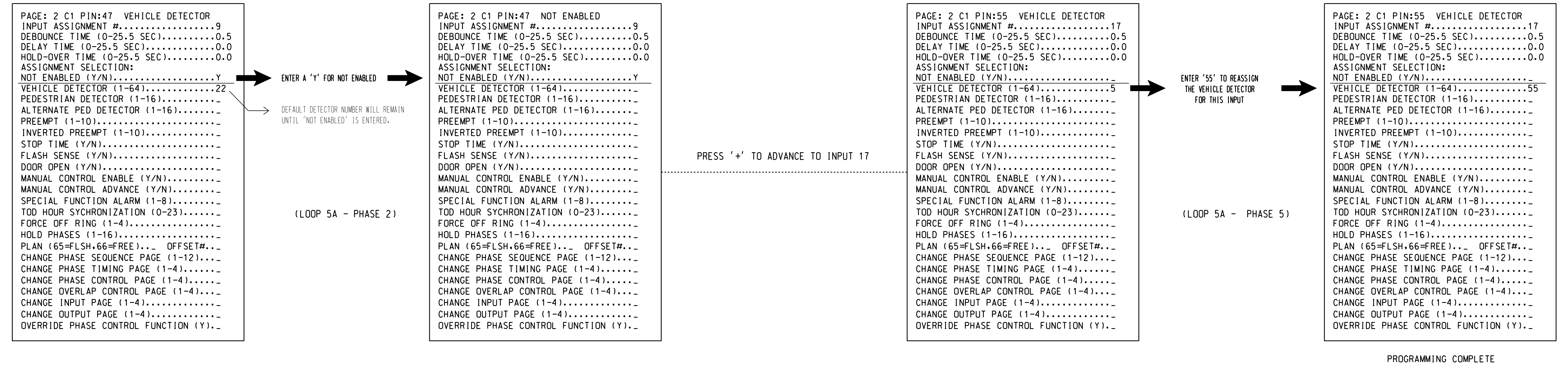
DocuSigned by: Keith M. Mims 2/8/2018  
2F8078E85C0346S  
SIG. INVENTORY NO. 07-11911I

INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 5A

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #9 (DETECTOR 22) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 2 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 55 TO INPUT #17 SO THAT THE DELAY ON LOOP 5A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

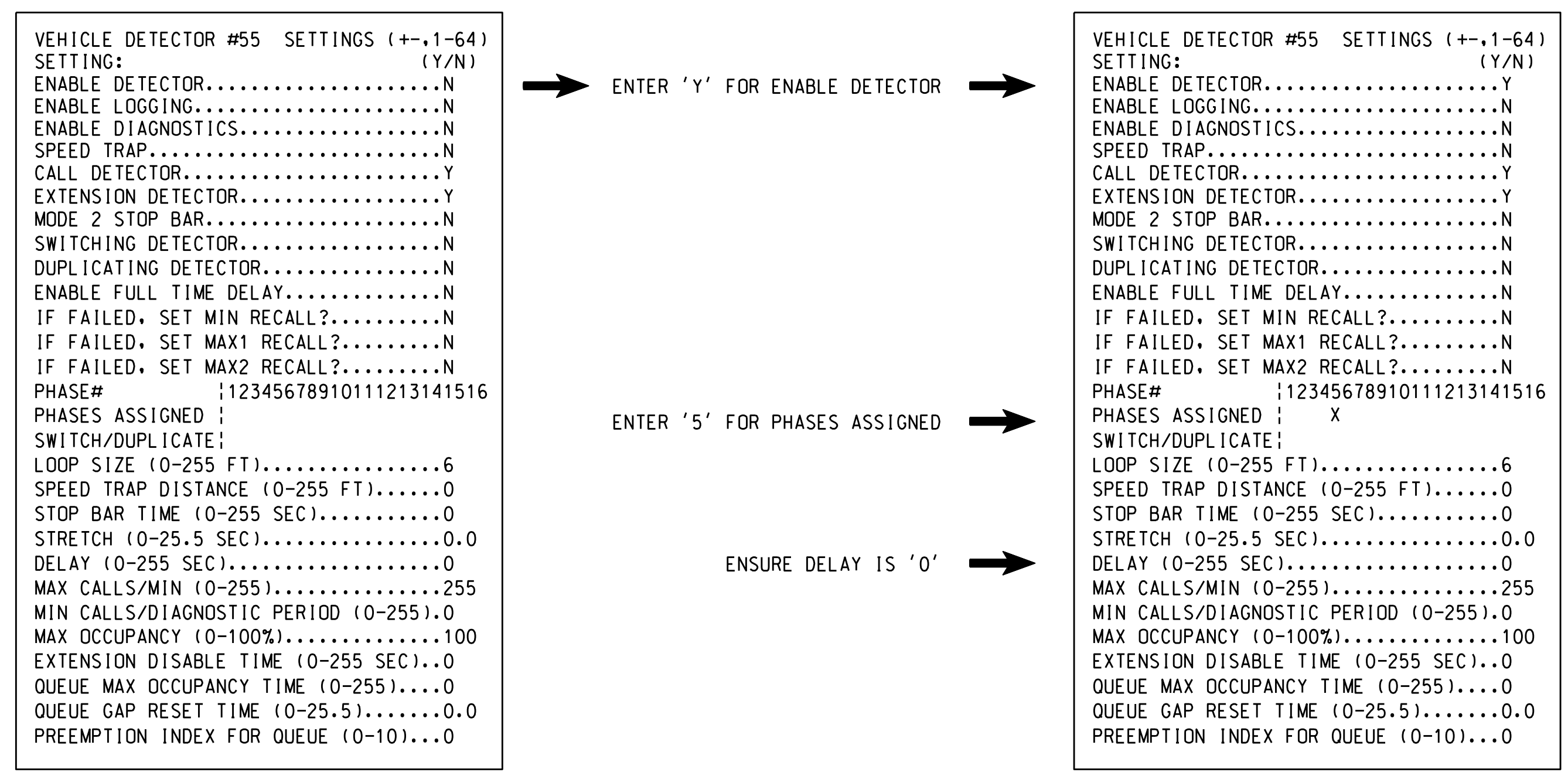
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 9 IS REACHED.



SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 5A (ALT.)

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #55.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-11911I  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

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Electrical Detail - Temporary Design 1 (TMP Phase I)  
Sheet 4 of 8

I-85 Bus. / US 29-70  
at  
SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY: [Signature]  
PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

REVISIONS: [Table with columns for REVISIONS, INIT., DATE]

DocuSigned by: Keith M. Mims 2/8/2018

750 N. Greenfield Pkwy, Garner, NC 27529

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SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
KEITH M. MIMS  
036880

SIG. INVENTORY NO. 07-11911I

## ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

<u>PHASING</u>	<u>INPUTS PAGE</u>	<u>OVERLAPS PAGE</u>
ACTIVE PAGES REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	1
ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	2

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

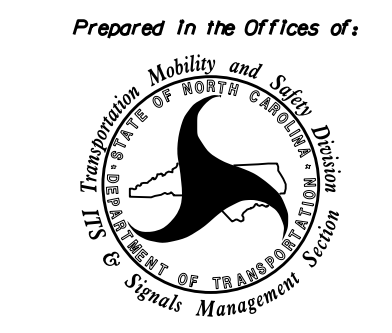
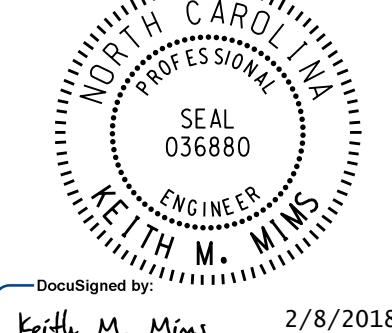
OVERLAPS PAGE 2: Modifies overlap parent phases for heads 11 and 51 to run protected turns only.

INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T1  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

Electrical Detail - Temporary Design 1 (TMP Phase I)  
 Sheet 5 of 8

ELECTRICAL AND PROGRAMMING DETAILS FOR:	<b>I-85 Bus. / US 29-70          at          SR 1144 (River Road)</b>	<b>DOCUMENT NOT CONSIDERED          FINAL UNLESS ALL          SIGNATURES COMPLETED</b>												
	Division 7      Guilford County      Jamestown													
	PLAN DATE: February 2018      REVIEWED BY:													
	PREPARED BY: S. Armstrong      REVIEWED BY:													
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REVISIONS	INIT.	DATE												
	DocuSigned by: <b>Keith M. Mins</b> 2/8/2018													
	750 N. Greenfield Pkwy, Garner, NC 27529	SIG. INVENTORY NO. 07-1191T1												

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 sarmstrong



**INPUT ASSIGNMENT PROGRAMMING DETAIL TO REASSIGN LONG VEHICLE OVERSPEED DETECTION SYSTEM FUNCTION**  
(program controller as shown below)

This programming takes each of the Long Vehicle Overspeed Detection System inputs and reassigns a unique Hold Phase to it.

FROM MAIN MENU PRESS '5' (INPUTS), THEN '+' UNTIL PIN 51 (INPUT 13) IS REACHED.

```

PAGE: 1 C1 PIN:51 HOLD PHASES
INPUT ASSIGNMENT #.....13
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....2
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+'

INPUT 13 IS THE OUTPUT FROM LVODS #1

```

PAGE: 1 C1 PIN:52 HOLD PHASES
INPUT ASSIGNMENT #.....14
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....6
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+' OR '-' TO REACH INPUT 64

INPUT 14 IS THE OUTPUT FROM LVODS #2

```

PAGE: 1 C1 PIN:0 PREEMPT
INPUT ASSIGNMENT #.....64
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....7
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PROGRAMMING COMPLETE

← Notice Preempt 7

**PREEMPT 7 PROGRAMMING DETAIL**  
(program controller as shown below)

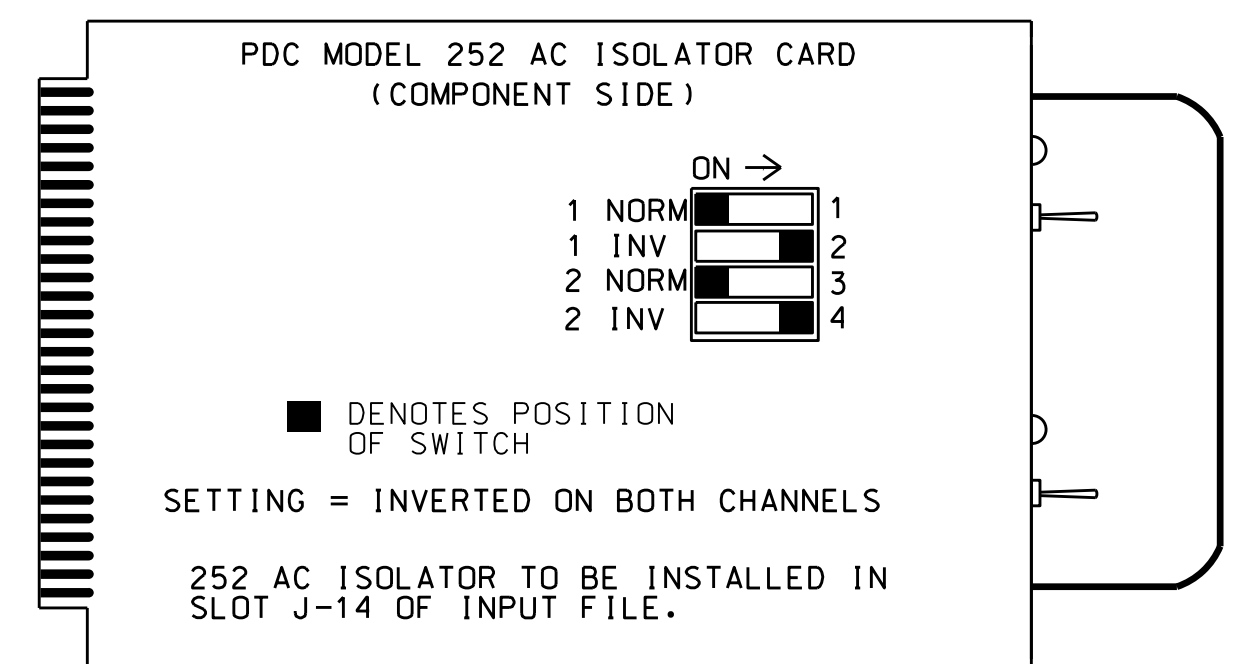
THIS PREEMPT GOES ACTIVE IF EITHER LVODS HAS BEEN ACTIVE FOR MORE THAN 4 CONSECUTIVE MINUTES AND WILL PUT THE INTERSECTION IN FLASH.

FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTIONS), PRESS 'NEXT' UNTIL PREEMPTION #7 IS REACHED.

PREEMPTION #7	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 15 0.0 0.0	X X
2 10 0.0 0.0	X X
3 255 0.0 0.0	X X
4 0 0.0 0.0	X X
5 1 0.0 0.0	X X
EXIT CALLS	
OPTIONS	
PRIORITY (Y/N TO SELECT)	.....HIGH
DELAY TIMER (0-255 SEC)	.....0.0
MIN GREEN BEFORE PRE (0= DEFAULT)	.....14
PED CLEAR BEFORE PRE (0= DEFAULT)	.....0
YELLOW CLEAR BEFORE PRE (0= DEFAULT)	.....0.0
RED CLEAR BEFORE PRE (0= DEFAULT)	.....0.0
DWELL MIN TIMER (0-255 SEC)	.....14
DWELL MAX TIMER (0=OFF,1-255MIN)	.....0
DWELL HOLD-OVER TIMER (0-255)	.....0
LATCH CALL?	.....N
LINK TO NEXT PREEMPT?	.....N
ENABLE BACKUP PROTECTION?	.....N
HOLD CLEAR 1 PHASES DURING DELAY?	.....N
FAST GREEN FLASH DWELL PHASES?	.....N
PED CLEARANCE THROUGH YELLOW?	.....N
INHIBIT OVERLAP GREEN EXTENSION?	.....N
SERVICE DURING SOFTWARE FLASH?	.....N
REST IN RED DURING DWELL INTERVAL?	.....N
FLASH DWELL INTERVAL?	.....Y
ALLOW PEDS IN DWELL INTERVAL?	.....N
RE-TIME DWELL INTERVAL?	.....Y
OVERLAPS:	ABCDEFGHIJKLMNOP
DWELL INT FLASH YELLOW	X X
OMIT OVERLAPS:	

**HOLD PHASE AC ISOLATOR (MODEL 252)**  
**OUTPUT PROGRAMMING DETAIL**

(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T1  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 1 (TMP Phase I)  
Sheet 6 of 8

Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL KEITH M. MINS ENGINEER 036880
	Division 7 Guilford County Jamestown PLAN DATE: February 2018 REVIEWED BY: PREPARED BY: S. Armstrong REVIEWED BY:	

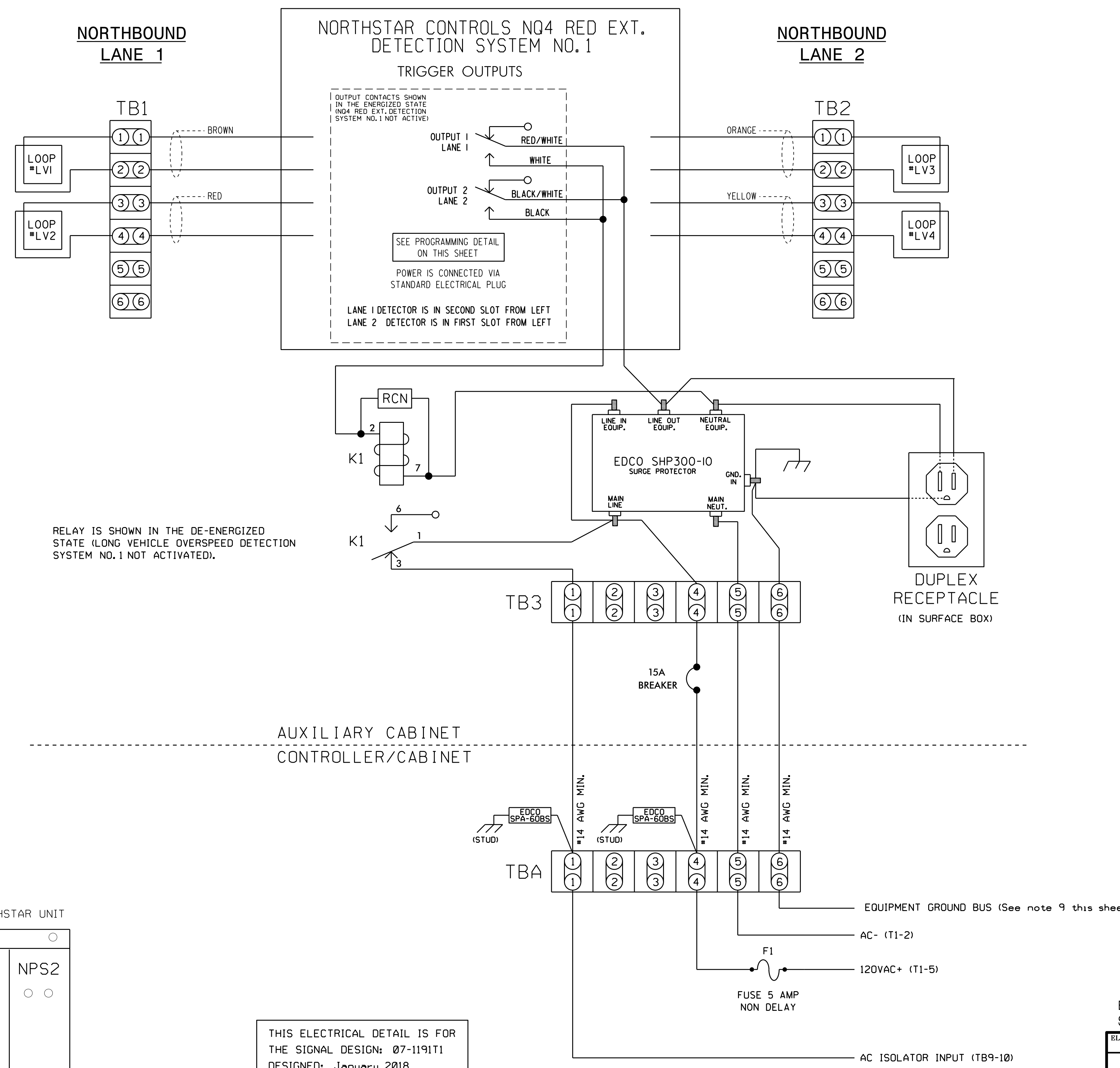
DocuSigned by: Keith M. Mins 2/8/2018  
2F8078E6CD3445 DATE  
SIG. INVENTORY NO. 07-1191T1

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 armstrong

(wire unit as shown below)

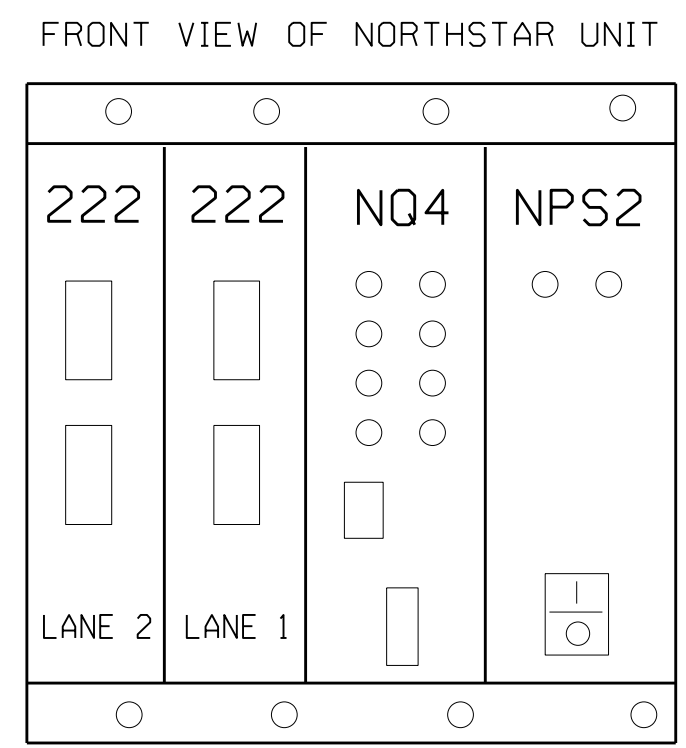
**NOTES**

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBA to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.
11. IMPORTANT! A jumper must be installed between Input File terminals J14-E and J14-K.
12. IMPORTANT! For proper operation of the Dynamic Red Extension System, tie TB9-12 to AC neutral.
13. IMPORTANT! Make sure both channels of the AC Isolator card inserted in Input File slot J14 are set for INVERTED OUTPUT operation. See sheet 6 of this electrical detail.



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 1 NOT ACTIVATED).

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T1  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A



**NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL**

(program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

Program the NQ4 by typing the following commands:

1. SET SPEED = 55
2. SET LENGTH = 22'
3. SET ALARMTIME = 12
4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
5. SET LOOP LENGTH = 6' (program actual measured loop length)
6. SAVE

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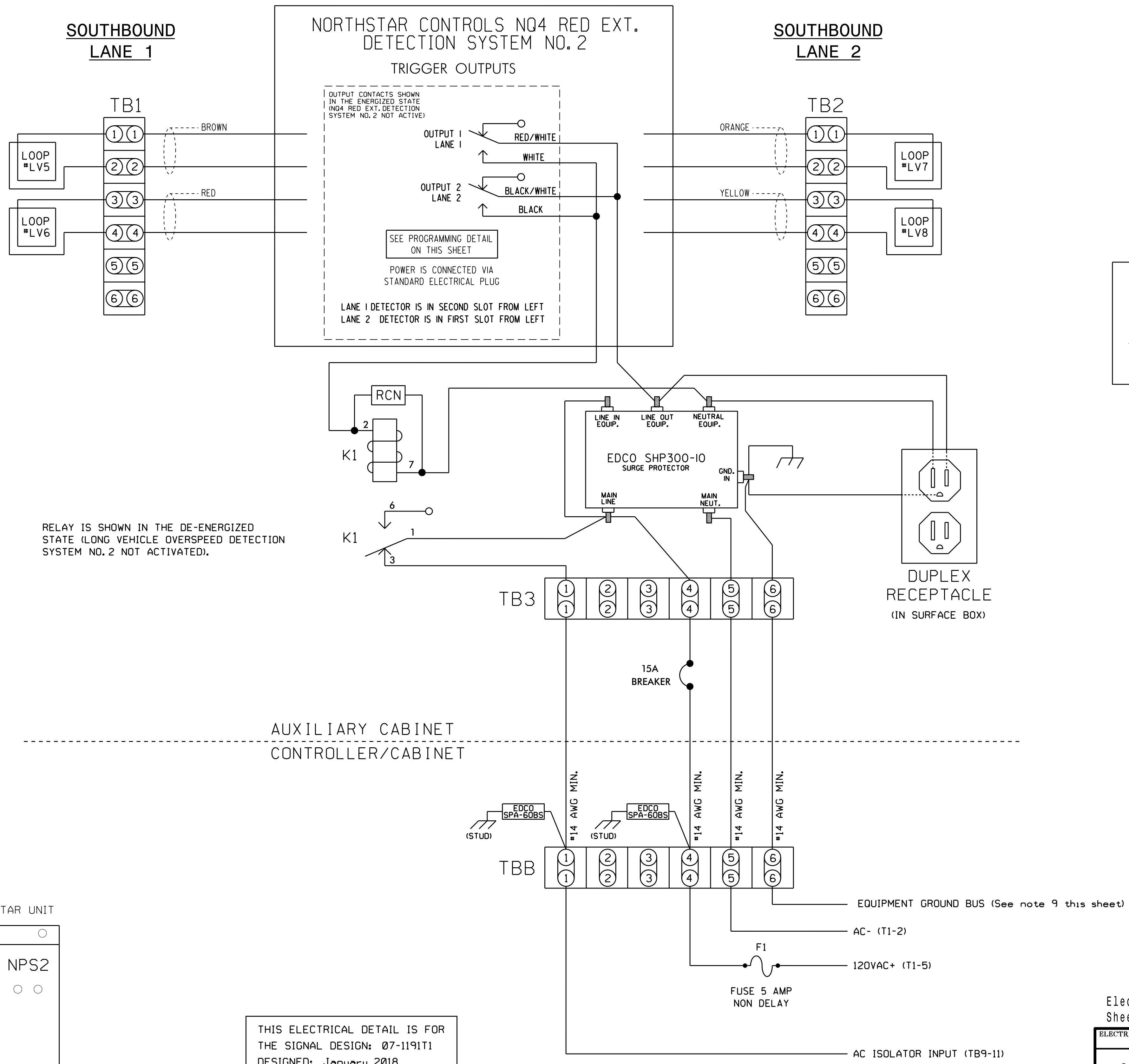
Electrical Detail - Temporary Design 1 (TMP Phase I)  
 Sheet 7 of 8

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  KEITH M. MIMS ENGINEER
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

**NOTES**

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBB to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.

(wire unit as shown below)



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 2 NOT ACTIVATED).

OUTPUT CONTACTS SHOWN IN THE ENERGIZED STATE (NQ4 RED EXT. DETECTION SYSTEM NO. 2 NOT ACTIVE)

SEE PROGRAMMING DETAIL ON THIS SHEET

POWER IS CONNECTED VIA STANDARD ELECTRICAL PLUG

LANE 1 DETECTOR IS IN SECOND SLOT FROM LEFT  
LANE 2 DETECTOR IS IN FIRST SLOT FROM LEFT

**NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL**

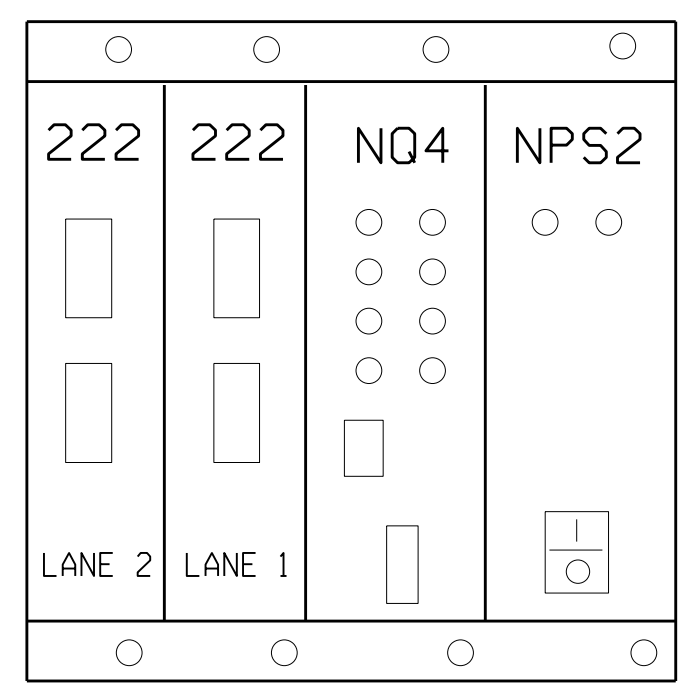
(program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

Program the NQ4 by typing the following commands:

1. SET SPEED = 55
2. SET LENGTH = 22'
3. SET ALARMTIME = 12
4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
5. SET LOOP LENGTH = 6' (program actual measured loop length)
6. SAVE

FRONT VIEW OF NORTHSTAR UNIT



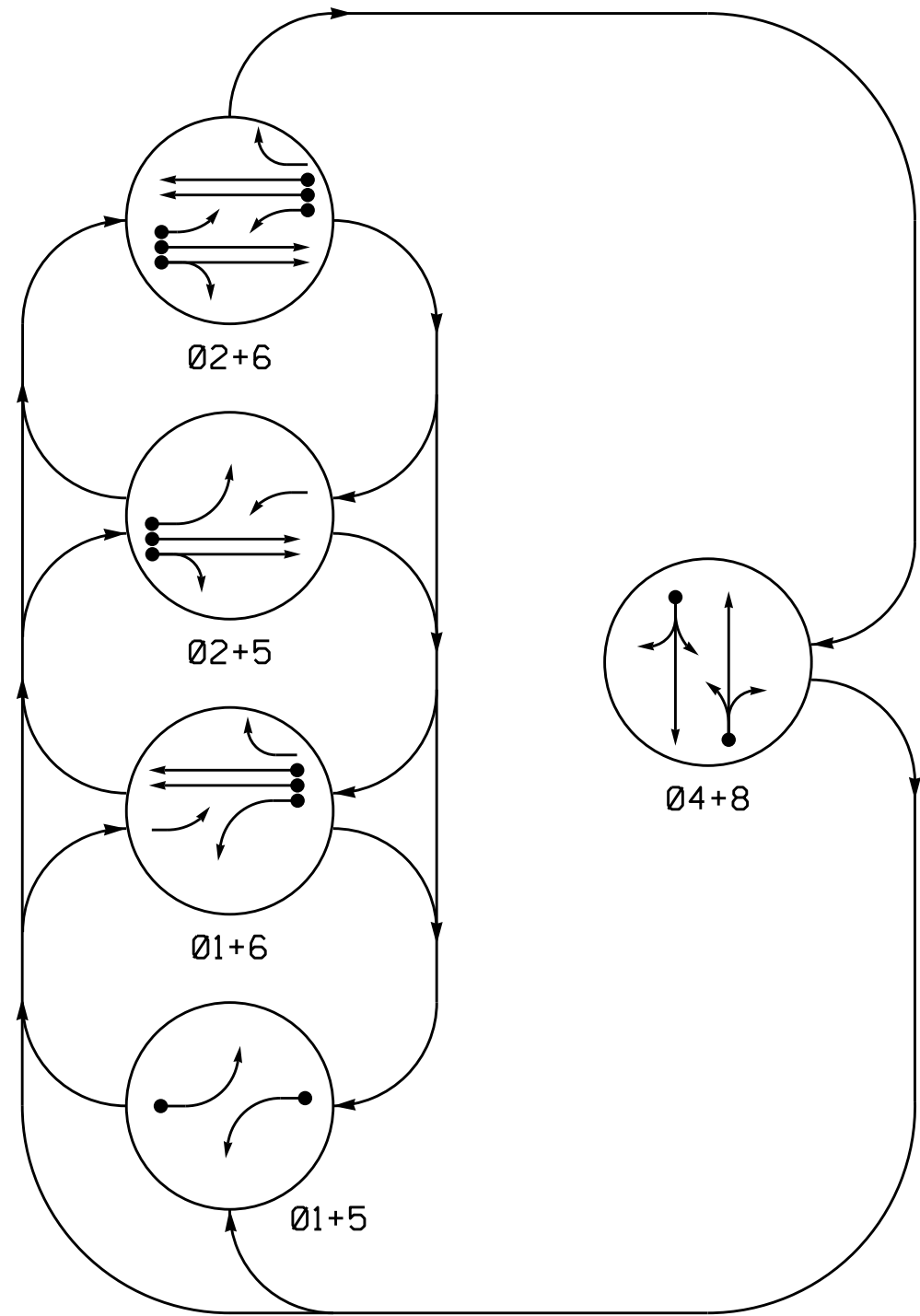
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T1  
DESIGNED: January 2018  
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REVISED: N/A

Electrical Detail - Temporary Design 1 (TMP Phase I)  
Sheet 8 of 8

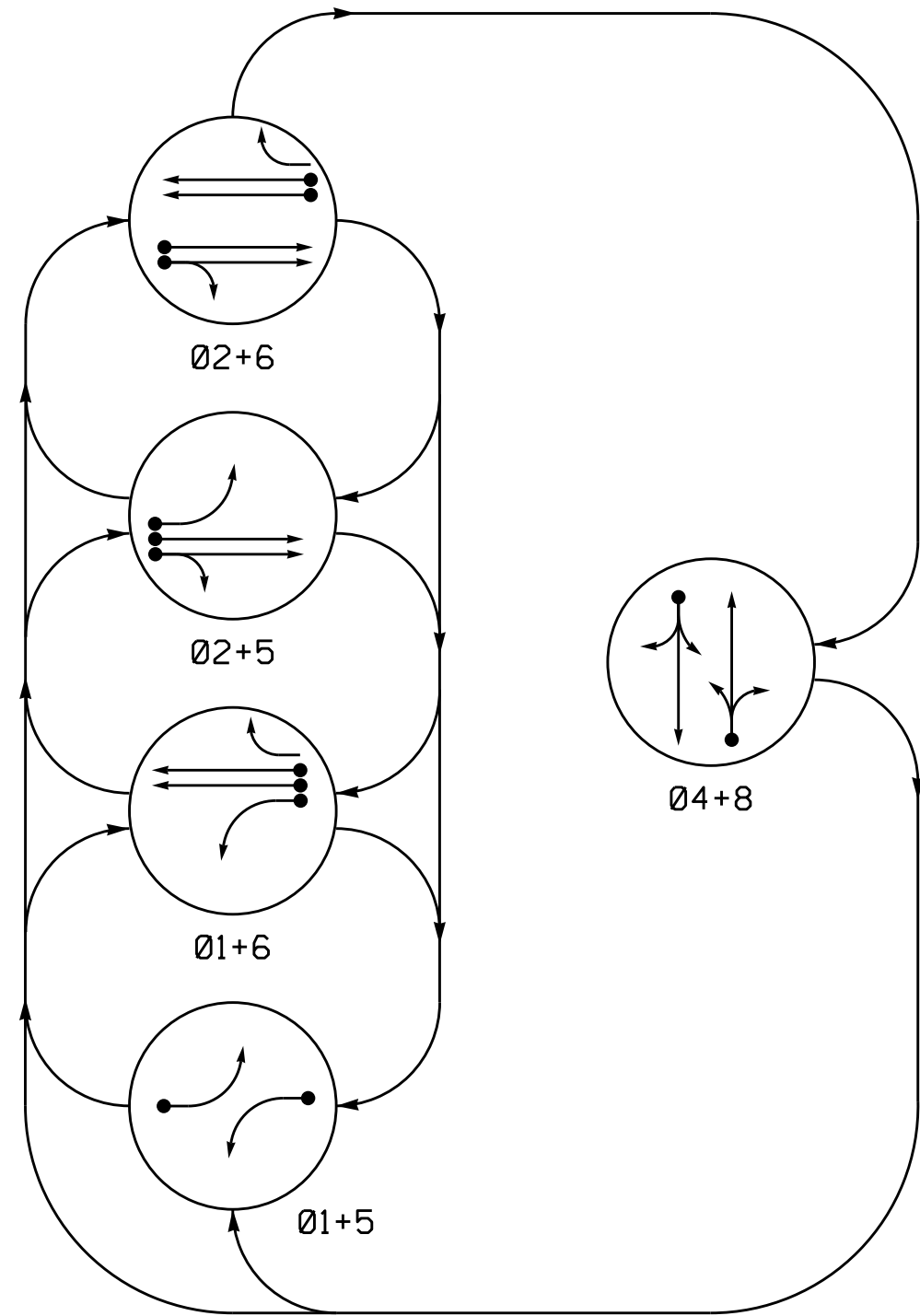
ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  Keith M. Miras 2/8/2018
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

08-FEB-2018 11:10 S:\MITSAS\115\_Signal\work\hgr\cous\sig\_Mph\armstrong071191\_sm.ele.xxx.dgn sarmsr00g

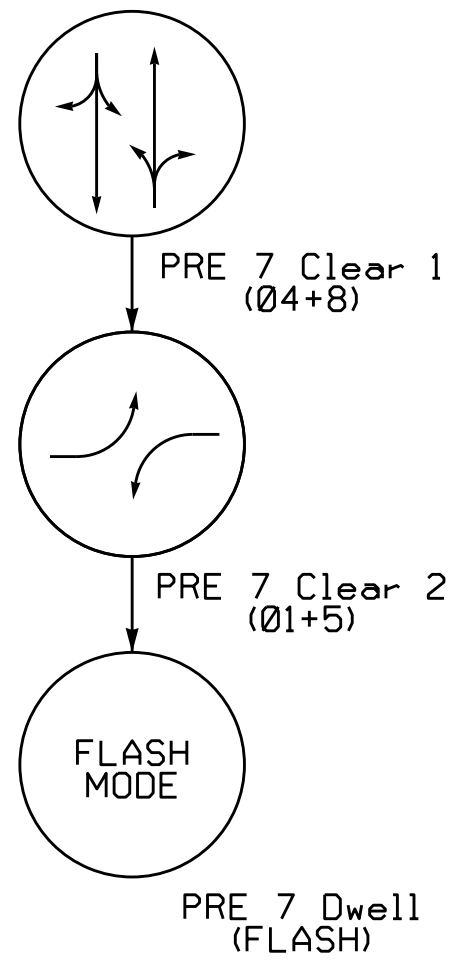
DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



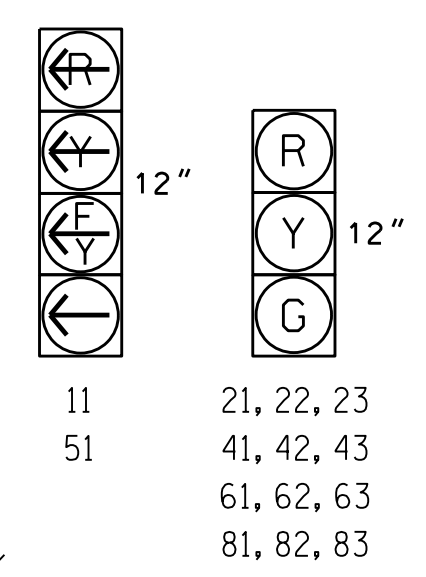
LONG VEHICLE EXTENSION FAILURE PREEMPT PHASES



DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	04+8	01+5	01+6	04+8
11	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	R	Y
41, 42, 43	R	R	R	G	G	R	R	R
51	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	Y	Y
81, 82, 83	R	R	R	R	G	G	R	R

SIGNAL FACE I.D.



OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING			
					PHASE	CALLING	EXTENSION	STRETCH TIME
1A	6X40	0	2-4-2	Y	1	Y	Y	15 *
2A	6X6	400	5	Y	2	Y	Y	-
2B	6X6	400	5	Y	2	Y	Y	-
4A	6X40	0	2-4-2	Y	4	Y	Y	10
5A	6X40	0	2-4-2	Y	5	Y	Y	15 *
6A	6X6	420	5	Y	6	Y	Y	-
6B	6X6	420	5	Y	6	Y	Y	-
8A	6X40	0	2-4-2	Y	8	Y	Y	10

\* Disable Delay During Alternate Phasing Operation.  
# Disable Phase Call For Loop During Alternate Phasing Operation.

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	04+8	01+5	01+6	04+8
11	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	R	Y
41, 42, 43	R	R	R	R	G	G	R	R
51	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	Y	Y
81, 82, 83	R	R	R	R	G	G	R	R

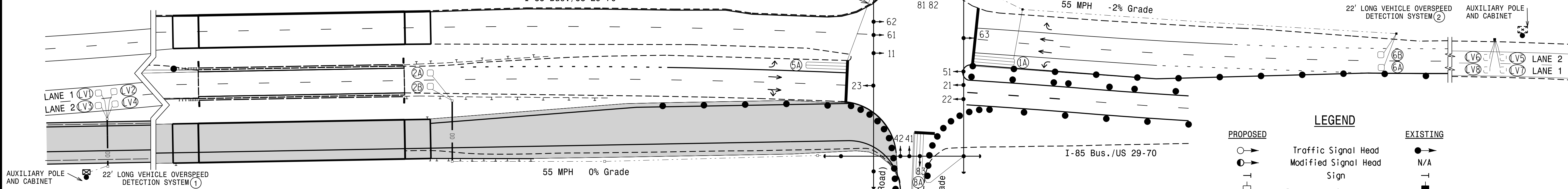
5 Phase Fully Actuated W/ Long Vehicle Detection (Isolated)

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Reposition existing signal heads 11, 12, 22, 23, 51, 61, 62, and 63.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.

PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT



LONG VEHICLE EXTENSION FAILURE PREEMPT

FUNCTION	PRE 7
Interval 1 - Green Clear	15
Interval 1 - Yellow Clear	0.0*
Interval 1 - Red Clear	0.0*
Interval 2 - Green Clear	10
Interval 2 - Yellow Clear	0.0*
Interval 2 - Red Clear	0.0*
Interval 3 - Dwell Green	255
Interval 3 - Dwell Yellow	0.0*
Interval 3 - Dwell Red	0.0*
Interval 4 - Exit Green	1
Interval 4 - Yellow	0.0
Interval 4 - Red	0.0
Exit Phase(s)	2+6
Priority	-
Delay Time	0
Min Green Before Pre	14
Ped Clear Before Pre	0
Yellow Clear Before Pre	0.0*
Red Clear Before Pre	0.0*
Dwell Min Time	14
Flash Dwell Interval?	Y
Enable Backup Protection	N
Ped Clear Through Yellow	N
Omit Overlaps	-

OASIS 2070 TIMING CHART

FEATURE	PHASE					
	1	2	4	5	6	8
Min Green 1 *	7	14	7	7	14	7
Extension 1 *	2.0	6.0	3.0	2.0	6.0	3.0
Max Green 1 *	20	120	25	25	120	25
Yellow Clearance	3.0	5.4	4.3	3.0	5.4	3.6
Red Clearance	2.8	1.2	2.1	3.2	1.2	2.5
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0
Walk 1 *	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-
Seconds Per Actuation *	-	1.5	-	-	1.5	-
Max Variable Initial *	-	46	-	-	46	-
Time Before Reduction *	-	20	-	-	20	-
Time To Reduce *	-	50	-	-	50	-
Minimum Gap	-	3.4	-	-	3.4	-
Recall Mode	-	MIN RECALL	-	-	MIN RECALL	-
Vehicle Call Memory	-	YELLOW	-	-	YELLOW	-
Dual Entry	-	-	ON	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LONG VEHICLE OVERSPEED DETECTION SYSTEM LOOP & DETECTION INSTALLATION CHART

LOOP NO.	SIZE (ft)	TURNS	DIST. FROM STOPBAR (ft)	NEW	EXISTING	L.V. DETECTION	CHANNEL	NEMA PHASE	TIMING		PLACE CALL DURING PHASE	INHIBIT DELAY DURING GREEN?	
									FEATURE	TIME			
LV1	6X6	4	1015	X	1	1	1	2*	NONE	- SEC.	ALL	NO	
LV2	6X6	4	999	X	1	1	2	2*	NONE	- SEC.	ALL	NO	
LV3	6X6	4	1015	X	2	2	1	2*	NONE	- SEC.	ALL	NO	
LV4	6X6	4	999	X	2	2	2	2*	NONE	- SEC.	ALL	NO	
LV5	6X6	4	1015	X	2	1	1	2	6*	NONE	- SEC.	ALL	NO
LV6	6X6	4	999	X	2	1	2	6*	NONE	- SEC.	ALL	NO	
LV7	6X6	4	1015	X	2	1	1	6*	NONE	- SEC.	ALL	NO	
LV8	6X6	4	999	X	2	2	2	6*	NONE	- SEC.	ALL	NO	
LVDS THRESHOLD SPEED (MPH)	55								2	6			
LVDS EXTEND TIME	12 SEC.								2	6			

\*Phase hold output to controller

- LEGEND
- PROPOSED Traffic Signal Head
  - PROPOSED Modified Signal Head Sign
  - PROPOSED Pedestrian Signal Head
  - PROPOSED Signal Pole with Guy
  - PROPOSED Signal Pole with Sidewalk Guy
  - PROPOSED Inductive Loop Detector
  - PROPOSED Controller & Cabinet
  - PROPOSED Junction Box
  - PROPOSED 2-in Underground Conduit
  - PROPOSED Right of Way
  - PROPOSED Directional Arrow
  - PROPOSED Guardrail
  - PROPOSED Directional Drill
  - PROPOSED Construction Zone Drums
  - PROPOSED Construction Zone
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A
  - EXISTING N/A

Signal Upgrade Temporary Design 2 (TMP Phase II)

Prepared in the Offices of:  
TRANSPORTATION MOBILITY AND SAFETY DIVISION  
STATE OF NORTH CAROLINA  
DEPARTMENT OF TRANSPORTATION  
Signal Design Section

750 N. Greenfield Pkwy, Garner, NC 27529

I-85 Bus. /US 29-70 at SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: January 2018 REVIEWED BY:

PREPARED BY: I. O. Umozurike REVIEWED BY:

REVISIONS

INIT. DATE

2/7/2018

SCALE 0 50 1"=50'

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

ROBERT J. ZEMBA

2/7/2018

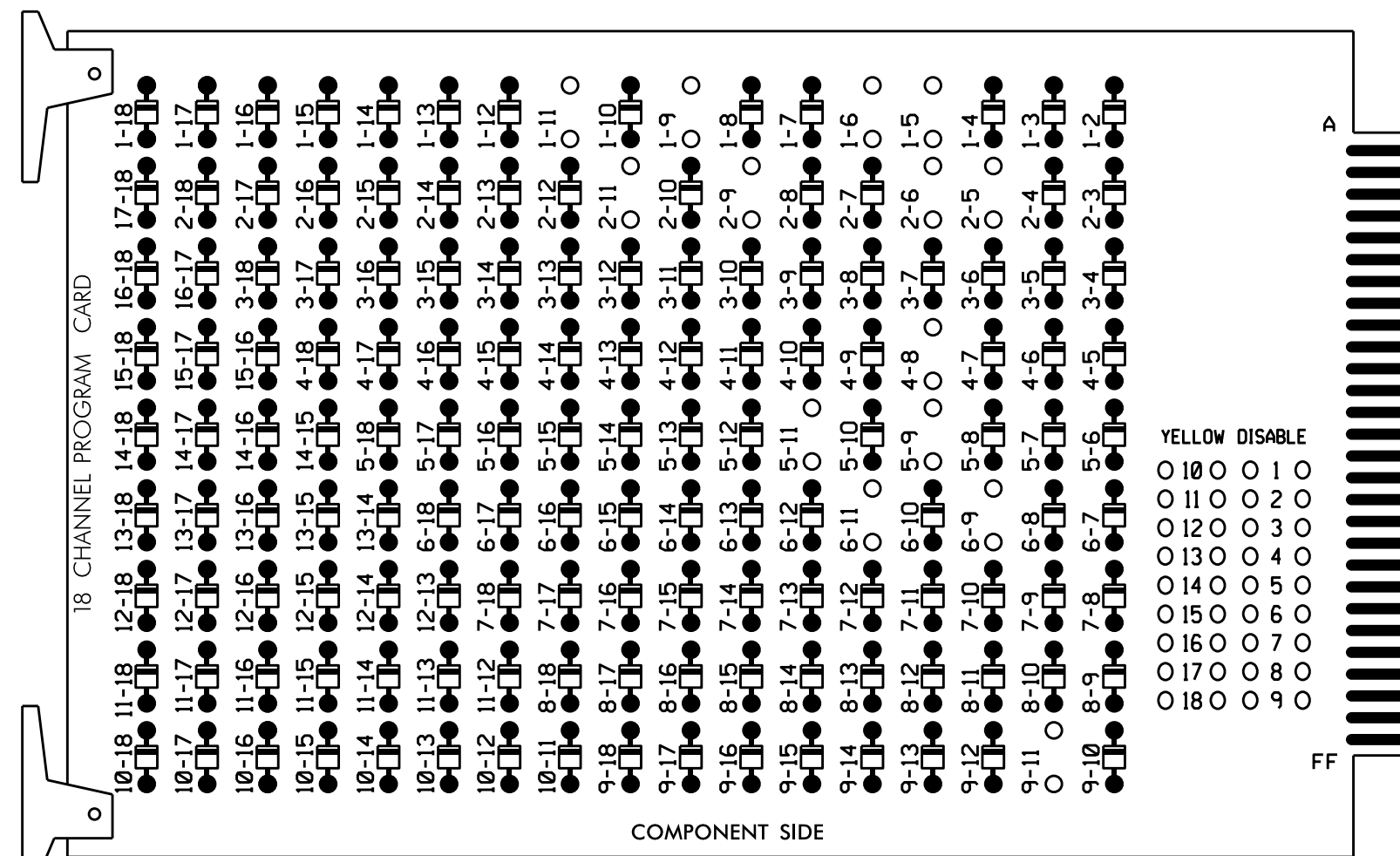
SIG. INVENTORY NO. 07-119112

07-FEB-2018 17:47 P:\IT\Projects\B-5351\Traffic\c4s\gnal\0407-1191\07119112.dwg:gnal:20180207.dgn

**EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

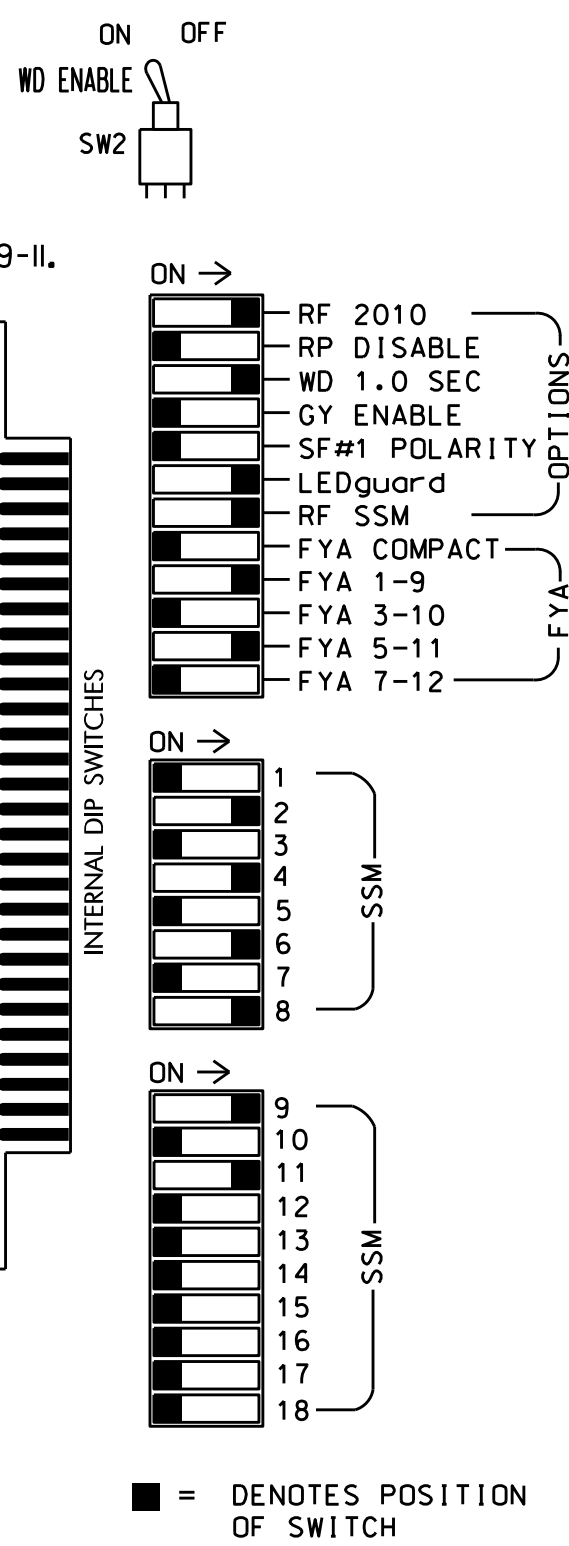
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUX S1,AUX S4  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22,23	NU	NU	41,42,43	NU	51	61,62,63	NU	NU	81,82,83	NU	11	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127							133										

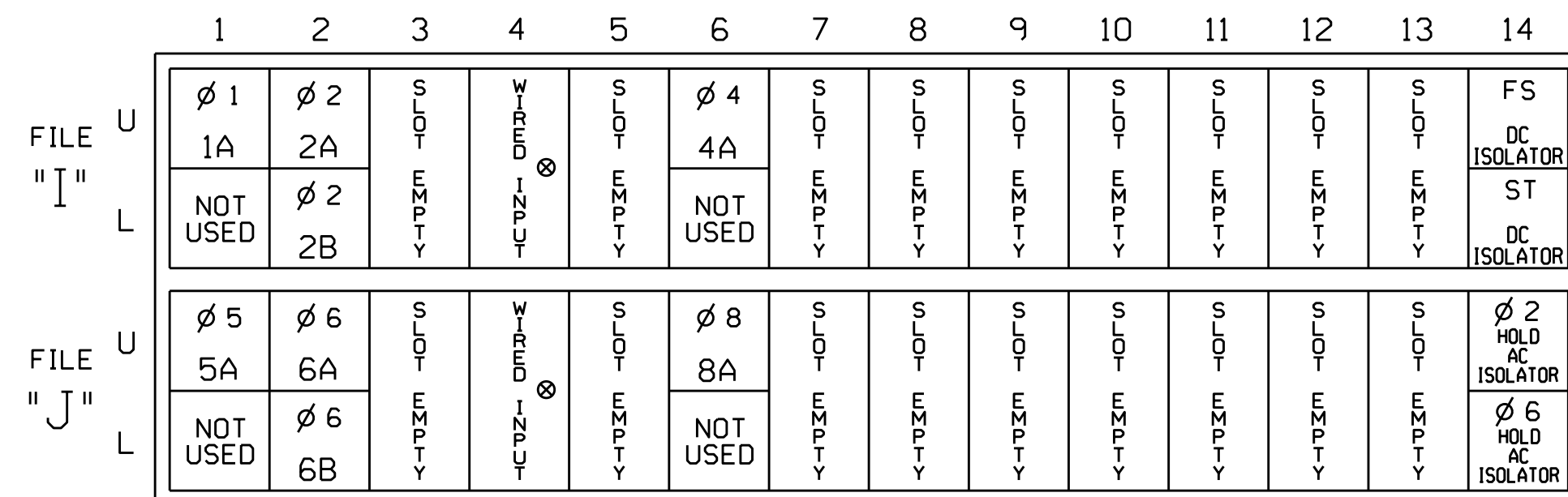
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

See AC Isolator programming detail on sheet 6.

**INPUT FILE CONNECTION & PROGRAMMING CHART**

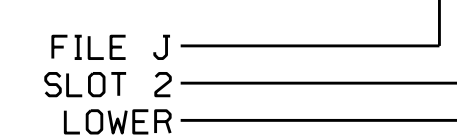
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9★	22	2	Y	Y	Y		3
	-	J1U	55	17★	55	5	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

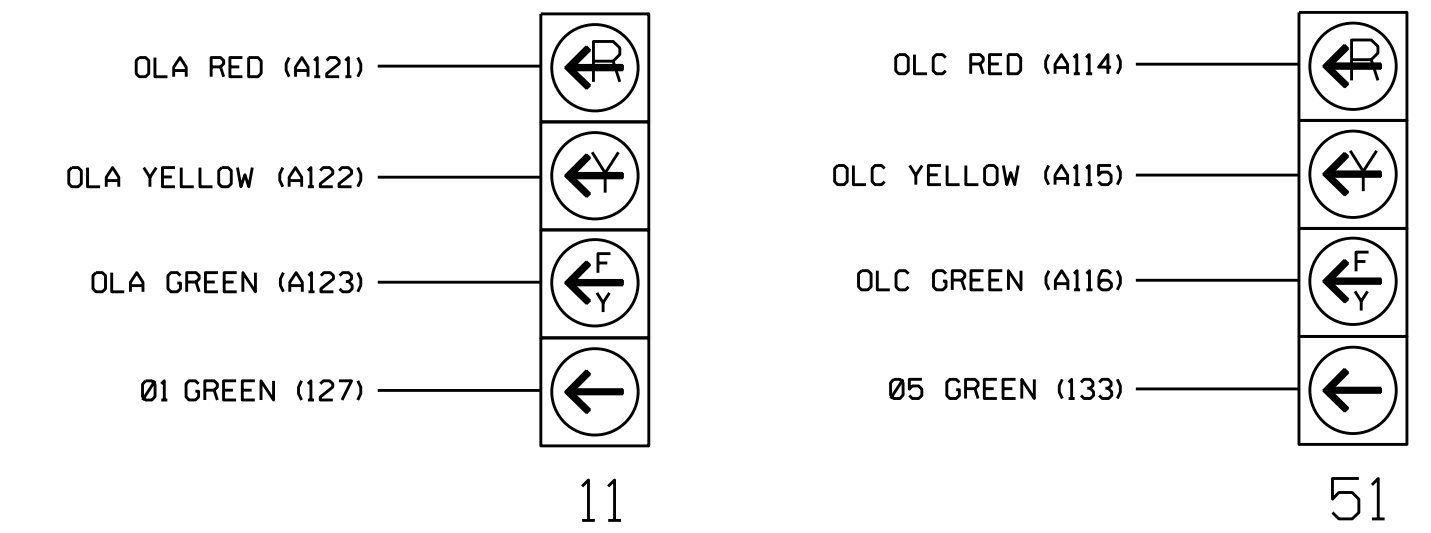
★ See Input Page Assignment programming details on sheets 3 and 4.

**INPUT FILE POSITION LEGEND:**



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

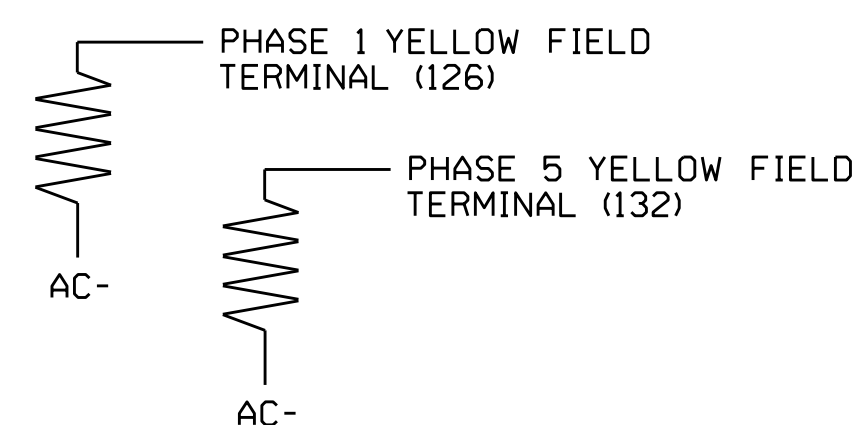
The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Temporary Design 2 (TMP Phase II)  
 Sheet 1 of 8

Electrical and Programming Details For: I-85 Bus. / US 29-70 at SR 1144 (River Road)

Prepared In the Offices of: Guilford County, North Carolina

Division 7, Guilford County, Jamestown

PLAN DATE: February 2018 REVIEWED BY: [Signature]

PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

REVISIONS: [Table]

DocuSigned by: Keith M. Mims 2/8/2018

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL: Keith M. Mims, Professional Engineer, No. 036880

SIG. INVENTORY NO. 07-1191T2

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SEQUENCE, AND TO FLASH INTERSECTION IF LVOD SYSTEMS FAIL**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, AND 7.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)  
IF INPUT ASSIGNMENT #13 IS ON  
OR INPUT ASSIGNMENT #14 IS ON

↓  
SCROLL DOWN

THEN:  
DELAY FOR 240.0 SECONDS  
SET INPUT ASSIGNMENT #64 ON

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**I/O REFERENCE SCHEDULE**

INPUT 13 = Input from LVODS #1  
INPUT 14 = Input from LVODS #2  
INPUT 64 = Preempt 7  
OUTPUT 42 = Overlap C Red  
OUTPUT 43 = Overlap C Yellow  
OUTPUT 44 = Overlap C Green  
OUTPUT 50 = Overlap A Red  
OUTPUT 51 = Overlap A Yellow  
OUTPUT 52 = Overlap A Green

INPUTS 13, 14, AND 64 HAVE BEEN REASSIGNED. SEE SHEET 6 FOR PROGRAMMING DETAILS.

**OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS), PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

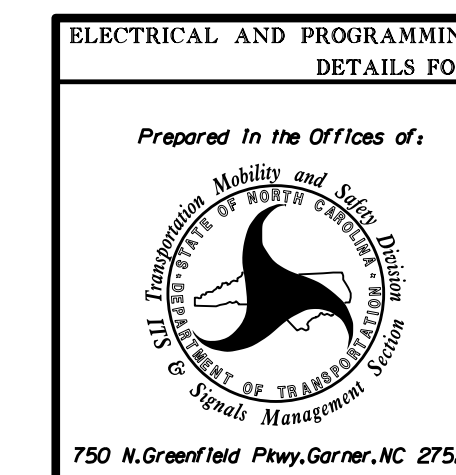
NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 2 (TMP Phase II)  
Sheet 2 of 8



I-85 Bus. / US 29-70 at SR 1144 (River Road)	
Division 7	Guilford County
PLAN DATE: February 2018	REVIEWED BY:
PREPARED BY: S. Armstrong	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



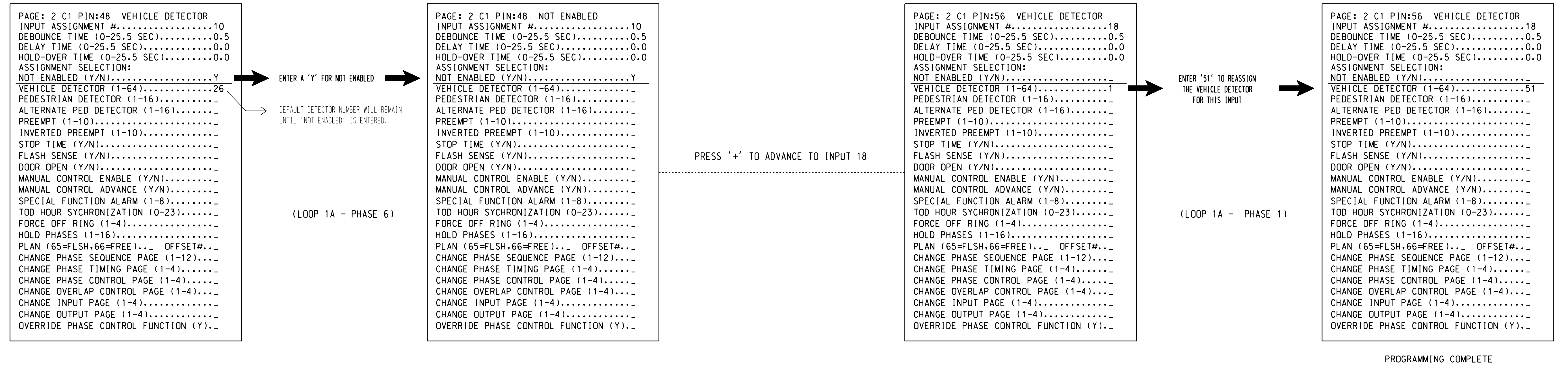
08-FEB-2018 14:26 C:\MITSAS\15-Signal\work\hgr\oups\sig\Map\harmstrong071191\_sml.elec.xxx.dgn sarmstrong

**INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A**

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

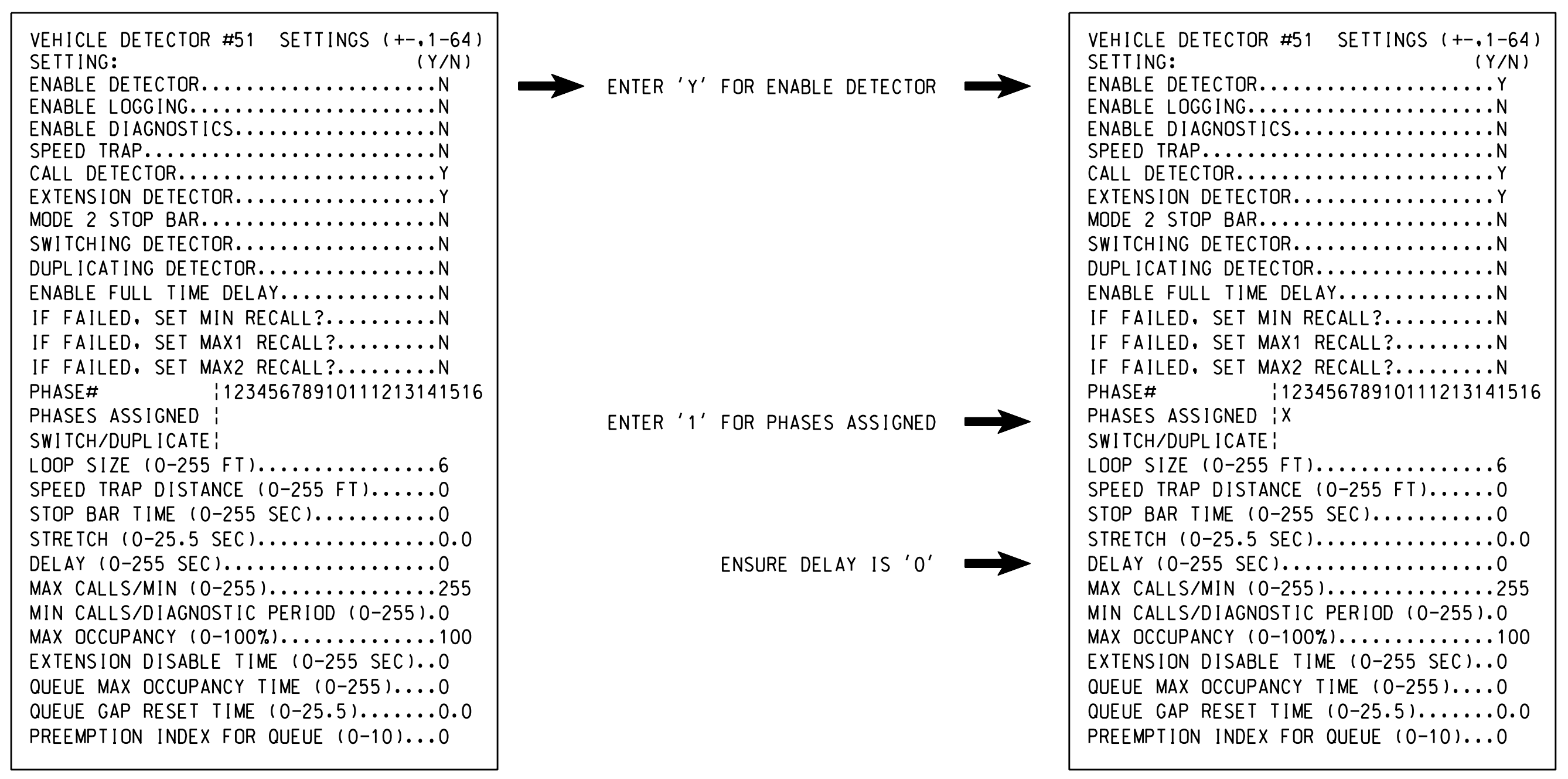
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.



**SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)**

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

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Electrical Detail - Temporary Design 2 (TMP Phase II)  
Sheet 3 of 8

	I-85 Bus. / US 29-70 at SR 1144 (River Road)		
	Prepared In the Offices of: S. Armstrong 750 N. Greenfield Pkwy, Garner, NC 27529	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	

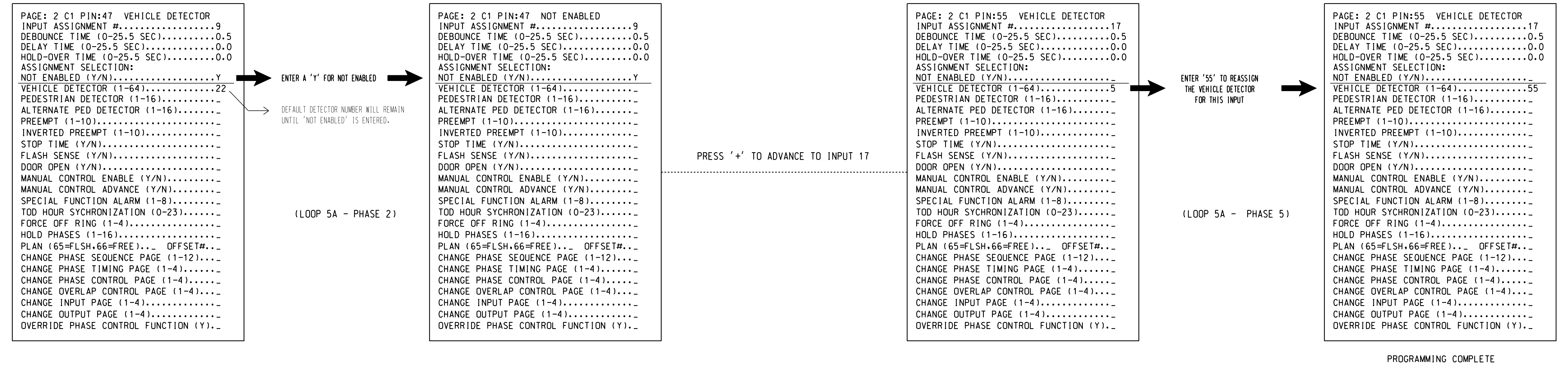
DocuSigned by:  
Keith M. Mims 2/8/2018  
2F8078E8E5CD3405 DATE  
SIG. INVENTORY NO. 07-1191T2

INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 5A

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #9 (DETECTOR 22) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 2 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 55 TO INPUT #17 SO THAT THE DELAY ON LOOP 5A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

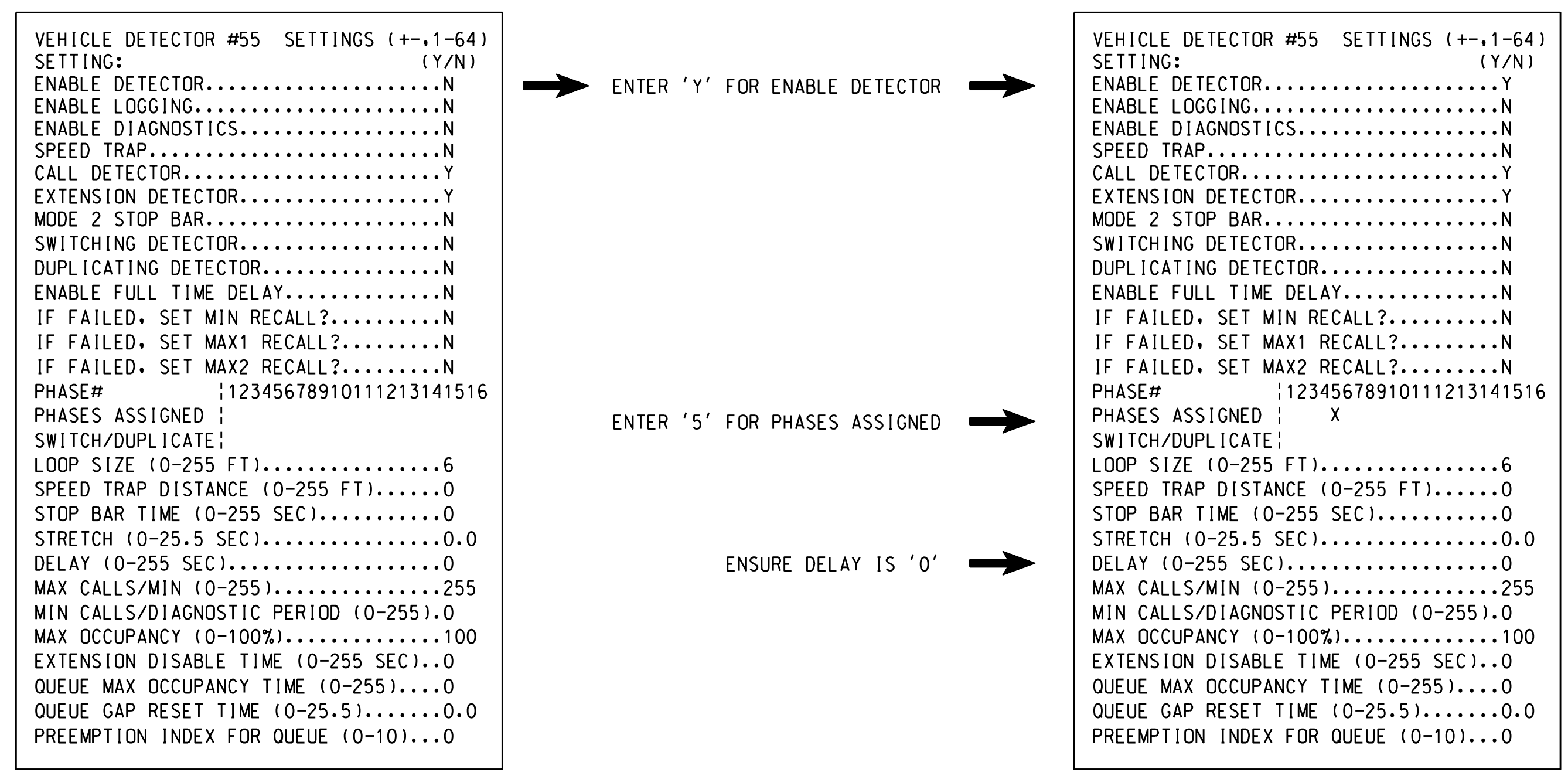
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 9 IS REACHED.



SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 5A (ALT.)

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #55.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

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Electrical Detail - Temporary Design 2 (TMP Phase II)  
Sheet 4 of 8

I-85 Bus. / US 29-70  
at  
SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY: [Signature]  
PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

REVISIONS: [Table with columns for REVISIONS, INIT., DATE]

DocuSigned by: Keith M. Mims 2/8/2018

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
KEITH M. MIMS  
036880

SIG. INVENTORY NO. 07-1191T2



## ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

<u>PHASING</u>	<u>INPUTS PAGE</u>	<u>OVERLAPS PAGE</u>
ACTIVE PAGES REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	1
ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	2

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

### ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAPS PAGE 2: Modifies overlap parent phases for heads 11 and 51 to run protected turns only.

INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 2 (TMP Phase II)  
Sheet 5 of 8

<p>ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="text-align: center;"><small>Prepared In the Offices of:</small></p> <p style="text-align: center;"><small>750 N. Greenfield Pkwy, Garner, NC 27529</small></p>	<p><b>I-85 Bus. / US 29-70</b> at <b>SR 1144 (River Road)</b></p> <p>Division 7      Guilford County      Jamestown</p> <p>PLAN DATE: February 2018      REVIEWED BY:</p> <p>PREPARED BY: S. Armstrong      REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> </tbody> </table>	REVISIONS	INIT.	DATE										<p style="text-align: center;"><b>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</b></p> <p style="text-align: center;">SEAL</p> <p style="text-align: center;"><small>DocuSigned by:</small> <b>Keith M. Mins</b>      2/8/2018</p> <p style="text-align: center;"><small>2F8078E6CD3445</small>      DATE</p> <p style="text-align: center;"><small>SIG. INVENTORY NO. 07-1191T2</small></p>
REVISIONS	INIT.	DATE												

**INPUT ASSIGNMENT PROGRAMMING DETAIL TO REASSIGN LONG VEHICLE OVERSPEED DETECTION SYSTEM FUNCTION**  
(program controller as shown below)

This programming takes each of the Long Vehicle Overspeed Detection System inputs and reassigns a unique Hold Phase to it.

FROM MAIN MENU PRESS '5' (INPUTS), THEN '+' UNTIL PIN 51 (INPUT 13) IS REACHED.

```

PAGE: 1 C1 PIN:51 HOLD PHASES
INPUT ASSIGNMENT #.....13
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....2
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+'

INPUT 13 IS THE OUTPUT FROM LVODS #1

```

PAGE: 1 C1 PIN:52 HOLD PHASES
INPUT ASSIGNMENT #.....14
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....6
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+' OR '-' TO REACH INPUT 64

INPUT 14 IS THE OUTPUT FROM LVODS #2

```

PAGE: 1 C1 PIN:0 PREEMPT
INPUT ASSIGNMENT #.....64
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....7
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PROGRAMMING COMPLETE

← Notice Preempt 7

**PREEMPT 7 PROGRAMMING DETAIL**  
(program controller as shown below)

THIS PREEMPT GOES ACTIVE IF EITHER LVODS HAS BEEN ACTIVE FOR MORE THAN 4 CONSECUTIVE MINUTES AND WILL PUT THE INTERSECTION IN FLASH.

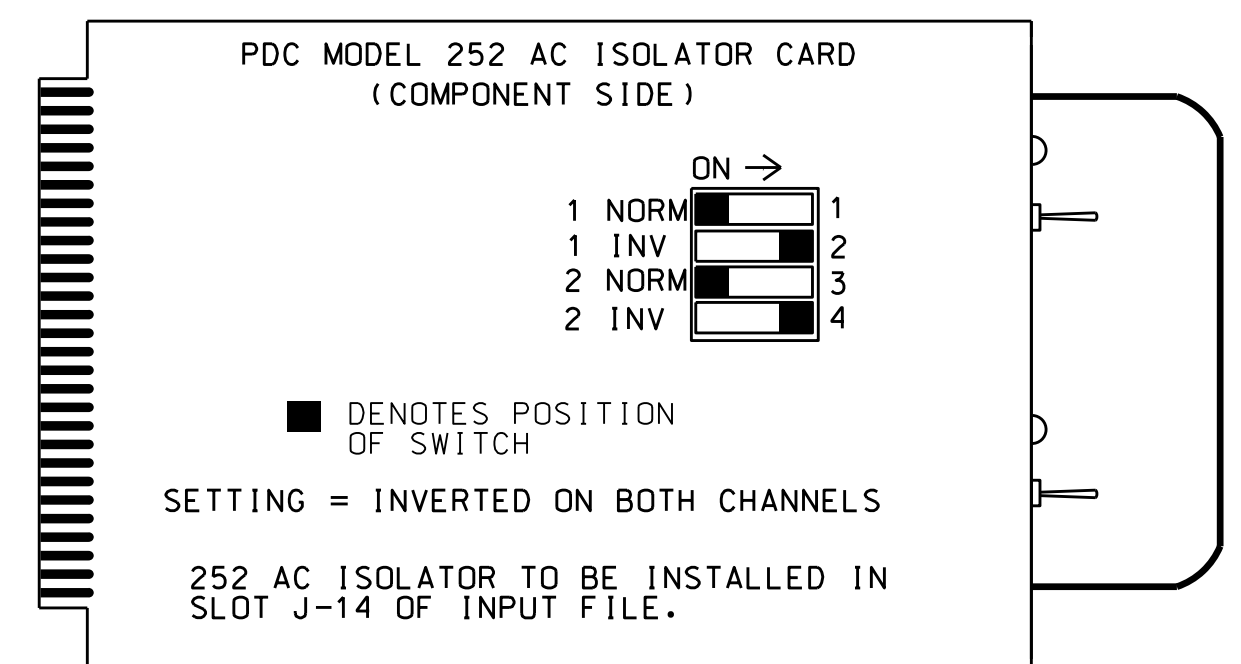
FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTIONS), PRESS 'NEXT' UNTIL PREEMPTION #7 IS REACHED.

PREEMPTION #7	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 15 0.0 0.0	X X
2 10 0.0 0.0	X X
3 255 0.0 0.0	X X
4 0 0.0 0.0	X X
5 1 0.0 0.0	X X

EXIT CALLS	OPTIONS
PRIORITY (Y/N TO SELECT)	.....HIGH
DELAY TIMER (0-255 SEC)	.....0.0
MIN GREEN BEFORE PRE (0= DEFAULT)	.....14
PED CLEAR BEFORE PRE (0= DEFAULT)	.....0
YELLOW CLEAR BEFORE PRE (0= DEFAULT)	.....0.0
RED CLEAR BEFORE PRE (0= DEFAULT)	.....0.0
DWELL MIN TIMER (0-255 SEC)	.....14
DWELL MAX TIMER (0=OFF,1-255MIN)	.....0
DWELL HOLD-OVER TIMER (0-255)	.....0
LATCH CALL?	.....N
LINK TO NEXT PREEMPT?	.....N
ENABLE BACKUP PROTECTION?	.....N
HOLD CLEAR 1 PHASES DURING DELAY?	.....N
FAST GREEN FLASH DWELL PHASES?	.....N
PED CLEARANCE THROUGH YELLOW?	.....N
INHIBIT OVERLAP GREEN EXTENSION?	.....N
SERVICE DURING SOFTWARE FLASH?	.....N
REST IN RED DURING DWELL INTERVAL?	.....N
FLASH DWELL INTERVAL?	.....Y
ALLOW PEDS IN DWELL INTERVAL?	.....Y
RE-TIME DWELL INTERVAL?	.....Y
OVERLAPS:	ABCDEFGHIJKLMNPO
DWELL INT FLASH YELLOW	X X
OMIT OVERLAPS:	

**HOLD PHASE AC ISOLATOR (MODEL 252)**  
**OUTPUT PROGRAMMING DETAIL**  
(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 2 (TMP Phase II)  
Sheet 6 of 8

Prepared In the Offices of:  
Gulf Transportation Mobility and Safety Division  
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION  
Signal Management Section  
750 N. Greenfield Pkwy, Garner, NC 27529

I-85 Bus. / US 29-70  
at  
SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY:

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSigned by:  
Keith M. Mims 2/8/2018  
278079E8C03445 DATE

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
KEITH M. MIMS  
036880

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

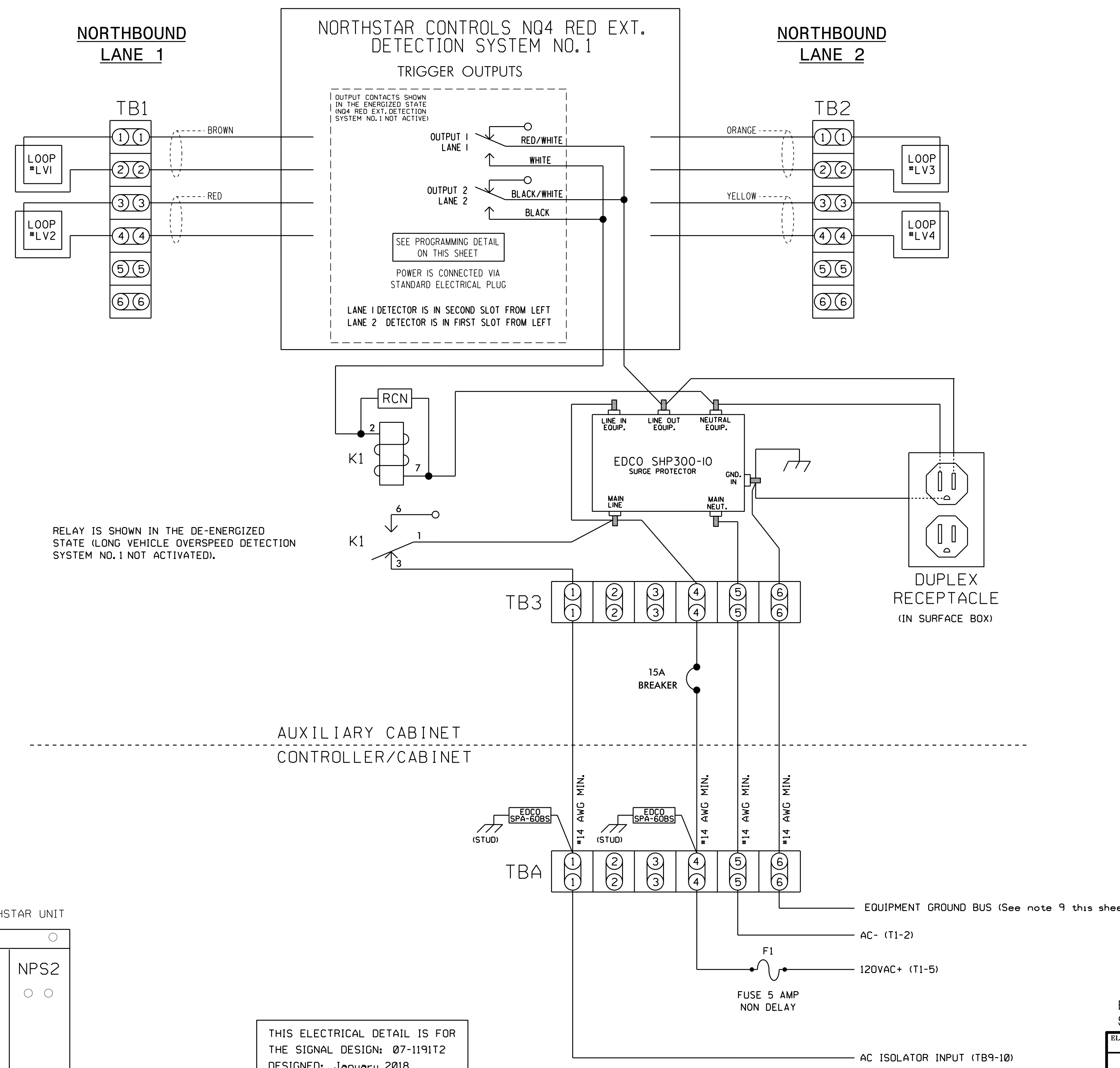
SIG. INVENTORY NO. 07-1191T2

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(wire unit as shown below)

NOTES

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBA to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.
11. IMPORTANT! A jumper must be installed between Input File terminals J14-E and J14-K.
12. IMPORTANT! For proper operation of the Dynamic Red Extension System, tie TB9-12 to AC neutral.
13. IMPORTANT! Make sure both channels of the AC Isolator card inserted in Input File slot J14 are set for INVERTED OUTPUT operation. See sheet 6 of this electrical detail.



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO.1 NOT ACTIVATED).

NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL

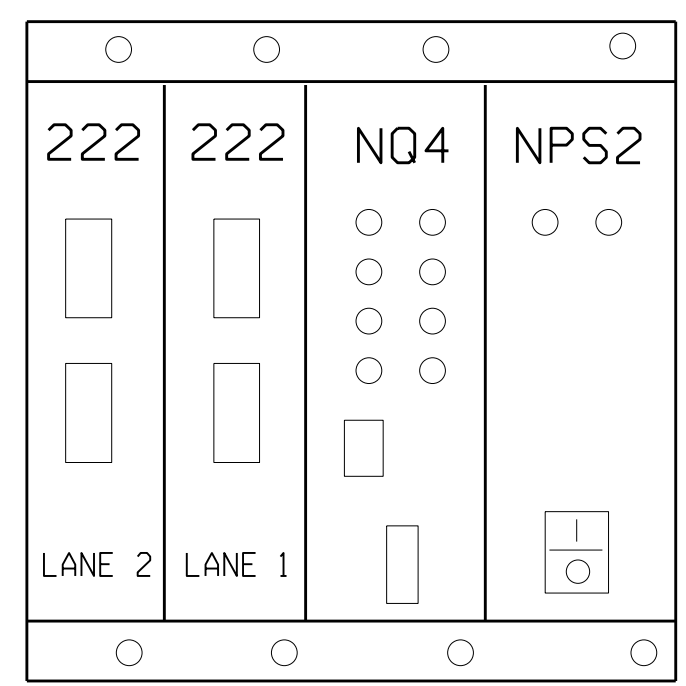
(program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

Program the NQ4 by typing the following commands:

1. SET SPEED = 55
2. SET LENGTH = 22'
3. SET ALARMTIME = 12
4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
5. SET LOOP LENGTH = 6' (program actual measured loop length)
6. SAVE

FRONT VIEW OF NORTHSTAR UNIT



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

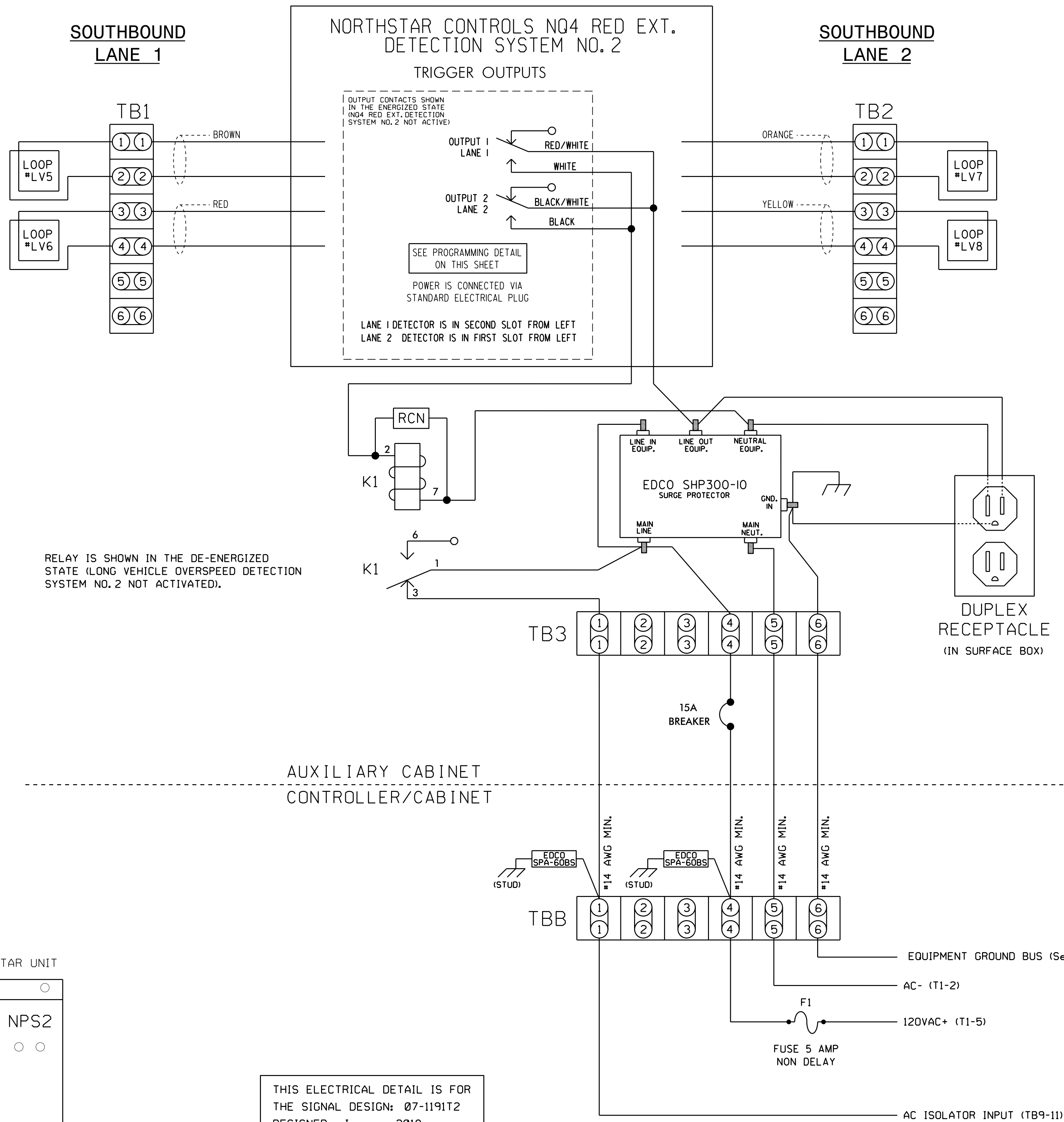
Electrical Detail - Temporary Design 2 (TMP Phase II)  
 Sheet 7 of 8

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  KEITH M. MINS ENGINEER 036880
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

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 sarmstrong

NOTES

(wire unit as shown below)



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 2 NOT ACTIVATED).

OUTPUT CONTACTS SHOWN IN THE ENERGIZED STATE (NQ4 RED EXT. DETECTION SYSTEM NO. 2 NOT ACTIVE)

SEE PROGRAMMING DETAIL ON THIS SHEET

POWER IS CONNECTED VIA STANDARD ELECTRICAL PLUG

LANE 1 DETECTOR IS IN SECOND SLOT FROM LEFT  
LANE 2 DETECTOR IS IN FIRST SLOT FROM LEFT

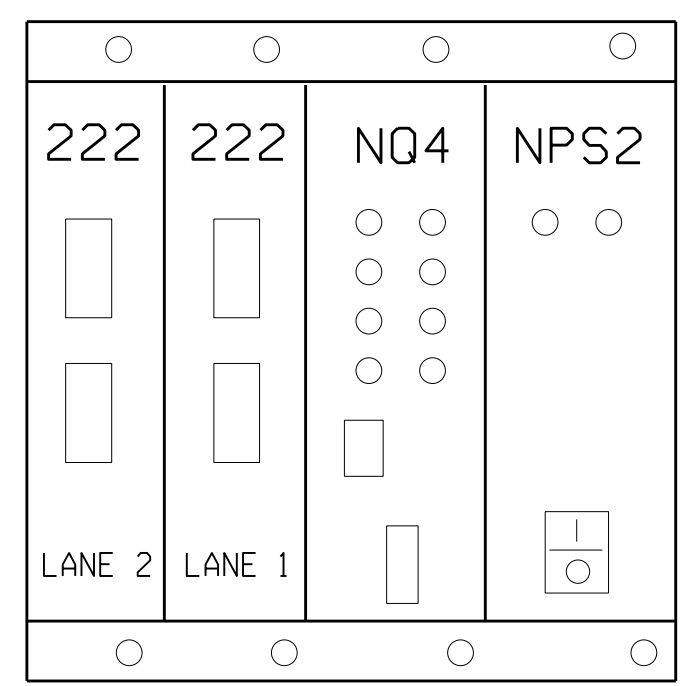
1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBB to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.

NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

- Program the NQ4 by typing the following commands:
1. SET SPEED = 55
  2. SET LENGTH = 22'
  3. SET ALARMTIME = 12
  4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
  5. SET LOOP LENGTH = 6' (program actual measured loop length)
  6. SAVE

FRONT VIEW OF NORTHSTAR UNIT



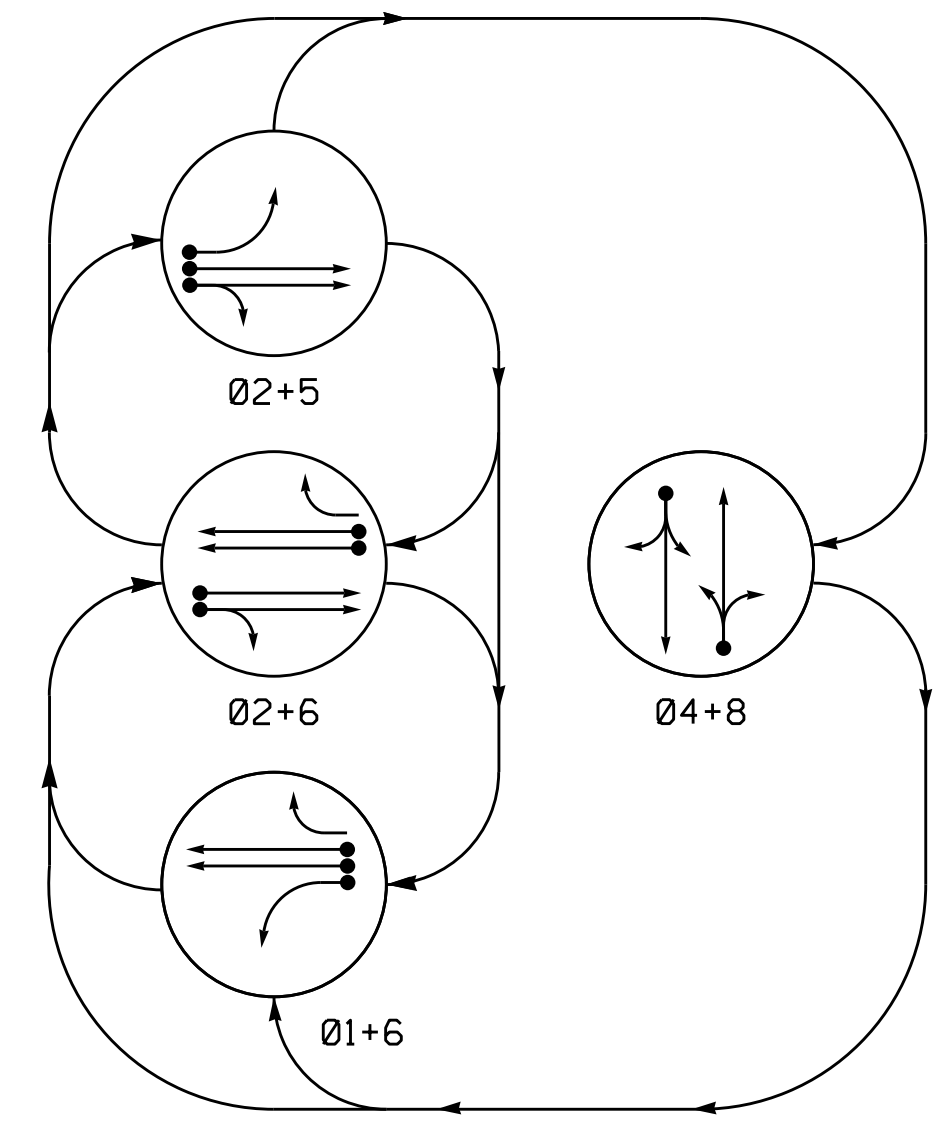
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T2  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 2 (TMP Phase II)  
Sheet 8 of 8

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  KEITH M. MINS ENGINEER 2/8/2018
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	
REVISIONS INIT. DATE			DocuSigned by: Keith M. Mins 2/8/2018 DATE:

08-FEB-2018 11:36  
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 sarmstrong

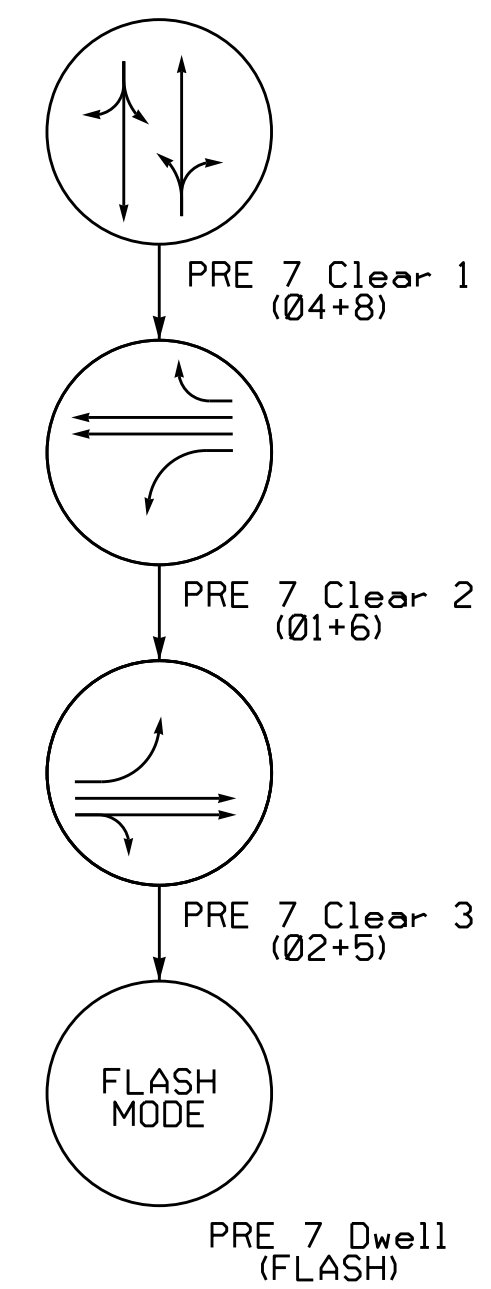
**PHASING DIAGRAM**



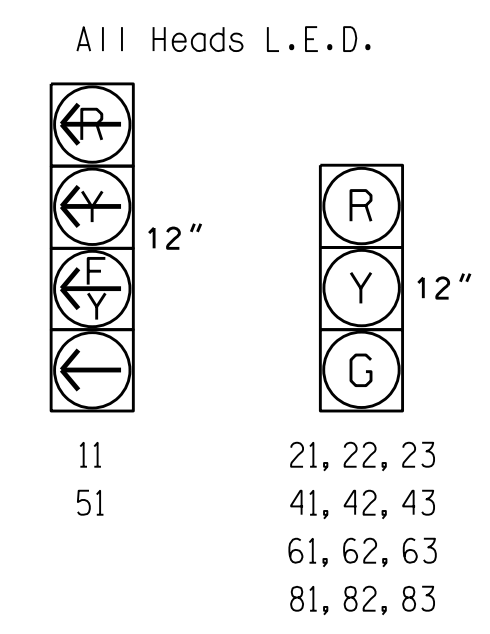
**PHASING DIAGRAM DETECTION LEGEND**

- ← DETECTED MOVEMENT
- ← UNDETECTED MOVEMENT (OVERLAP)
- ← UNSIGNALIZED MOVEMENT
- - - ← PEDESTRIAN MOVEMENT

**LONG VEHICLE EXTENSION FAILURE PREEMPT PHASES**



**SIGNAL FACE I.D.**



**TABLE OF OPERATION**

SIGNAL FACE	PHASE										
	01+6	02+6	04+8	07+1	07+2	07+3	07+4	07+5	07+6	07+7	07+8
11	-	R	R	R	R	R	R	R	R	R	R
21, 22, 23	R	G	R	R	R	R	R	R	R	R	R
41, 42, 43	R	R	G	G	R	R	R	R	R	R	R
51	-	R	R	R	R	R	R	R	R	R	R
61, 62, 63	G	R	G	R	R	R	R	R	R	R	R
81, 82, 83	R	R	R	G	G	R	R	R	R	R	R

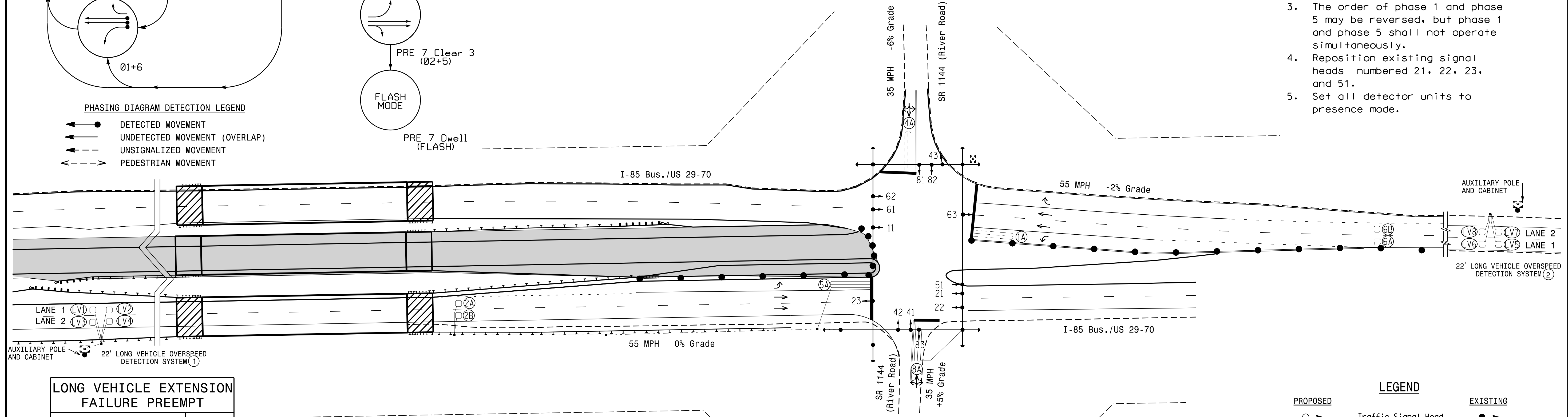
**OASIS 2070 LOOP & DETECTOR INSTALLATION CHART**

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	DETECTOR PROGRAMMING						
				NEW LOOP	PHASE	CALLING EXTENSION	STRETCH TIME	DELAY TIME	SYSTEM LOOP NEW CARD	
1A	6X40	0	2-4-2	-	1	Y	Y	-	-	-
2A	6X6	400	5	Y	2	Y	Y	-	-	-
2B	6X6	400	5	Y	2	Y	Y	-	-	-
4A	6X40	0	2-4-2	-	4	Y	Y	-	10	-
5A	6X40	0	2-4-2	Y	5	Y	Y	-	-	-
6A	6X6	420	5	-	6	Y	Y	-	-	-
6B	6X6	420	5	-	6	Y	Y	-	-	-
8A	6X40	0	2-4-2	Y	8	Y	Y	-	10	-

4 Phase Fully Actuated W/ Long Vehicle Detection (Isolated)

**NOTES**

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- The order of phase 1 and phase 5 may be reversed, but phase 1 and phase 5 shall not operate simultaneously.
- Reposition existing signal heads numbered 21, 22, 23, and 51.
- Set all detector units to presence mode.



**LONG VEHICLE EXTENSION FAILURE PREEMPT**

FUNCTION	PRE 7
Interval 1 - Green Clear	15
Interval 1 - Yellow Clear	0.0*
Interval 1 - Red Clear	0.0*
Interval 2 - Green Clear	10
Interval 2 - Yellow Clear	0.0*
Interval 2 - Red Clear	0.0*
Interval 3 - Green Clear	15
Interval 3 - Yellow Clear	0.0*
Interval 3 - Red Clear	0.0*
Interval 4 - Dwell Green	255
Interval 4 - Dwell Yellow	0.0*
Interval 4 - Dwell Red	0.0*
Interval 5 - Exit Green	1
Interval 5 - Yellow	0.0
Interval 5 - Red	0.0
Exit Phase(s)	2+6
Priority	-
Delay Time	0
Min Green Before Pre	14
Ped Clear Before Pre	0
Yellow Clear Before Pre	0.0*
Red Clear Before Pre	0.0*
Dwell Min Time	14
Flash Dwell Interval?	Y
Enable Backup Protection	N
Ped Clear Through Yellow	N
Omit Overlaps	-

\* Time defaults to time used for phase during normal operation

**OASIS 2070 TIMING CHART**

FEATURE	PHASE							
	1	2	4	5	6	8		
Min Green 1 *	7	14	7	7	14	7		
Extension 1 *	2.0	6.0	3.0	2.0	6.0	3.0		
Max Variable Initial *	20	120	25	25	120	25		
Yellow Clearance	3.0	5.2	4.3	3.0	5.4	3.6		
Red Clearance	3.3	1.0	2.8	3.5	1.0	2.7		
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0		
Walk 1 *	-	-	-	-	-	-		
Don't Walk 1	-	-	-	-	-	-		
Seconds Per Actuation *	-	1.5	-	-	1.5	-		
Max Variable Initial *	-	46	-	-	46	-		
Time Before Reduction *	-	20	-	-	20	-		
Time To Reduce *	-	50	-	-	50	-		
Minimum Gap	-	3.4	-	-	3.4	-		
Recall Mode	-	MIN RECALL	-	-	MIN RECALL	-		
Vehicle Call Memory	-	YELLOW	-	-	YELLOW	-		
Dual Entry	-	-	ON	-	-	ON		
Simultaneous Gap	ON	ON	ON	ON	ON	ON		

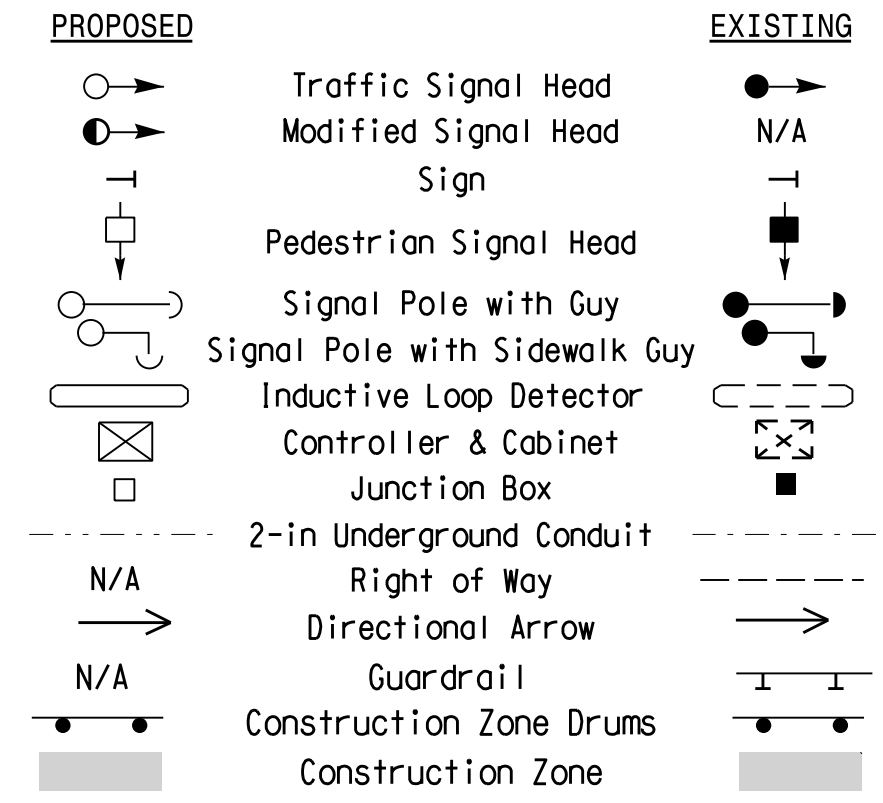
\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

**LONG VEHICLE OVERSPEED DETECTION SYSTEM LOOP & DETECTION INSTALLATION CHART**

LOOP NO.	SIZE (ft)	TURNS	DIST. FROM STOPBAR (ft)	NEW EXISTING	DETECTOR UNITS						
					TIME	PLACE CALL DURING PHASE	INHIBIT DELAY DURING GREEN?				
LV1	6X6	4	1015	X	1	1	2	NONE	- SEC.	ALL	NO
LV2	6X6	4	999	X	1	1	2	NONE	- SEC.	ALL	NO
LV3	6X6	4	1015	X	2	2	2	NONE	- SEC.	ALL	NO
LV4	6X6	4	999	X	2	2	2	NONE	- SEC.	ALL	NO
LV5	6X6	4	1015	X	1	1	1	NONE	- SEC.	ALL	NO
LV6	6X6	4	999	X	1	1	2	NONE	- SEC.	ALL	NO
LV7	6X6	4	1015	X	2	2	1	NONE	- SEC.	ALL	NO
LV8	6X6	4	999	X	2	2	2	NONE	- SEC.	ALL	NO
LVODS THRESHOLD SPEED (MPH)					55		2				
LVODS EXTEND TIME					12 sec.		2				

\*Phase hold output to controller

**LEGEND**



Signal Upgrade Temporary Design 3 (TMP Phase III)

750 N. Greenfield Pkwy, Garner, NC 27529

**I-85 Bus./US 29-70 at SR 1144 (River Road)**

Division 7 Guilford County Jamestown

PLAN DATE: January 2018 REVIEWED BY:

PREPARED BY: I. O. Umozurike REVIEWED BY:

SCALE: 1" = 50'

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

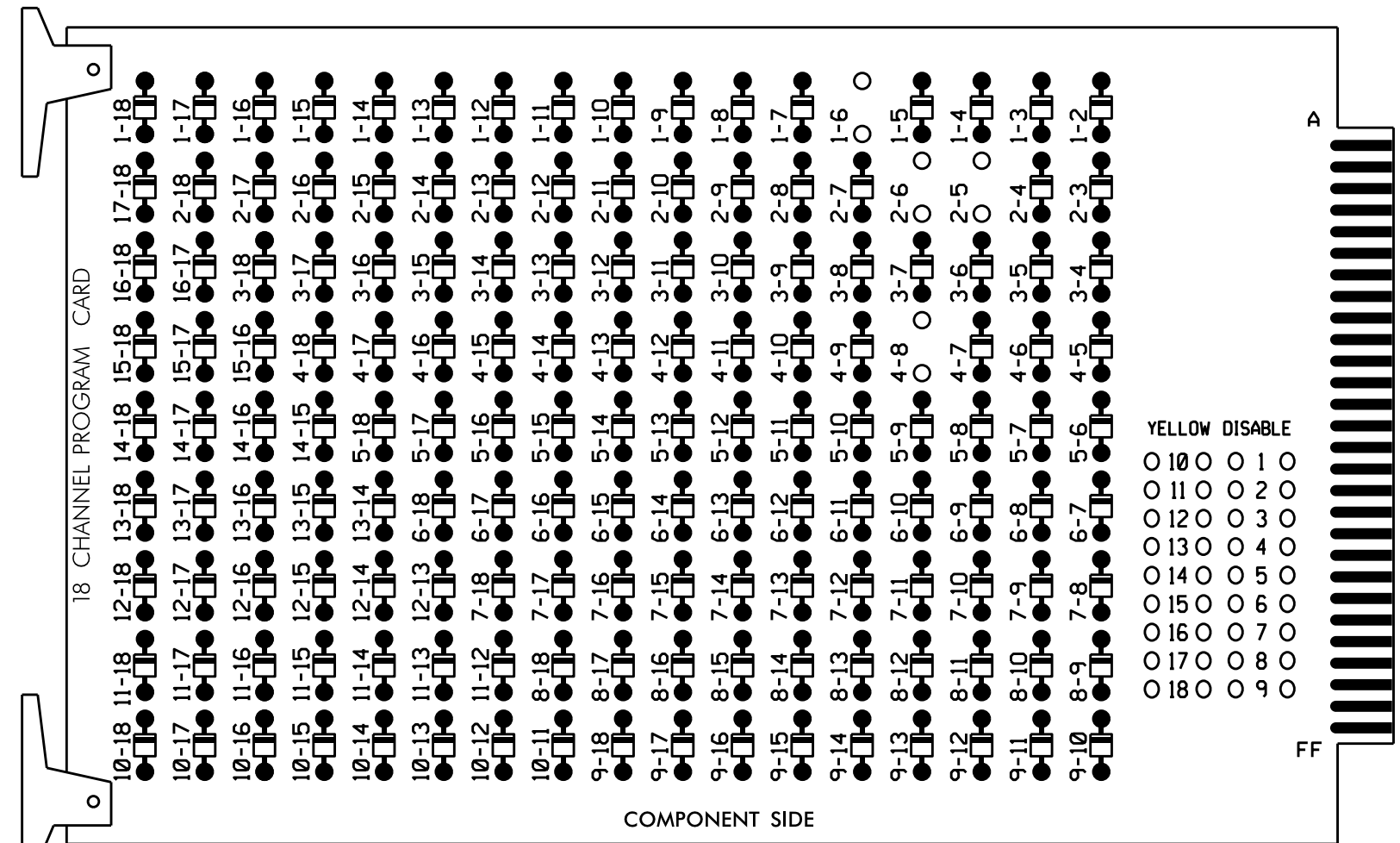
2/7/2018

07-FEB-2018 18:28 P:\IT\PROJ\act-5-945351\Traffic\c45\gnal\407-1191\407119113\_1.dgn 20180207.dgn

**EDI MODEL 2018ECL-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

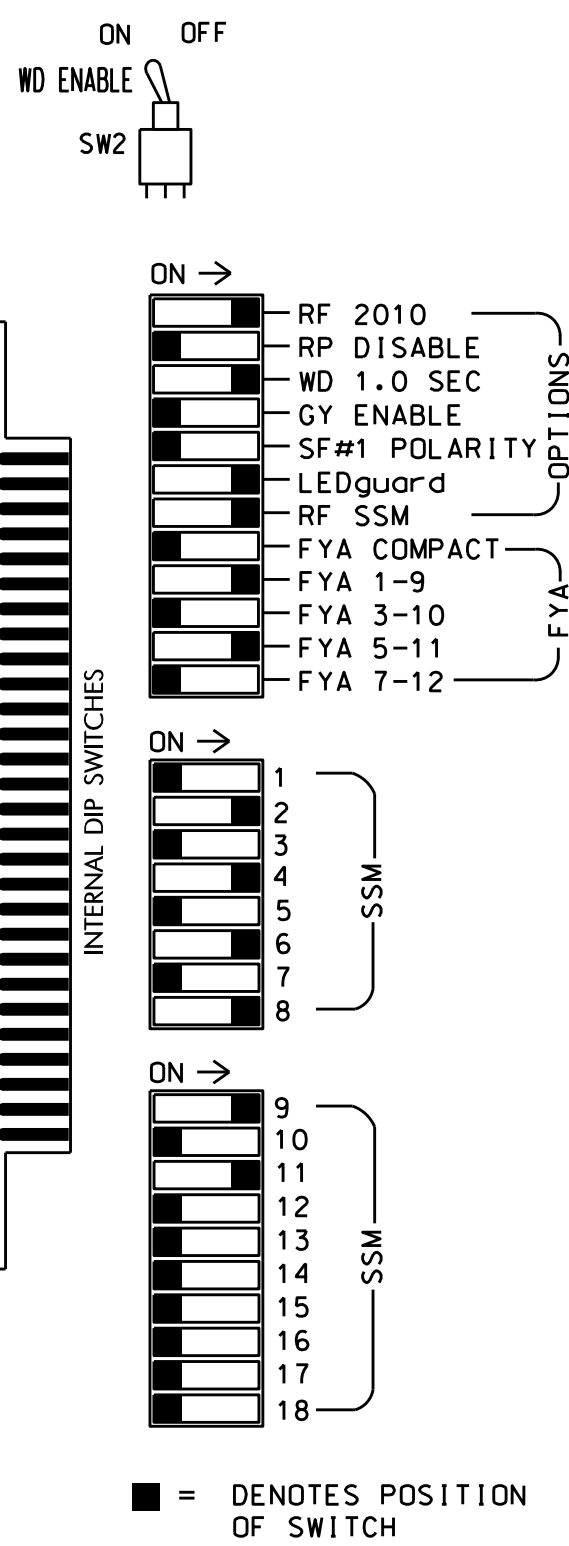
REMOVE DIODE JUMPERS 1-6, 2-5, 2-6, and 4-8.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Return controller to Factory Defaults before programming per this electrical detail.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUX S1,AUX S4  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAP "A".....1  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22,23	NU	NU	41,42,43	NU	51	61,62,63	NU	NU	81,82,83	NU	11	NU	NU	51	NU	NU	
RED		128			101			134			107								
YELLOW	*	129			102		*	135			108								
GREEN		130			103			136			109								
RED ARROW													A121				A114		
YELLOW ARROW														A122				A115	
FLASHING YELLOW ARROW															A123				A116
GREEN ARROW	127							133											

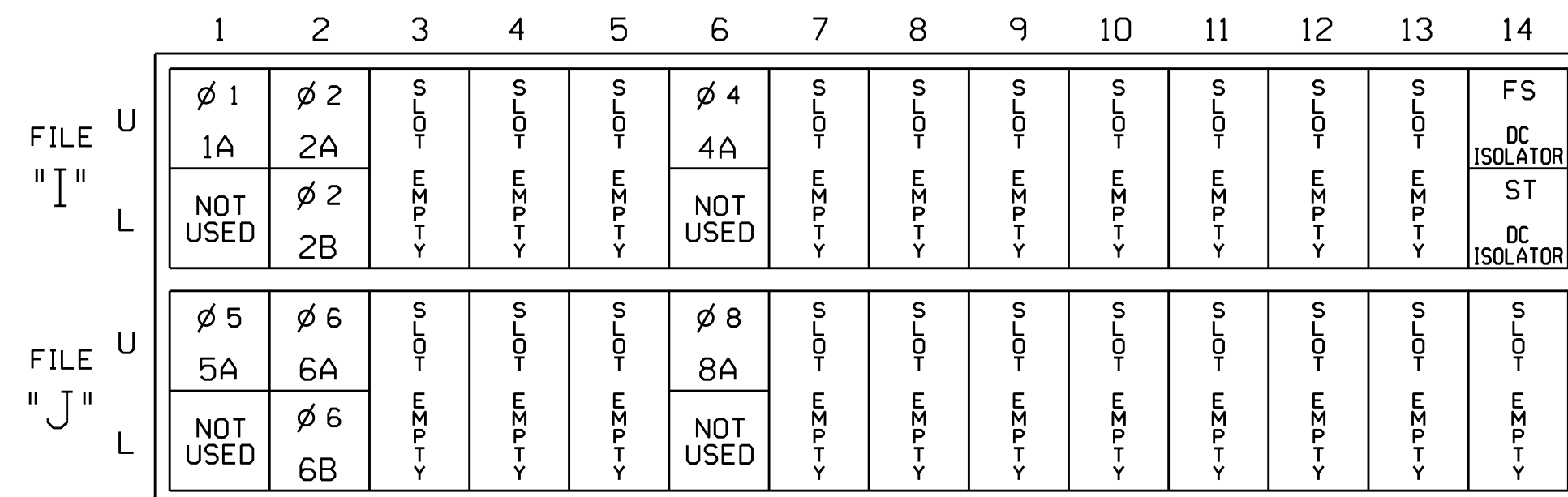
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

See AC Isolator programming detail on sheet 3.

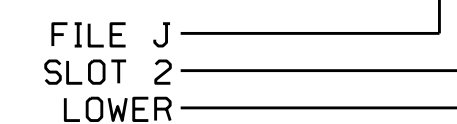
**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

Remove the following wires, if present:

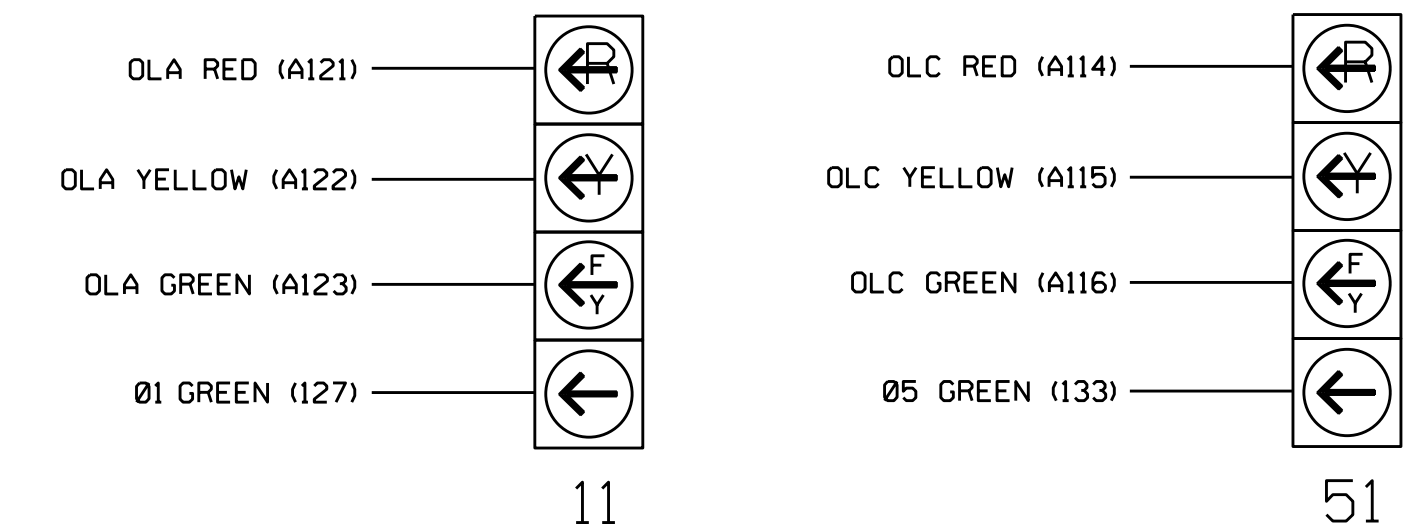
- From I1-W to J4-W, on rear of input file.
- From J1-W to I4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



**FYA SIGNAL WIRING DETAIL**

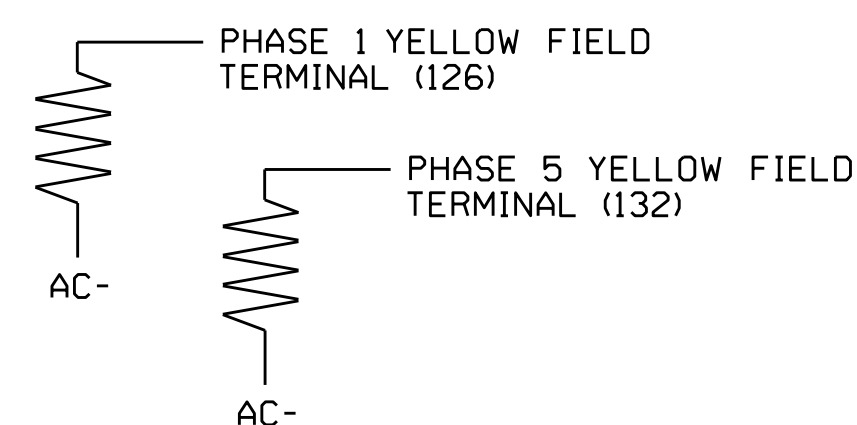
(wire signal heads as shown)



**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



**PHASE SEQUENCE PROGRAMMING DETAIL**

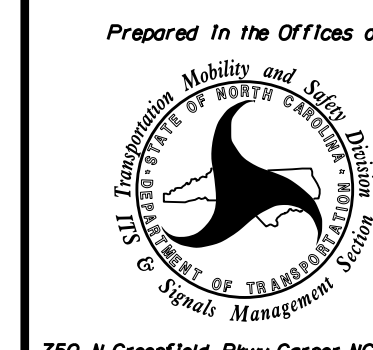
(program controller as shown below)

FROM OASIS LOCAL CONTROLLER MAIN MENU  
 SELECT: 4 PHASE SEQUENCE

PHASE SEQUENCE: PAGE 1		NEXT: PAGES		
RNG:LEAD	BARRIER 1	X-LAG:LEAD	BARRIER 2	X-LAG
1   1	2   0	0   0	4   0	0   0
2   0	6   0	5   0	8   0	0   0
3   0	0   0	0   0	0   0	0   0
4   0	0   0	0   0	0   0	0   0

Electrical Detail - Temporary Design 3 (TMP Phase III)  
 Sheet 1 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR:



I-85 Bus. / US 29-70  
 at  
 SR 1144 (River Road)

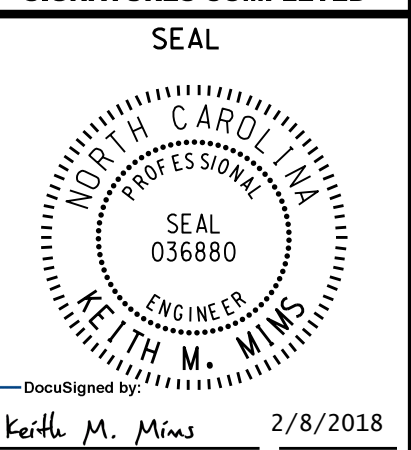
Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY:

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



DocuSigned by: Keith M. Mins 2/8/2018

SIG. INVENTORY NO. 07-1191T3

**OVERLAP PROGRAMMING DETAIL  
FOR DEFAULT PHASING**

*(program controller as shown below)*

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN  
'1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      :12345678910111213141516
VEH OVL PARENTS: :X
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW - GREEN
-----
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

PRESS '+' TWICE

```

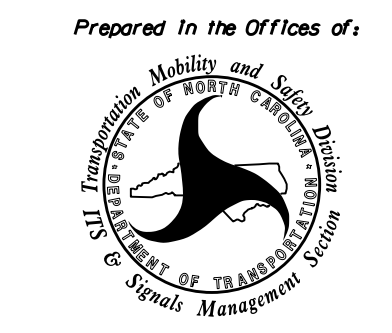
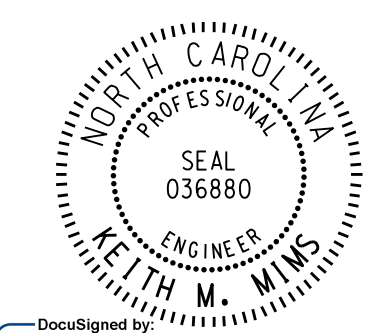
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      :12345678910111213141516
VEH OVL PARENTS: : X
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW - GREEN
-----
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 07-1191T3  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

08-FEB-2018 11:37  
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sarmstrong

Electrical Detail - Temporary Design 3 (TMP Phase III)  
Sheet 2 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR:  Prepared In the Offices of:   750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
	Division 7      Guilford County      Jamestown PLAN DATE: February 2018      REVIEWED BY: PREPARED BY: S. Armstrong      REVIEWED BY:	SEAL  ENGINEER KEITH M. MINS
REVISIONS      INIT.      DATE	SIG. INVENTORY NO. 07-1191T3	

**INPUT ASSIGNMENT PROGRAMMING DETAIL TO REASSIGN LONG VEHICLE OVERSPEED DETECTION SYSTEM FUNCTION**

(program controller as shown below)

This programming takes each of the Long Vehicle Overspeed Detection System inputs and reassigns a unique Hold Phase to it.

FROM MAIN MENU PRESS '5' (INPUTS), THEN '+' UNTIL PIN 51 (INPUT 13) IS REACHED.

```

PAGE: 1 C1 PIN:51 HOLD PHASES
INPUT ASSIGNMENT #.....13
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....2
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4).....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+'

INPUT 13 IS THE OUTPUT FROM LVODS #1

```

PAGE: 1 C1 PIN:52 HOLD PHASES
INPUT ASSIGNMENT #.....14
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....6
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4).....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+' OR '-' TO REACH INPUT 64

INPUT 14 IS THE OUTPUT FROM LVODS #2

```

PAGE: 1 C1 PIN:0 PREEMPT
INPUT ASSIGNMENT #.....64
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....7
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4).....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

← Notice Preempt 7

PROGRAMMING COMPLETE

**PREEMPT 7 PROGRAMMING DETAIL**

(program controller as shown below)

THIS PREEMPT GOES ACTIVE IF EITHER LVODS HAS BEEN ACTIVE FOR MORE THAN 4 CONSECUTIVE MINUTES AND WILL PUT THE INTERSECTION IN FLASH.

FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTIONS). PRESS 'NEXT' UNTIL PREEMPTION #7 IS REACHED.

PREEMPTION #7	INTERVAL/TIMING	SETTINGS (NEXT:1-10)
GRN YEL RED	12345678910111213141516	
1 15 0.0 0.0		X X
2 10 0.0 0.0		X X
3 15 0.0 0.0		X X
4 255 0.0 0.0		X X
5 1 0.0 0.0		X X

EXIT CALLS

OPTIONS

PRIORITY (Y/N TO SELECT) .....HIGH

DELAY TIMER (0-255 SEC) .....0.0

MIN GREEN BEFORE PRE (0= DEFAULT)....14

PED CLEAR BEFORE PRE (0= DEFAULT)....0

YELLOW CLEAR BEFORE PRE (0= DEFAULT)....0.0

RED CLEAR BEFORE PRE (0= DEFAULT)....0.0

DWELL MIN TIMER (0-255 SEC) .....14

DWELL MAX TIMER (0=OFF,1-255MIN) ....0

DWELL HOLD-OVER TIMER (0-255) .....0

LATCH CALL? .....N

LINK TO NEXT PREEMPT? .....N

ENABLE BACKUP PROTECTION? .....N

HOLD CLEAR 1 PHASES DURING DELAY? ...N

FAST GREEN FLASH DWELL PHASES? .....N

PED CLEARANCE THROUGH YELLOW? .....N

INHIBIT OVERLAP GREEN EXTENSION? ...N

SERVICE DURING SOFTWARE FLASH? .....N

REST IN RED DURING DWELL INTERVAL? ..N

FLASH DWELL INTERVAL? .....Y

ALLOW PEDS IN DWELL INTERVAL? .....N

RE-TIME DWELL INTERVAL? .....Y

OVERLAPS: ABCDEFGHIJKLMNPO

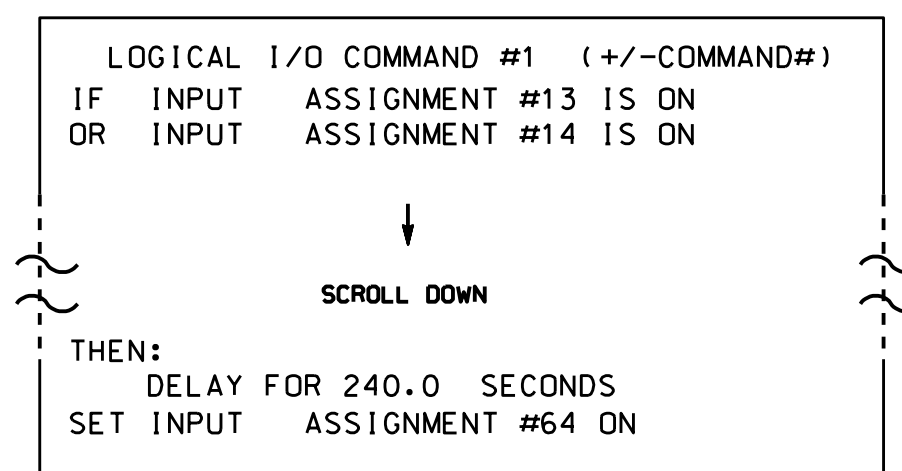
DWELL INT FLASH YELLOW X X

OMIT OVERLAPS:

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO FLASH INTERSECTION IF LVOD SYSTEMS FAIL**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMAND 1.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

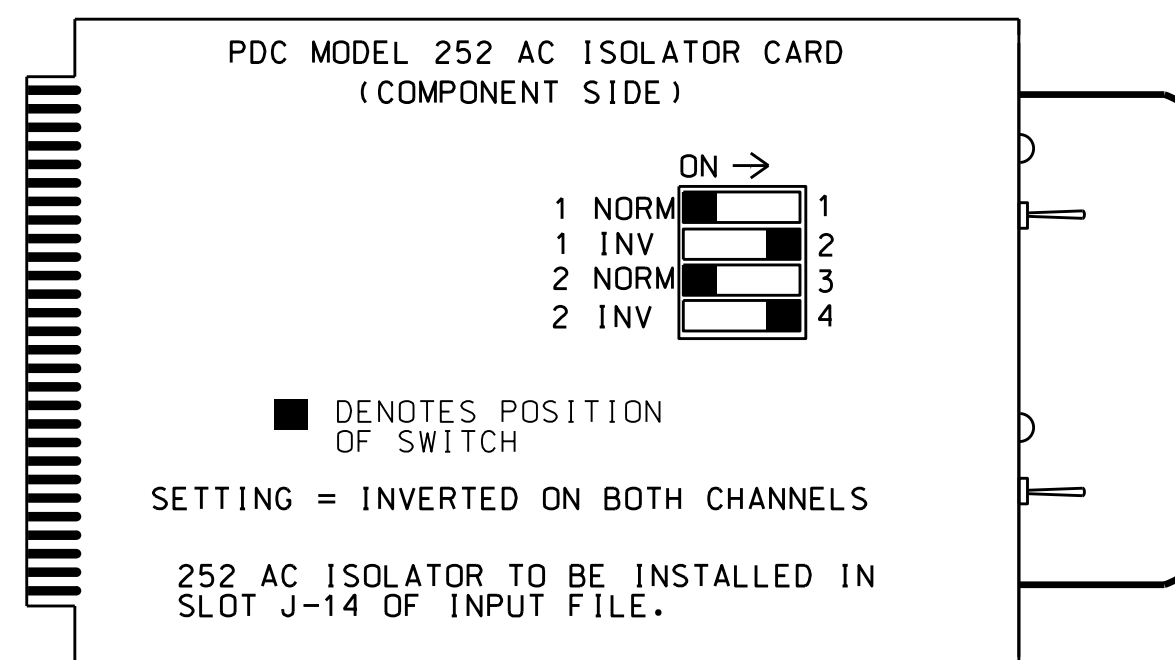
**INPUT REFERENCE SCHEDULE**

INPUT 13 = Input from LVODS #1  
 INPUT 14 = Input from LVODS #2  
 INPUT 64 = Preempt 7

INPUTS 13, 14, AND 64 HAVE BEEN REASSIGNED. SEE PROGRAMMING DETAILS ON THIS SHEET.

**HOLD PHASE AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL**

(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T3  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

Electrical Detail - Temporary Design 3 (TMP Phase III)  
 Sheet 3 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL KEITH M. MIMS ENGINEER
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

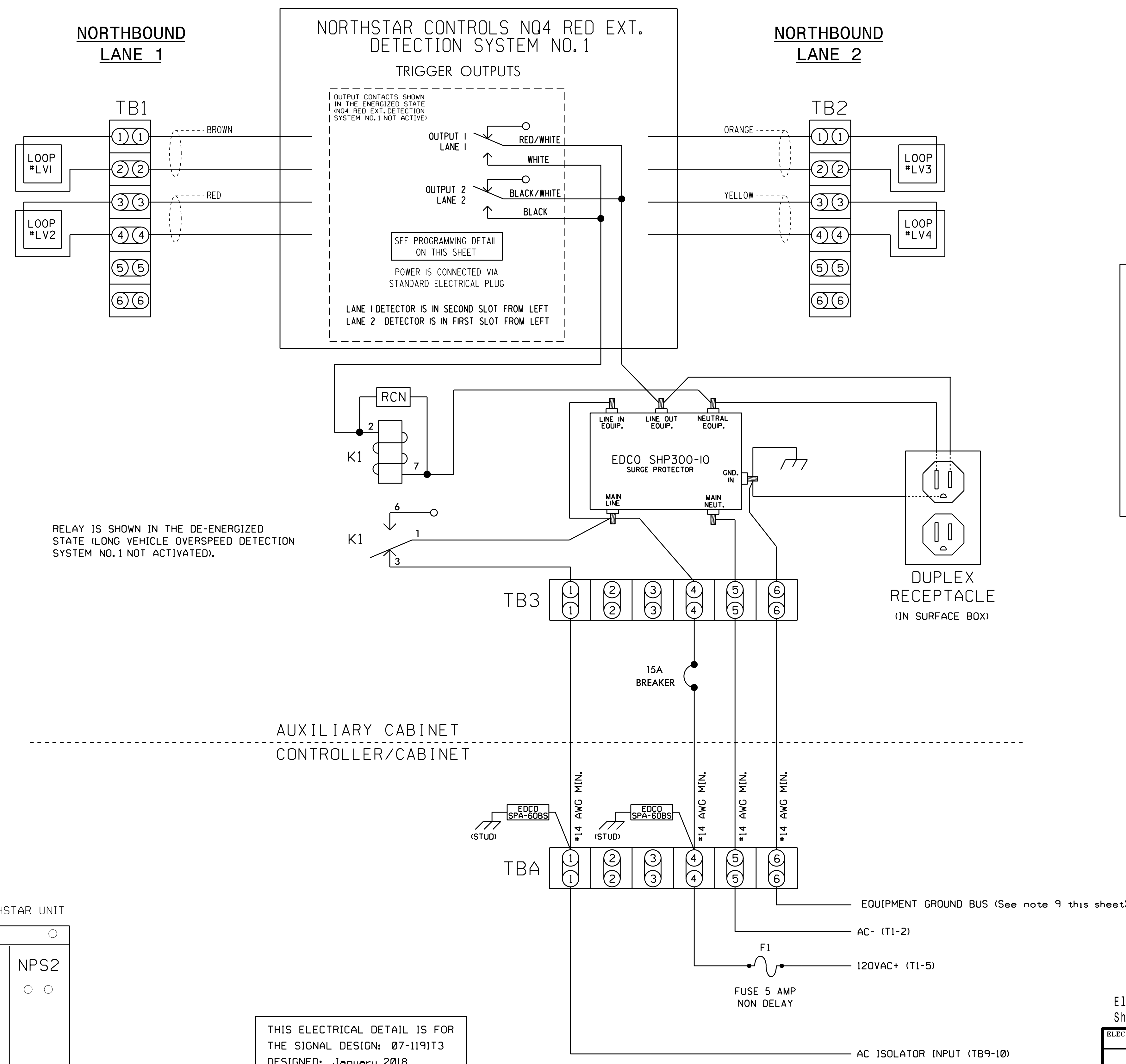
08-FEB-2018 14:32  
 C:\PITSAS\115\_Signal\work\hgr\oups\51g\_Morph\mstron\071191\_sml\_e\c\_xxx.dgn  
 sarmstrong



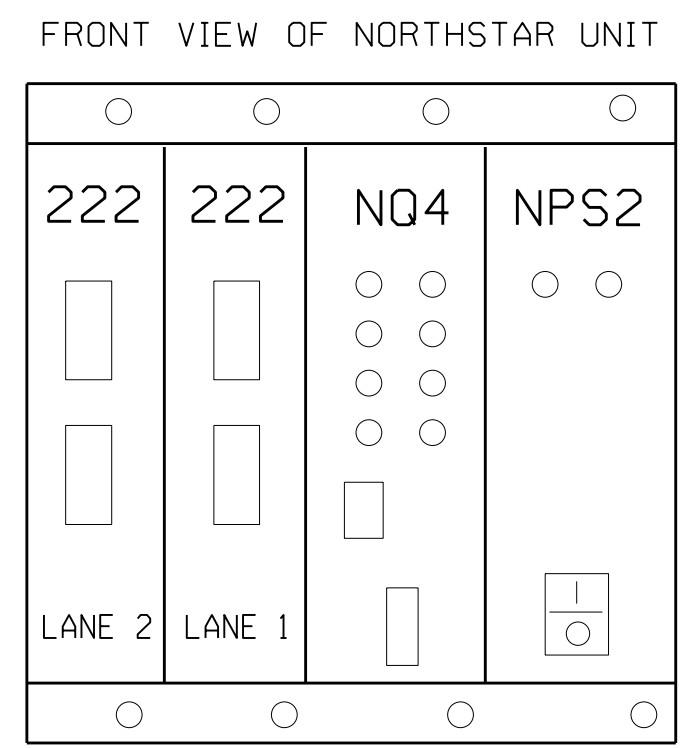
(wire unit as shown below)

NOTES

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBA to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.
11. IMPORTANT! A jumper must be installed between Input File terminals J14-E and J14-K.
12. IMPORTANT! For proper operation of the Dynamic Red Extension System, tie TB9-12 to AC neutral.
13. IMPORTANT! Make sure both channels of the AC Isolator card inserted in Input File slot J14 are set for INVERTED OUTPUT operation. See sheet 3 of this electrical detail.



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 1 NOT ACTIVATED).



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T3  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL

(program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

Program the NQ4 by typing the following commands:

1. SET SPEED = 55
2. SET LENGTH = 22'
3. SET ALARMTIME = 12
4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
5. SET LOOP LENGTH = 6' (program actual measured loop length)
6. SAVE

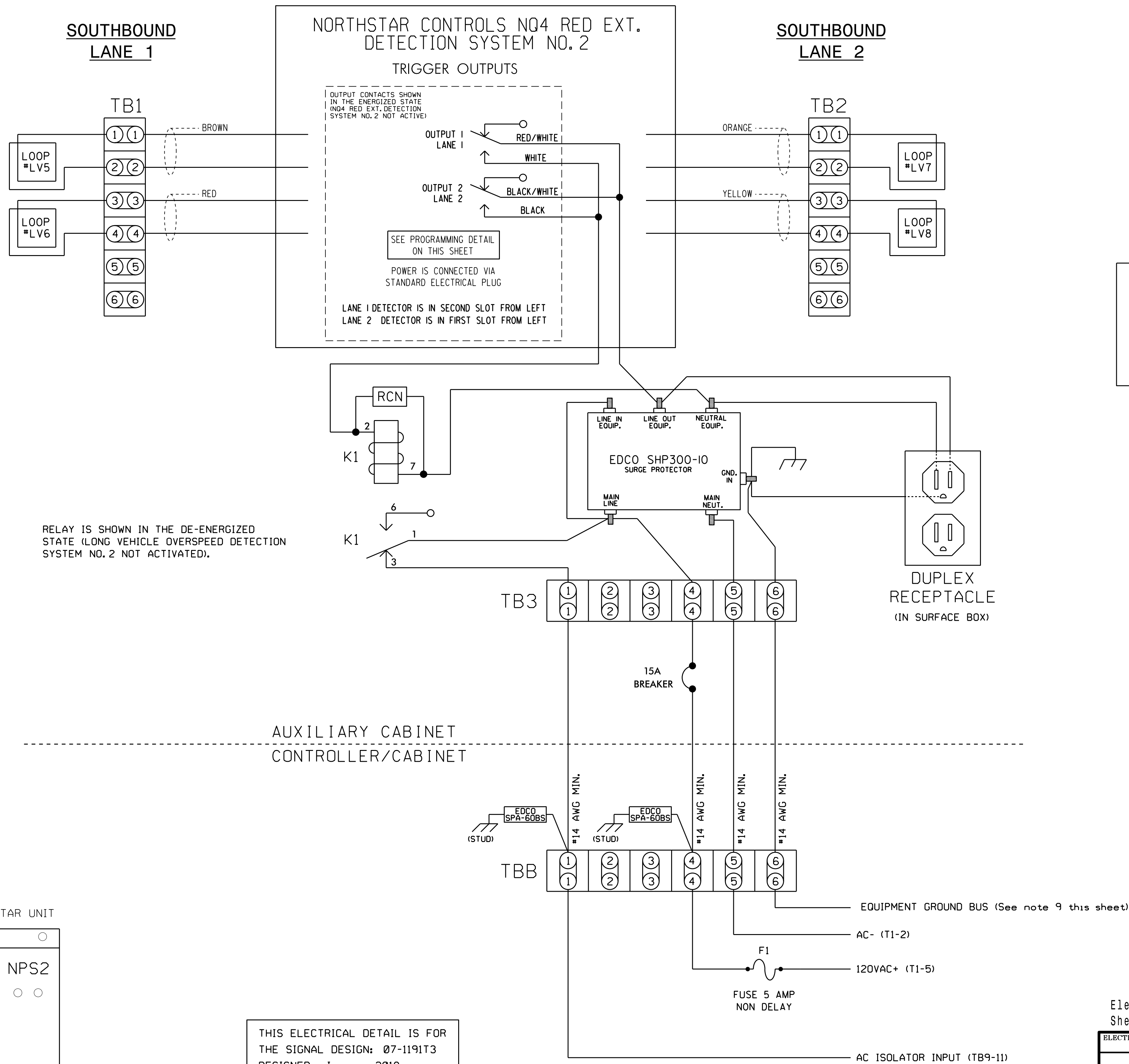
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Electrical Detail - Temporary Design 3 (TMP Phase III)  
 Sheet 4 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  KEITH M. MIMS ENGINEER 036880
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

NOTES

(wire unit as shown below)



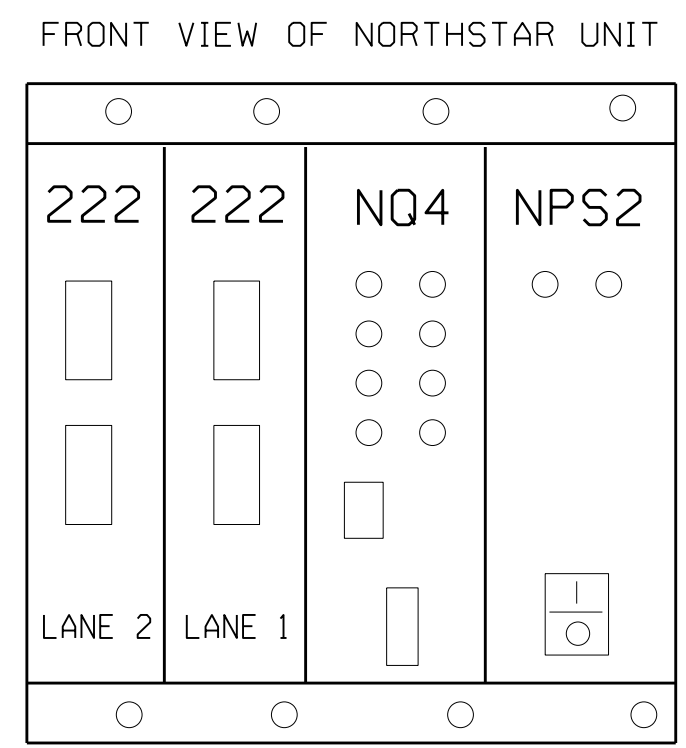
1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBB to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.

**NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL**  
(program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

Program the NQ4 by typing the following commands:

1. SET SPEED = 55
2. SET LENGTH = 22'
3. SET ALARMTIME = 12
4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
5. SET LOOP LENGTH = 6' (program actual measured loop length)
6. SAVE



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T3  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 3 (TMP Phase III)  
Sheet 5 of 5

Prepared In the Offices of:  
Gulf Transportation Mobility and Safety Division  
Gulf Transportation Mobility and Safety Division  
Professional Engineer  
Keith M. Mims  
750 N. Greenfield Pkwy, Garner, NC 27529

I-85 Bus. / US 29-70 at SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY:

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSigned by:  
Keith M. Mims 2/8/2018  
290798E8C03445 DATE

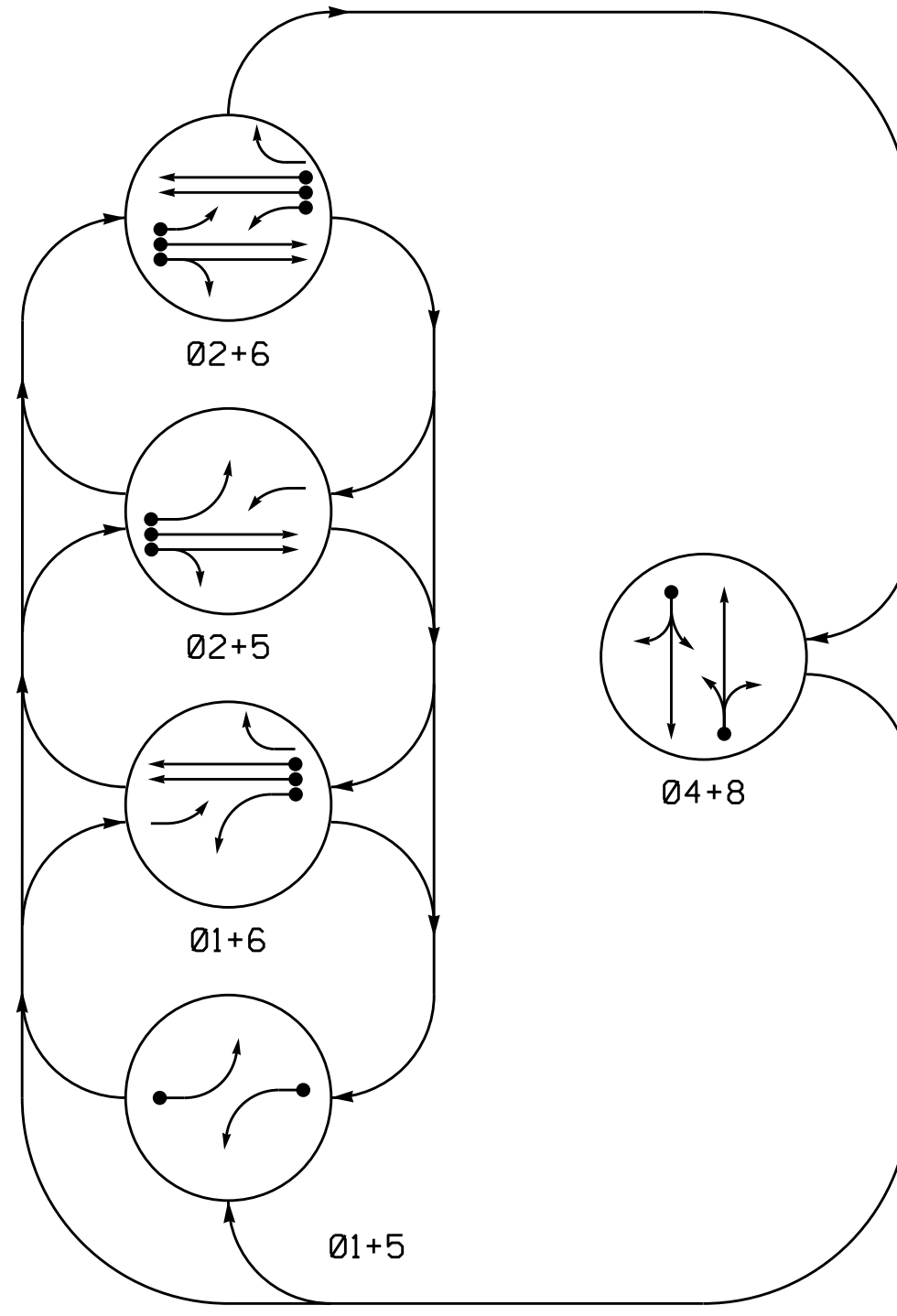
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
SEAL 036880  
KEITH M. MIMS

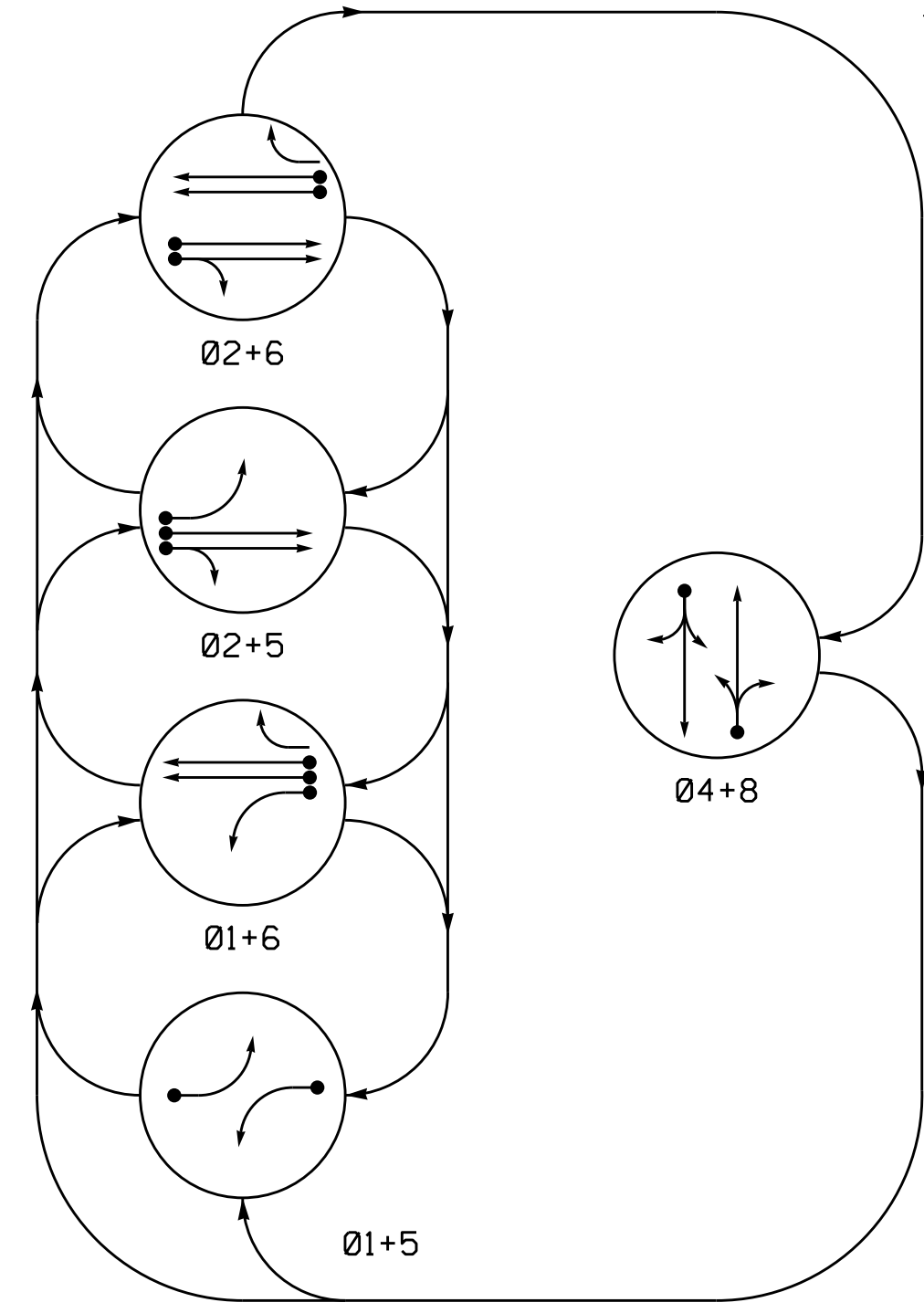
SIG. INVENTORY NO. 07-1191T3

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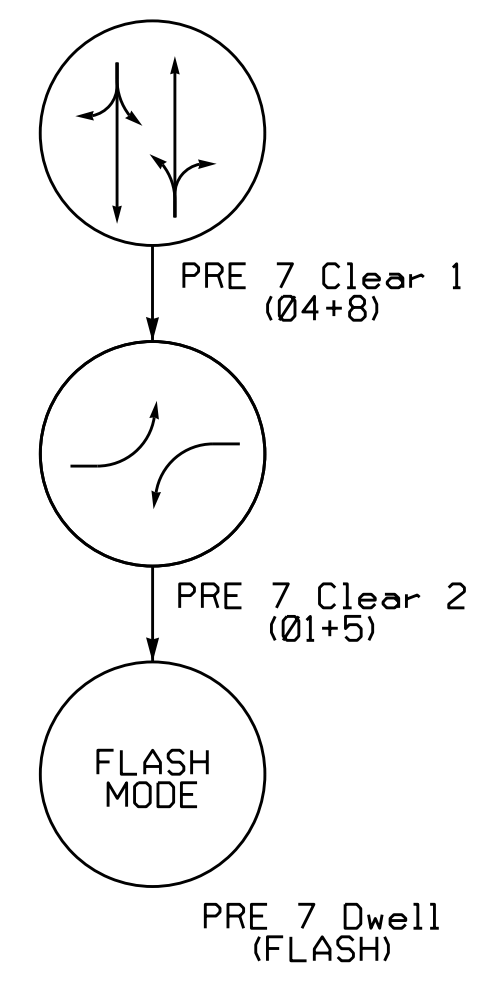
**DEFAULT PHASING DIAGRAM**



**ALTERNATE PHASING DIAGRAM**



**LONG VEHICLE EXTENSION FAILURE PREEMPT PHASES**



**DEFAULT PHASING TABLE OF OPERATION**

SIGNAL FACE	PHASE											
	01+5	01+6	02+5	02+6	04+8	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7
11	-	-	-	-	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	R	R	R	R	R	R
41, 42, 43	R	R	R	R	G	G	R	R	R	R	R	R
51	-	-	-	-	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	R	R	R	R	R	R
81, 82, 83	R	R	R	R	G	G	R	R	R	R	R	R

**OASIS 2070 LOOP & DETECTOR INSTALLATION CHART**

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING						SYSTEM LOOP	NEW CARD
					PHASE	CALLING	EXTENSION	STRETCH TIME	DELAY TIME	NEW LOOP		
1A	6X40	0	2-4-2	Y	1	Y	Y	-	-	15*	-	-
2A	6X6	400	5	-	2	Y	Y	-	-	-	-	-
2B	6X6	400	5	-	2	Y	Y	-	-	-	-	-
4A	6X40	0	2-4-2	Y	4	Y	Y	-	-	10	-	-
5A	6X40	0	2-4-2	-	5	Y	Y	-	-	15*	-	-
6A	6X6	420	5	Y	6	Y	Y	-	-	-	-	-
6B	6X6	420	5	Y	6	Y	Y	-	-	-	-	-
8A	6X40	0	2-4-2	-	8	Y	Y	-	-	10	-	-

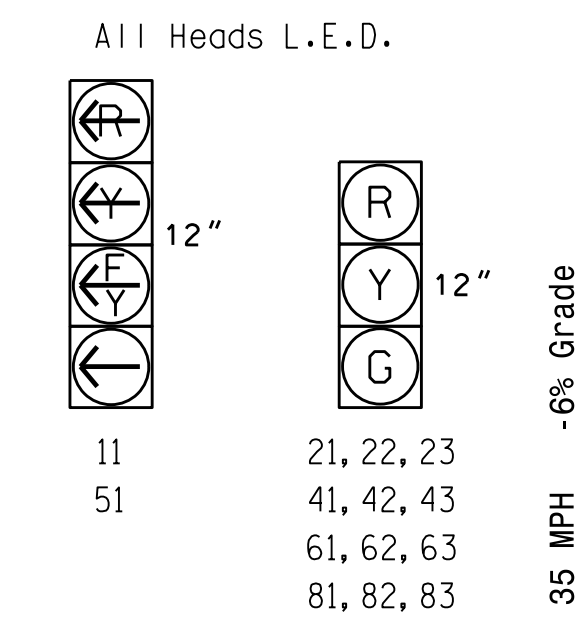
\* Disable Delay During Alternate Phasing Operation.  
# Disable Phase Call For Loop During Alternate Phasing Operation.

**5 Phase Fully Actuated W/ Long Vehicle Detection (Isolated)**

**NOTES**

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Reposition existing signal heads 11, 12, 23, 51, 61, 62, and 63.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.

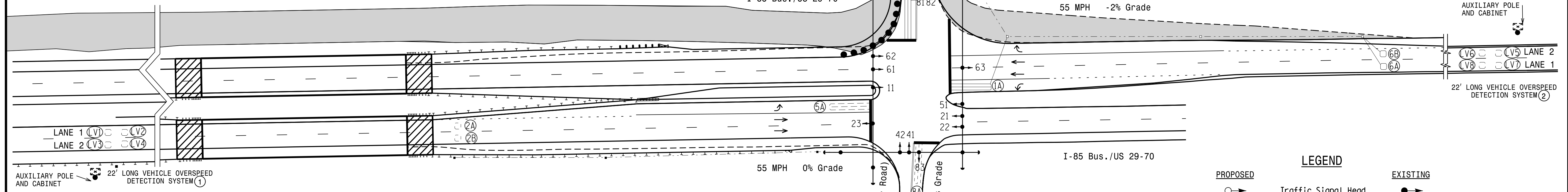
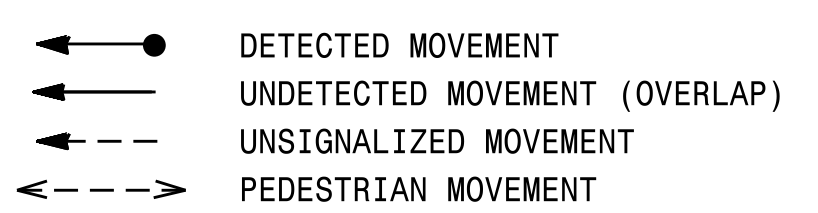
**SIGNAL FACE I.D.**



**ALTERNATE PHASING TABLE OF OPERATION**

SIGNAL FACE	PHASE											
	01+5	01+6	02+5	02+6	04+8	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7	PRE 7
11	-	-	-	-	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	R	R	R	R	R	R
41, 42, 43	R	R	R	R	G	G	R	R	R	R	R	R
51	-	-	-	-	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	R	R	R	R	R	R
81, 82, 83	R	R	R	R	G	G	R	R	R	R	R	R

**PHASING DIAGRAM DETECTION LEGEND**



**LONG VEHICLE EXTENSION FAILURE PREEMPT**

FUNCTION	PRE 7
Interval 1 - Green Clear	15
Interval 1 - Yellow Clear	0.0*
Interval 1 - Red Clear	0.0*
Interval 2 - Green Clear	10
Interval 2 - Yellow Clear	0.0*
Interval 2 - Red Clear	0.0*
Interval 3 - Dwell Green	255
Interval 3 - Dwell Yellow	0.0*
Interval 3 - Dwell Red	0.0*
Interval 4 - Exit Green	1
Interval 4 - Yellow	0.0
Interval 4 - Red	0.0
Exit Phase(s)	2+6
Priority	-
Delay Time	0
Min Green Before Pre	14
Ped Clear Before Pre	0
Yellow Clear Before Pre	0.0*
Red Clear Before Pre	0.0*
Dwell Min Time	14
Flash Dwell Interval?	Y
Enable Backup Protection	N
Ped Clear Through Yellow	N
Omit Overlaps	-

\* Time defaults to time used for phase during normal operation

**OASIS 2070 TIMING CHART**

FEATURE	PHASE					
	1	2	4	5	6	8
Min Green 1*	7	14	7	7	14	7
Extension 1*	2.0	6.0	3.0	2.0	6.0	3.0
Max Green 1*	20	120	25	25	120	25
Yellow Clearance	3.0	5.4	4.3	3.0	5.4	3.6
Red Clearance	2.3	1.0	1.9	2.8	1.0	2.0
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0
Walk 1*	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-
Seconds Per Actuation*	-	1.5	-	-	1.5	-
Max Variable Initial*	-	46	-	-	46	-
Time Before Reduction*	-	20	-	-	20	-
Time To Reduce*	-	50	-	-	50	-
Minimum Gap	-	3.4	-	-	3.4	-
Recall Mode	-	MIN RECALL	-	-	MIN RECALL	-
Vehicle Call Memory	-	YELLOW	-	-	YELLOW	-
Dual Entry	-	-	ON	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON

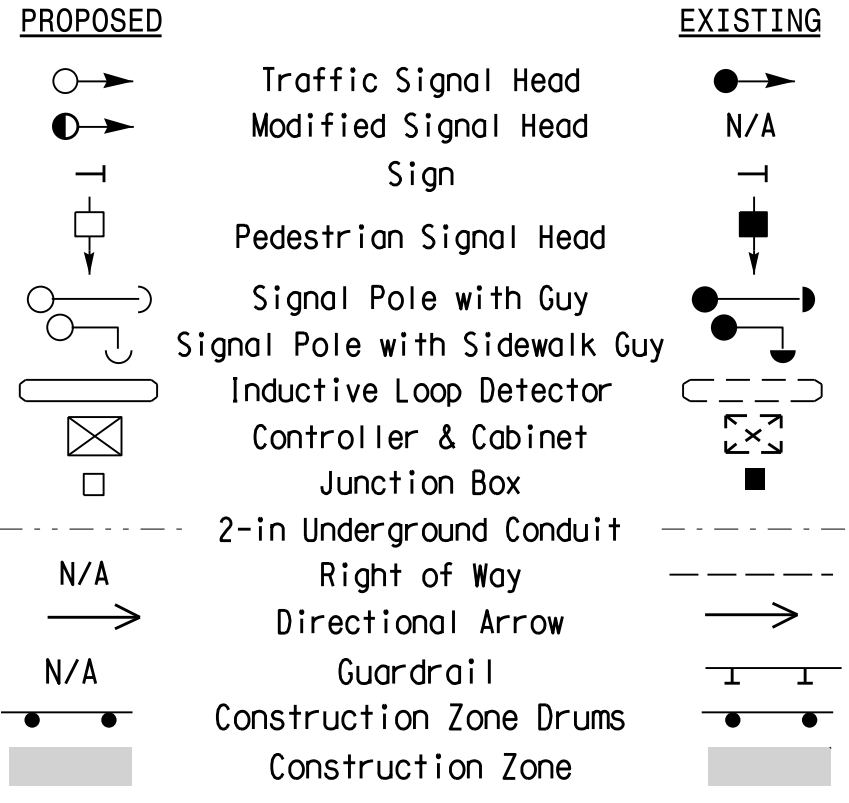
\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

**LONG VEHICLE OVERSPEED DETECTION SYSTEM LOOP & DETECTOR INSTALLATION CHART**

LOOP NO.	SIZE (FT)	TURNS	DIST. FROM STOPBAR (FT)	NEW	EXISTING	INDUCTIVE LOOP	DETECTOR UNITS	TIMING		PLACE CALL DURING PHASE	INHIBIT DELAY DURING GREEN?	
								FEATURE	TIME			
LV1	6X6	4	1015	X	X	1	1	NONE	- SEC.	ALL	NO	
LV2	6X6	4	999	X	X	1	1	NONE	- SEC.	ALL	NO	
LV3	6X6	4	1015	X	X	2	2	NONE	- SEC.	ALL	NO	
LV4	6X6	4	999	X	X	2	2	NONE	- SEC.	ALL	NO	
LV5	6X6	4	1015	X	X	1	1	NONE	- SEC.	ALL	NO	
LV6	6X6	4	999	X	X	1	1	NONE	- SEC.	ALL	NO	
LV7	6X6	4	1015	X	X	2	2	NONE	- SEC.	ALL	NO	
LV8	6X6	4	999	X	X	2	2	NONE	- SEC.	ALL	NO	
LVODS THRESHOLD SPEED (MPH)							55	2				
LVODS EXTEND TIME							12 SEC.	2				

\*Phase hold output to controller

**LEGEND**



**Signal Upgrade Temporary Design 4 (TMP Phase IV)**

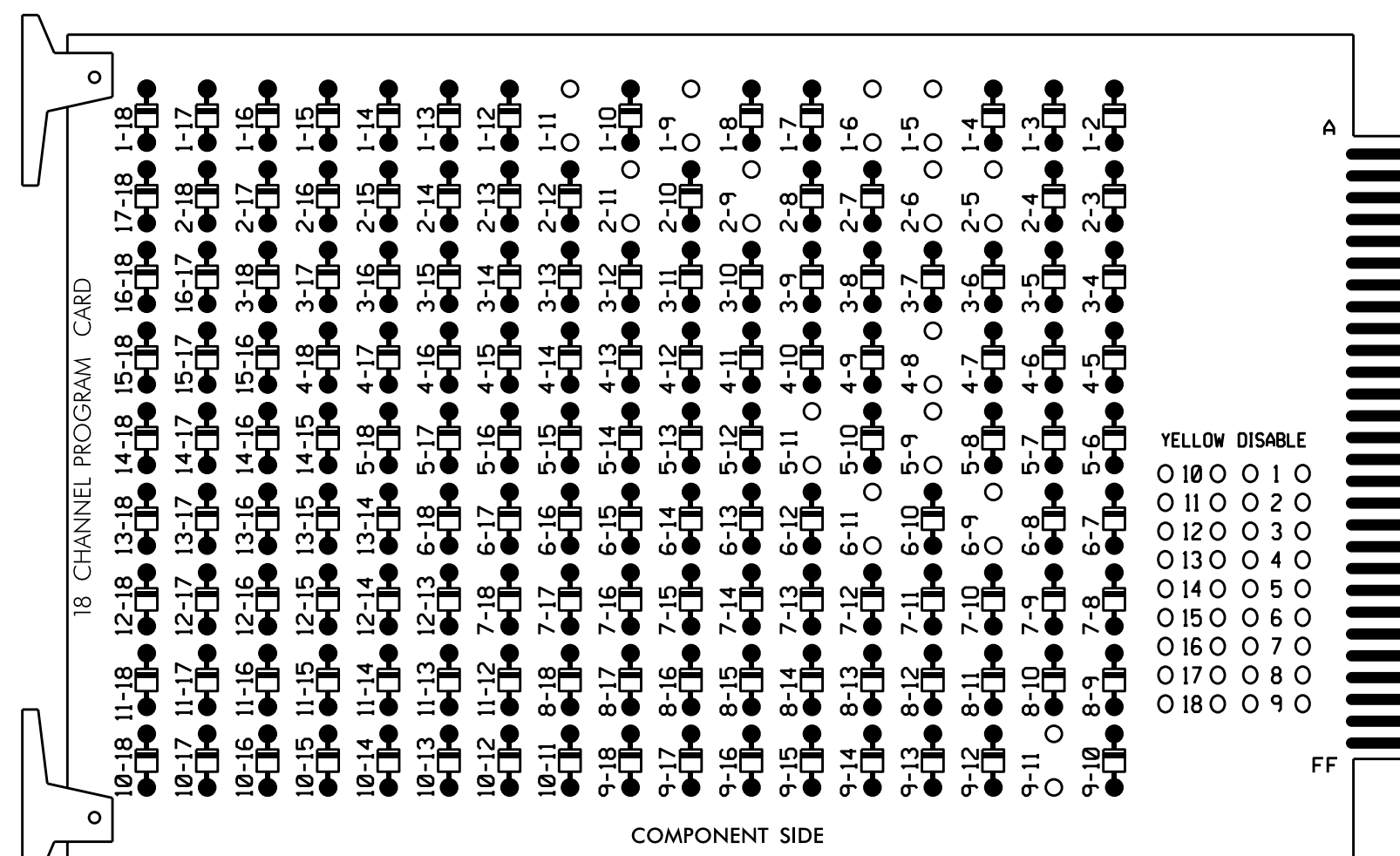
Prepared in the Offices of:  
  
 I-85 Bus. /US 29-70 at SR 1144 (River Road)  
 Division 7 Guilford County Jamestown  
 PLAN DATE: January 2018 REVIEWED BY:  
 PREPARED BY: I. O. Umzurike REVIEWED BY:  
 REVISIONS: \_\_\_\_\_ INIT. DATE  
 2/7/2018  
 SEAL: 026486  
 DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED  
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**EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

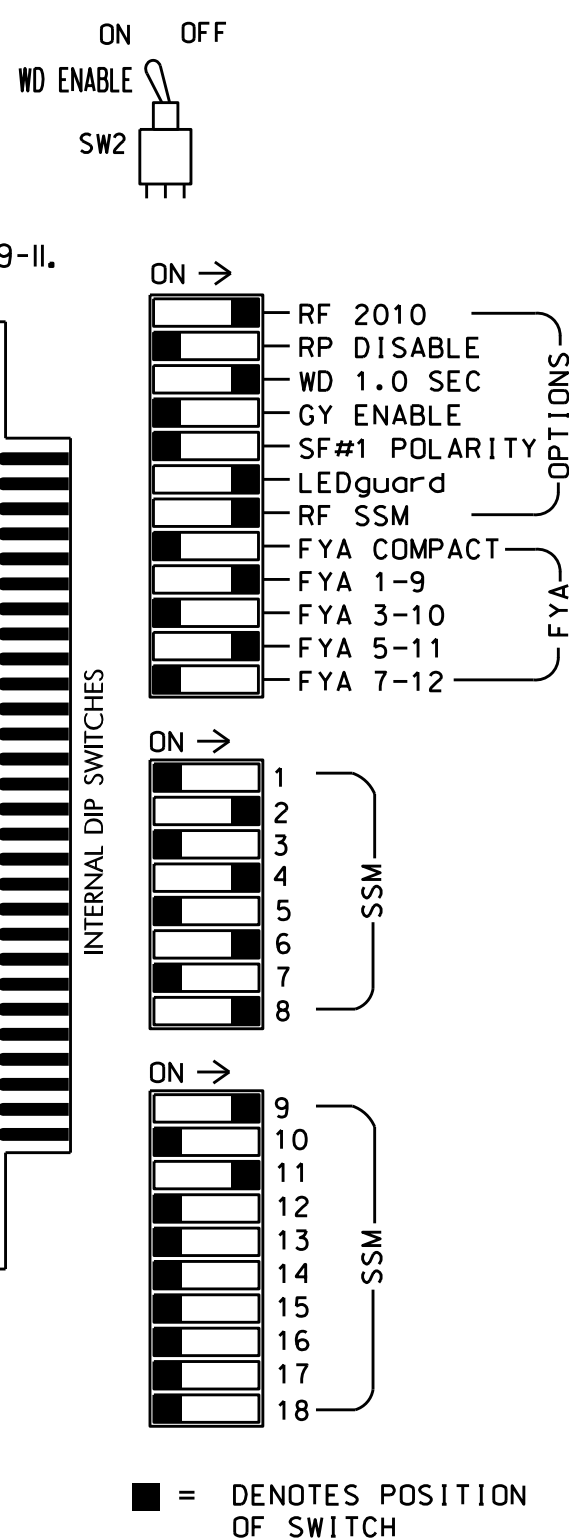
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Return controller to Factory Defaults before programming per this electrical detail.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUX S1,AUX S4  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22 23	NU	NU	41,42 43	NU	51	61,62 63	NU	NU	81,82 83	NU	11	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127							133										

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I" U	∅ 1 1A	∅ 2 2A	∅ 3 3A	∅ 4 4A	∅ 5 5A	∅ 6 6A	∅ 7 7A	∅ 8 8A	∅ 9 9A	∅ 10 10A	∅ 11 11A	∅ 12 12A	∅ 13 13A	∅ 14 14A
FILE "I" L	NOT USED	∅ 2 2B	∅ 3 3B	NOT USED	∅ 5 5B	∅ 6 6B	∅ 7 7B	∅ 8 8B	∅ 9 9B	∅ 10 10B	∅ 11 11B	∅ 12 12B	∅ 13 13B	∅ 14 14B
FILE "J" U	∅ 5 5A	∅ 6 6A	∅ 7 7A	∅ 8 8A	∅ 9 9A	∅ 10 10A	∅ 11 11A	∅ 12 12A	∅ 13 13A	∅ 14 14A	∅ 15 15A	∅ 16 16A	∅ 17 17A	∅ 18 18A
FILE "J" L	NOT USED	∅ 6 6B	∅ 7 7B	NOT USED	∅ 9 9B	∅ 10 10B	∅ 11 11B	∅ 12 12B	∅ 13 13B	∅ 14 14B	∅ 15 15B	∅ 16 16B	∅ 17 17B	∅ 18 18B

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

See AC Isolator programming detail on sheet 6.

**INPUT FILE CONNECTION & PROGRAMMING CHART**

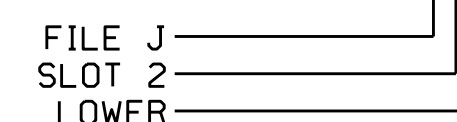
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9★	22	2	Y	Y	Y		3
	-	J1U	55	17★	55	5	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

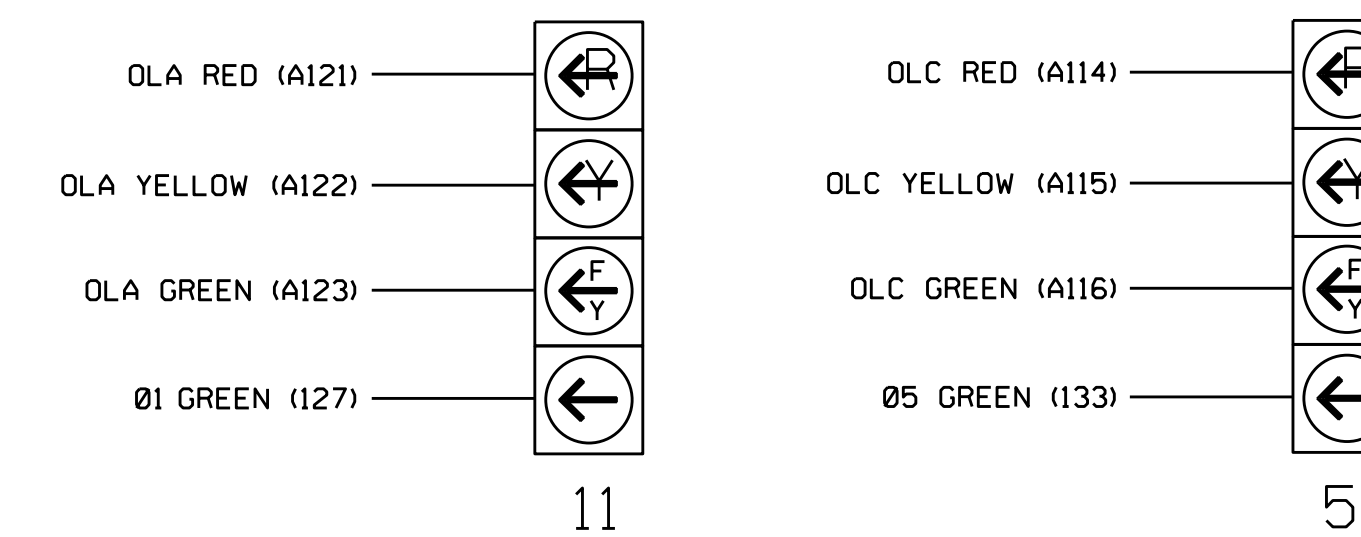
★ See Input Page Assignment programming details on sheets 3 and 4.

**INPUT FILE POSITION LEGEND: J2L**



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

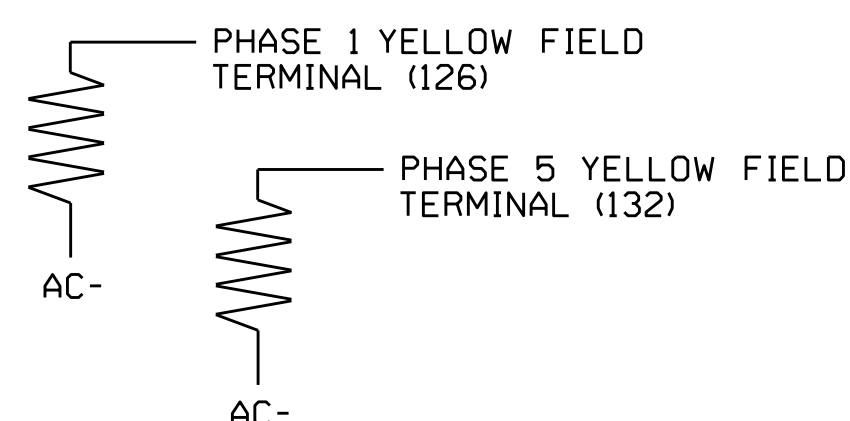
The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T4  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Temporary Design 4 (TMP Phase IV)  
 Sheet 1 of 8

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  
 G.L. Transportation, Mobility and Safety Division  
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION  
 Signal Management Section  
 750 N. Greenfield Pkwy, Garner, NC 27529

DETAILS FOR: I-85 Bus. / US 29-70 at SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY:  
 PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

Seal: KEITH M. MINIS, ENGINEER, 036880

DocuSigned by: Keith M. Minis 2/8/2018  
 2780796E8CD3445 DATE

SIG. INVENTORY NO. 07-1191T4

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SEQUENCE, AND TO FLASH INTERSECTION IF LVOD SYSTEMS FAIL**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, AND 7.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)  
IF INPUT ASSIGNMENT #13 IS ON  
OR INPUT ASSIGNMENT #14 IS ON

↓  
SCROLL DOWN

THEN:  
DELAY FOR 240.0 SECONDS  
SET INPUT ASSIGNMENT #64 ON

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**I/O REFERENCE SCHEDULE**

- INPUT 13 = Input from LVODS #1
- INPUT 14 = Input from LVODS #2
- INPUT 64 = Preempt 7
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

INPUTS 13, 14, AND 64 HAVE BEEN REASSIGNED. SEE SHEET 6 FOR PROGRAMMING DETAILS.

**OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS), PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

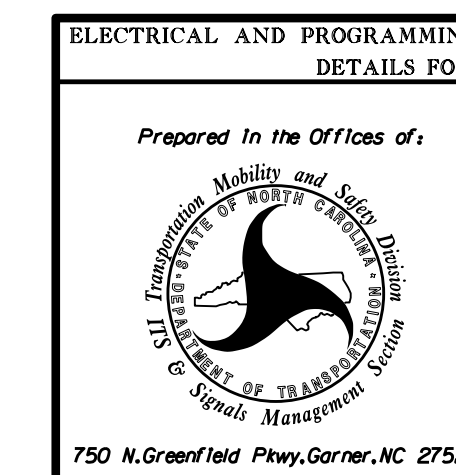
NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T4  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 4 (TMP Phase IV)  
Sheet 2 of 8



I-85 Bus. / US 29-70 at SR 1144 (River Road)	
Division 7	Guilford County
PLAN DATE: February 2018	REVIEWED BY:
PREPARED BY: S. Armstrong	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

Keith M. Mims  
2/8/2018

SIG. INVENTORY NO. 07-1191T4

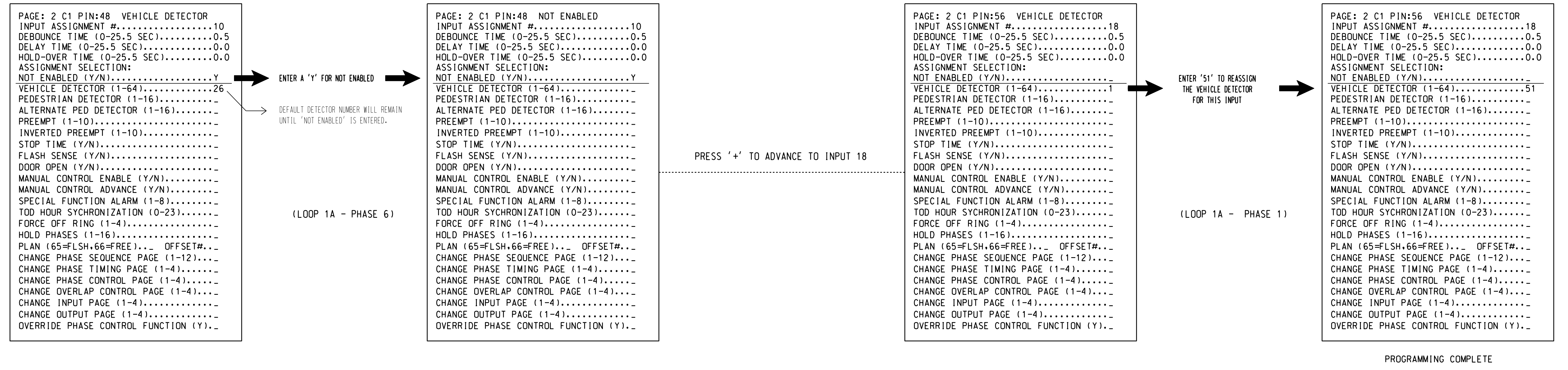
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**INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A**

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

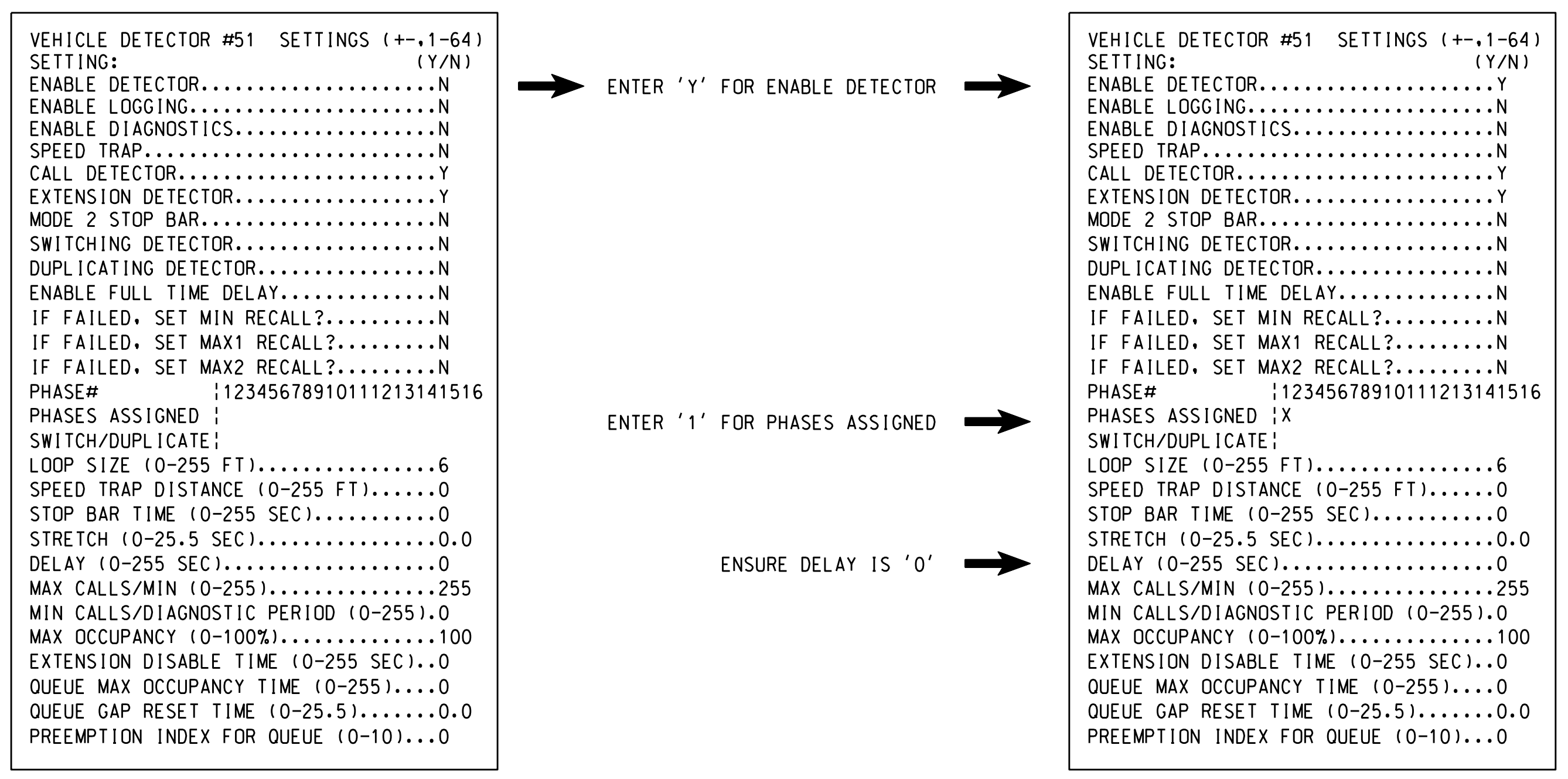
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.



**SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)**

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T4  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

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Electrical Detail - Temporary Design 4 (TMP Phase IV)  
Sheet 3 of 8

	I-85 Bus. / US 29-70 at SR 1144 (River Road)		
	Prepared In the Offices of: Guilford County, North Carolina Department of Transportation and Safety Signal Management Section 750 N. Greenfield Pkwy, Garner, NC 27529	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	
REVISIONS		INIT. DATE	DocuSigned by: Keith M. Mims 2/8/2018 DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

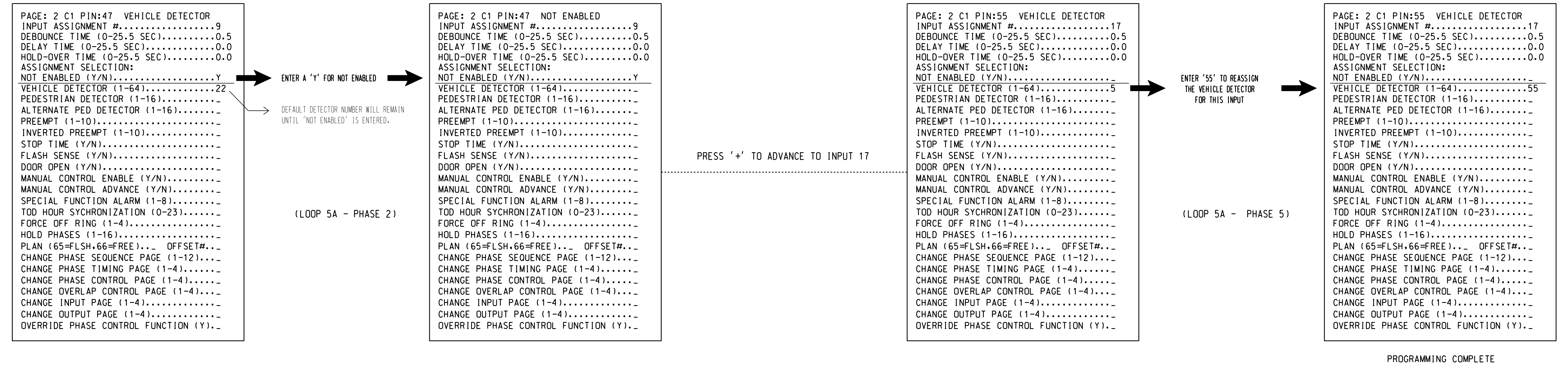
SIG. INVENTORY NO. 07-1191T4

INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 5A

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #9 (DETECTOR 22) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 2 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 55 TO INPUT #17 SO THAT THE DELAY ON LOOP 5A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

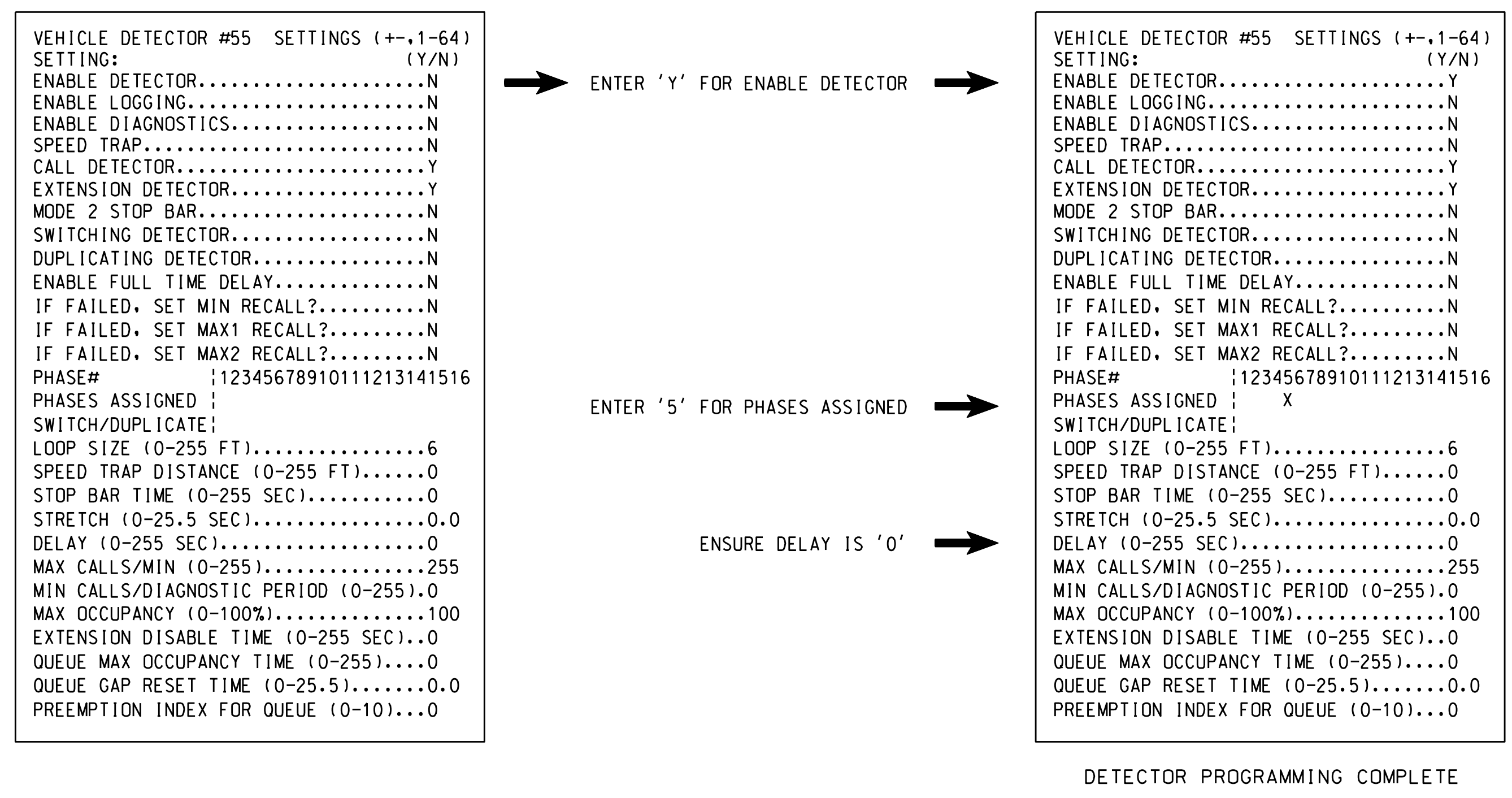
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 9 IS REACHED.



SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 5A (ALT.)

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #55.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T4  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

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Electrical Detail - Temporary Design 4 (TMP Phase IV)  
Sheet 4 of 8

Prepared In the Offices of:  
G.L. Transportation, Mobility and Safety Division  
G.L. Transportation, Mobility and Safety Division  
Signal Management Section  
750 N. Greenfield Pkwy, Garner, NC 27529

DETAILS FOR: I-85 Bus. / US 29-70 at SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY: [Signature]  
PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

REVISIONS	INIT.	DATE

DocuSigned by: Keith M. Mims 2/8/2018  
27807968C03445 DATE

SEAL  
NORTH CAROLINA PROFESSIONAL ENGINEER  
KEITH M. MIMS  
036880  
SIG. INVENTORY NO. 07-1191T4





**INPUT ASSIGNMENT PROGRAMMING DETAIL TO REASSIGN LONG VEHICLE OVERSPEED DETECTION SYSTEM FUNCTION**  
(program controller as shown below)

This programming takes each of the Long Vehicle Overspeed Detection System inputs and reassigns a unique Hold Phase to it.

FROM MAIN MENU PRESS '5' (INPUTS), THEN '+' UNTIL PIN 51 (INPUT 13) IS REACHED.

```

PAGE: 1 C1 PIN:51 HOLD PHASES
INPUT ASSIGNMENT #.....13
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....2
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+'

INPUT 13 IS THE OUTPUT FROM LVODS #1

```

PAGE: 1 C1 PIN:52 HOLD PHASES
INPUT ASSIGNMENT #.....14
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....6
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+' OR '-' TO REACH INPUT 64

INPUT 14 IS THE OUTPUT FROM LVODS #2

```

PAGE: 1 C1 PIN:0 PREEMPT
INPUT ASSIGNMENT #.....64
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....7
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)....
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PROGRAMMING COMPLETE

← Notice Preempt 7

**PREEMPT 7 PROGRAMMING DETAIL**  
(program controller as shown below)

THIS PREEMPT GOES ACTIVE IF EITHER LVODS HAS BEEN ACTIVE FOR MORE THAN 4 CONSECUTIVE MINUTES AND WILL PUT THE INTERSECTION IN FLASH.

FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTIONS), PRESS 'NEXT' UNTIL PREEMPTION #7 IS REACHED.

PREEMPTION #7	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 15 0.0 0.0	X X
2 10 0.0 0.0	X X
3 255 0.0 0.0	X X
4 0 0.0 0.0	X X
5 1 0.0 0.0	X X

EXIT CALLS

OPTIONS

PRIORITY (Y/N TO SELECT) .....HIGH

DELAY TIMER (0-255 SEC) .....0.0

MIN GREEN BEFORE PRE (0= DEFAULT)....14

PED CLEAR BEFORE PRE (0= DEFAULT)....0

YELLOW CLEAR BEFORE PRE (0= DEFAULT)....0.0

RED CLEAR BEFORE PRE (0= DEFAULT)....0.0

DWELL MIN TIMER (0-255 SEC) .....14

DWELL MAX TIMER (0=OFF,1-255MIN) ....0

DWELL HOLD-OVER TIMER (0-255) .....0

LATCH CALL? .....N

LINK TO NEXT PREEMPT? .....N

ENABLE BACKUP PROTECTION? .....N

HOLD CLEAR 1 PHASES DURING DELAY? ...N

FAST GREEN FLASH DWELL PHASES? .....N

PED CLEARANCE THROUGH YELLOW? .....N

INHIBIT OVERLAP GREEN EXTENSION? ...N

SERVICE DURING SOFTWARE FLASH? .....N

REST IN RED DURING DWELL INTERVAL? ..N

FLASH DWELL INTERVAL? .....Y

ALLOW PEDS IN DWELL INTERVAL? .....Y

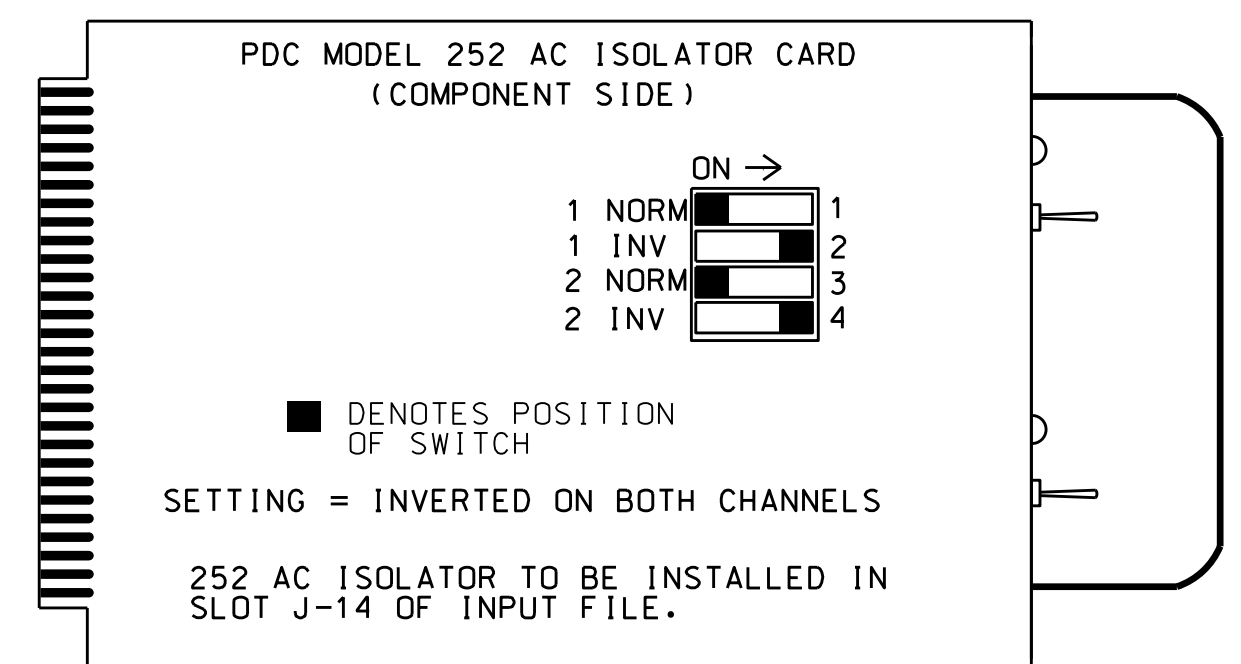
RE-TIME DWELL INTERVAL? .....Y

OVERLAPS: ABCDEFGHIJKLMNPO

DWELL INT FLASH YELLOW X X

OMIT OVERLAPS:

**HOLD PHASE AC ISOLATOR (MODEL 252)**  
**OUTPUT PROGRAMMING DETAIL**  
(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T4  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Temporary Design 4 (TMP Phase IV)  
Sheet 6 of 8

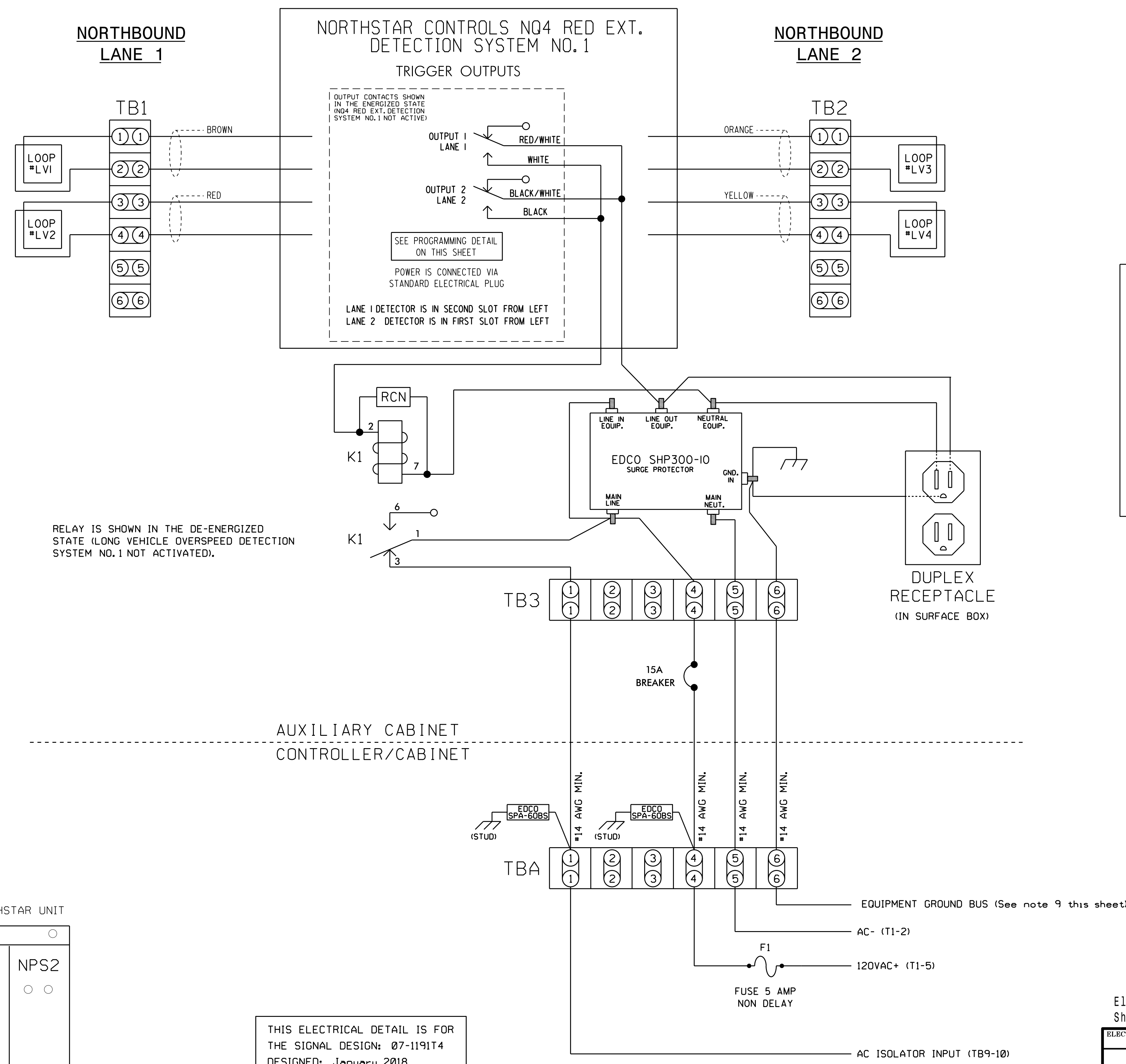
Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL KEITH M. MIMS ENGINEER 2/8/2018 DATE
	Division 7 Guilford County Jamestown PLAN DATE: February 2018 REVIEWED BY: PREPARED BY: S. Armstrong REVIEWED BY:	

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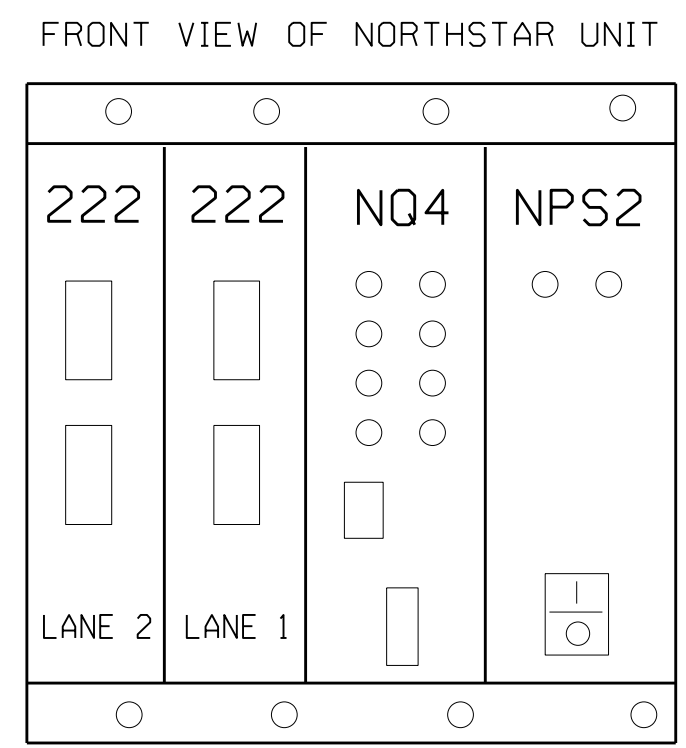
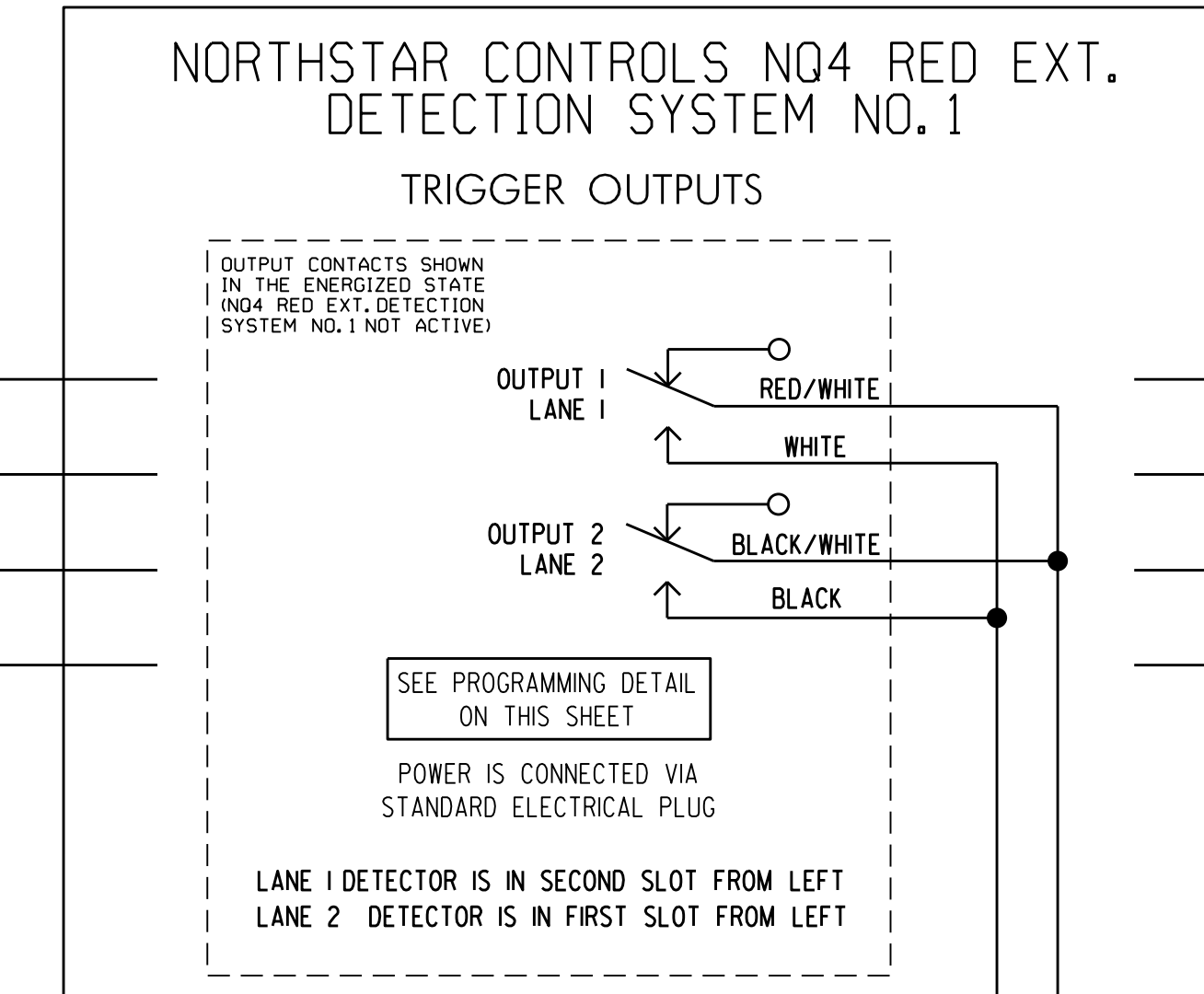
(wire unit as shown below)

**NOTES**

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBA to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.
11. IMPORTANT! A jumper must be installed between Input File terminals J14-E and J14-K.
12. IMPORTANT! For proper operation of the Dynamic Red Extension System, tie TB9-12 to AC neutral.
13. IMPORTANT! Make sure both channels of the AC Isolator card inserted in Input File slot J14 are set for INVERTED OUTPUT operation. See sheet 6 of this electrical detail.



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 1 NOT ACTIVATED).



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T4  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

**NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL**  
 (program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

Program the NQ4 by typing the following commands:

1. SET SPEED = 55
2. SET LENGTH = 22'
3. SET ALARMTIME = 12
4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
5. SET LOOP LENGTH = 6' (program actual measured loop length)
6. SAVE

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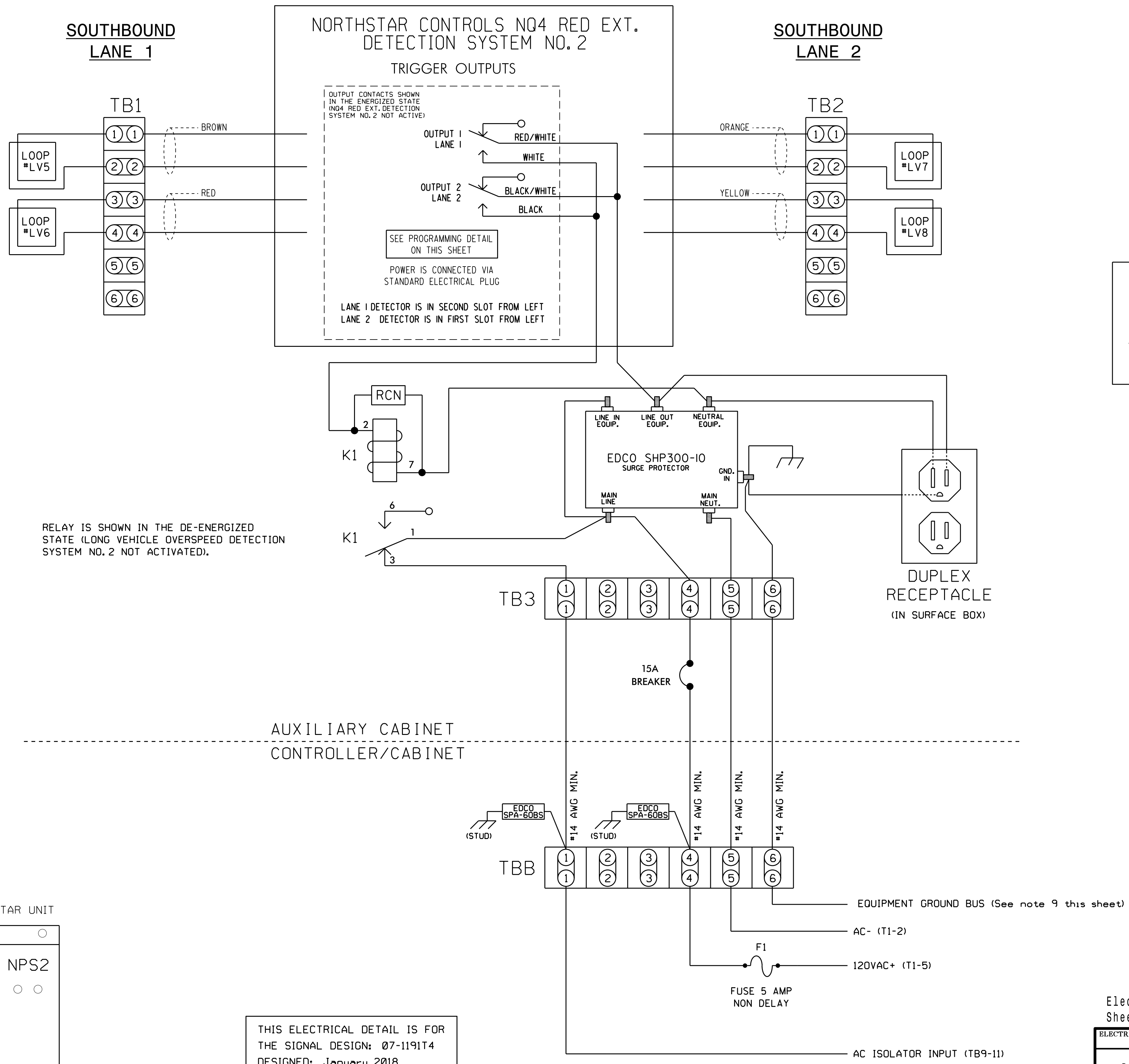
Electrical Detail - Temporary Design 4 (TMP Phase IV)  
 Sheet 7 of 8

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  KEITH M. MINS ENGINEER
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

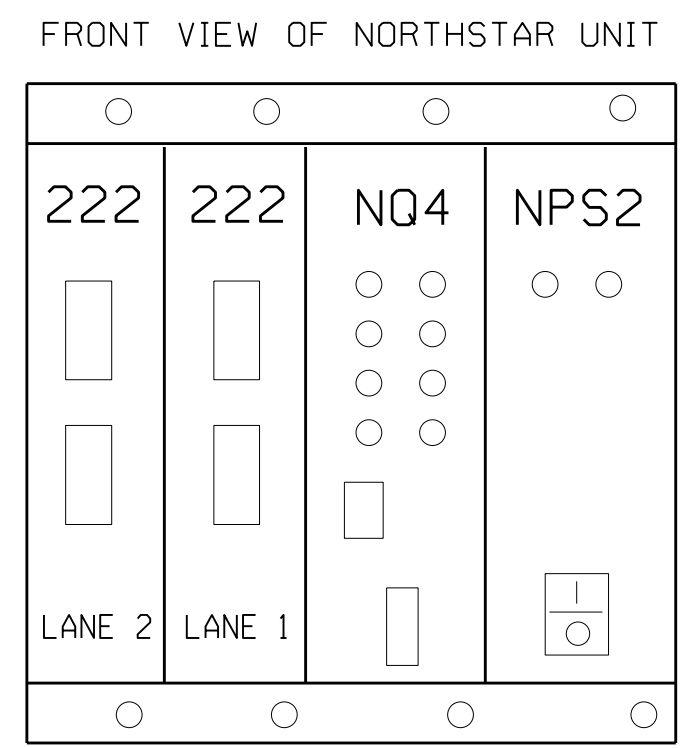
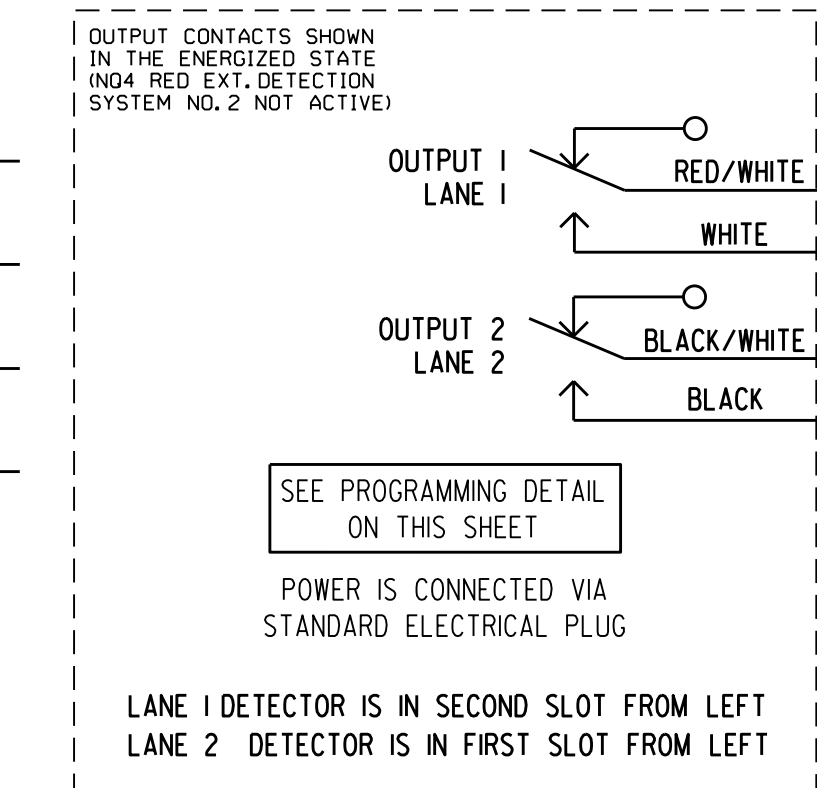
**NOTES**

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBB to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.

(wire unit as shown below)



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 2 NOT ACTIVATED).



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191T4  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

**NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL**  
 (program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

- Program the NQ4 by typing the following commands:
1. SET SPEED = 55
  2. SET LENGTH = 22'
  3. SET ALARMTIME = 12
  4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
  5. SET LOOP LENGTH = 6' (program actual measured loop length)
  6. SAVE

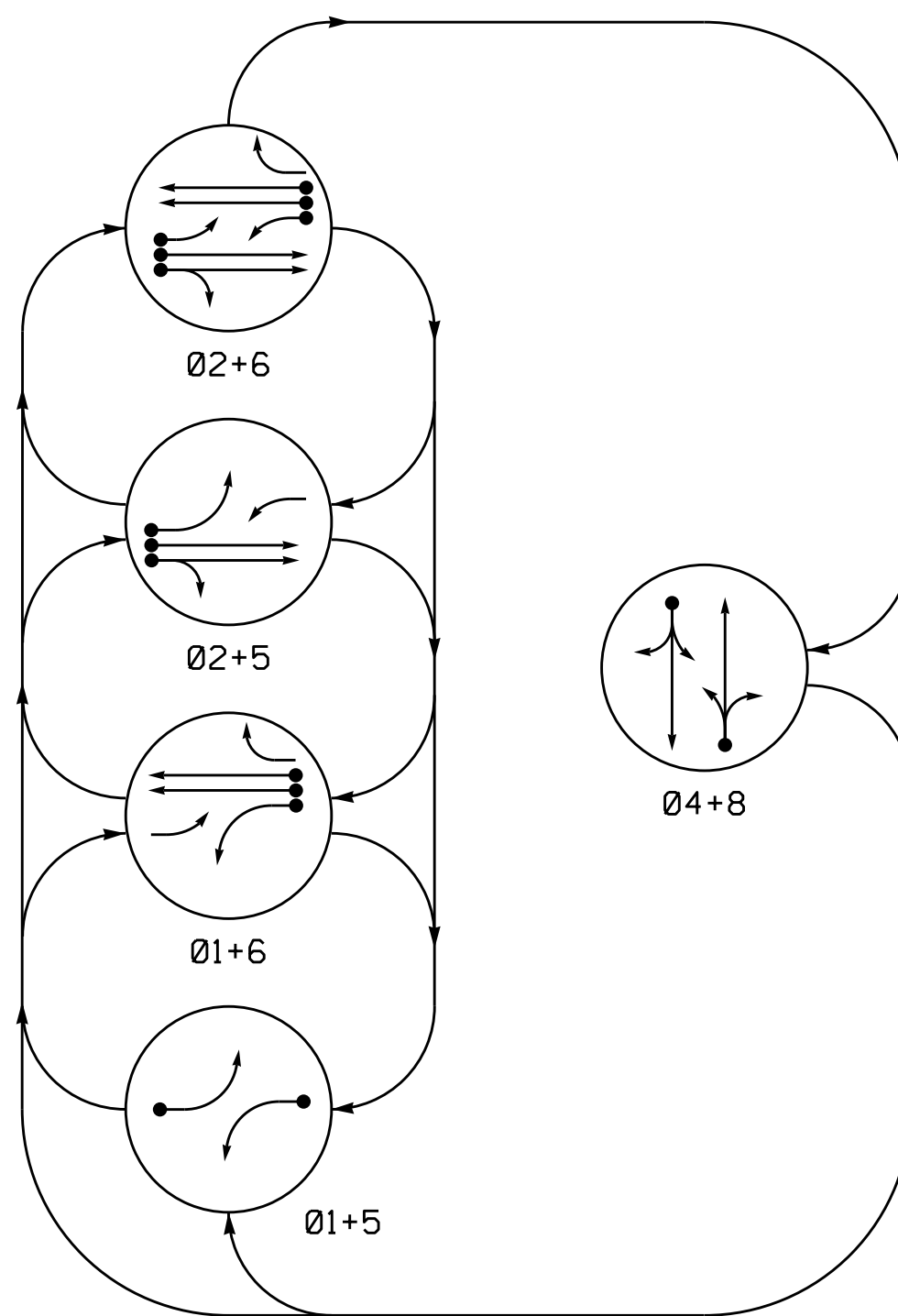
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Electrical Detail - Temporary Design 4 (TMP Phase IV)  
 Sheet 8 of 8

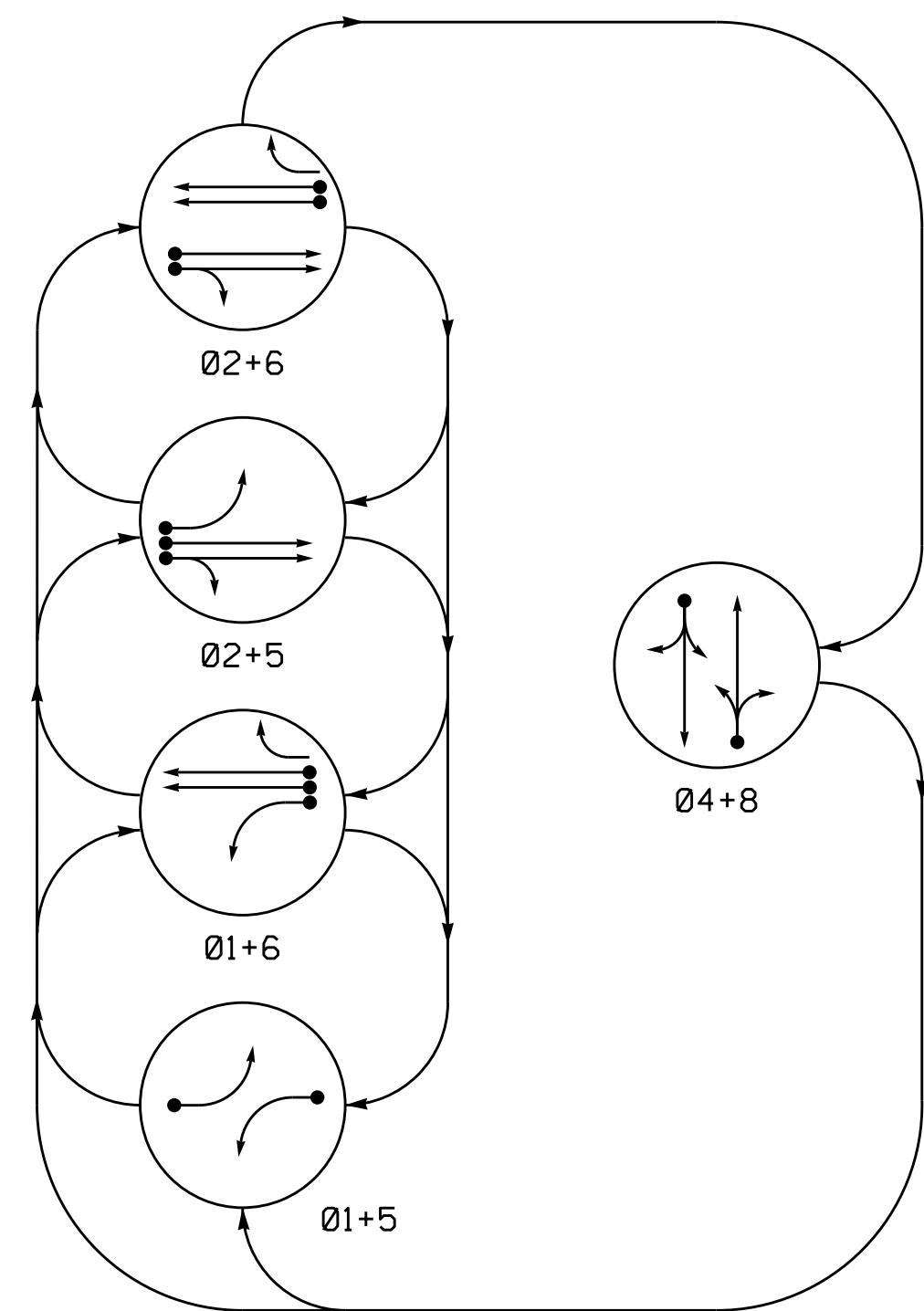
ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  KEITH M. MIMS ENGINEER 036880
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

SIG. INVENTORY NO. 07-1191T4

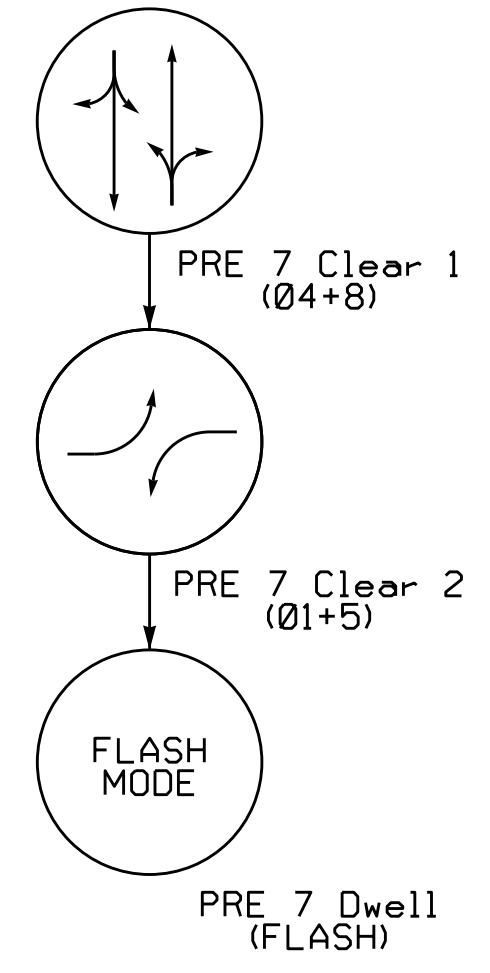
DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



LONG VEHICLE EXTENSION FAILURE PREEMPT PHASES



DEFAULT PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	04+8	PRE 7	PRE 7	F
11	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	Y	Y
41, 42, 43	R	R	R	R	G	G	R	R
51	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	Y	Y
81, 82, 83	R	R	R	R	G	G	R	R

ALTERNATE PHASING TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	04+8	PRE 7	PRE 7	F
11	-	-	-	-	-	-	-	-
21, 22, 23	R	R	G	G	R	R	Y	Y
41, 42, 43	R	R	R	R	G	G	R	R
51	-	-	-	-	-	-	-	-
61, 62, 63	R	G	R	G	R	R	Y	Y
81, 82, 83	R	R	R	R	G	G	R	R

5 Phase Fully Actuated W/ Long Vehicle Detection (Isolated)

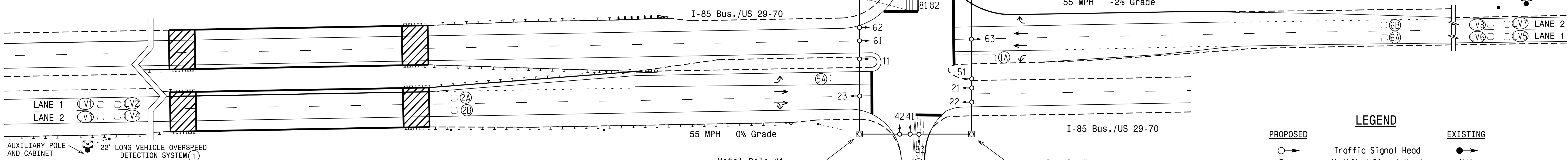
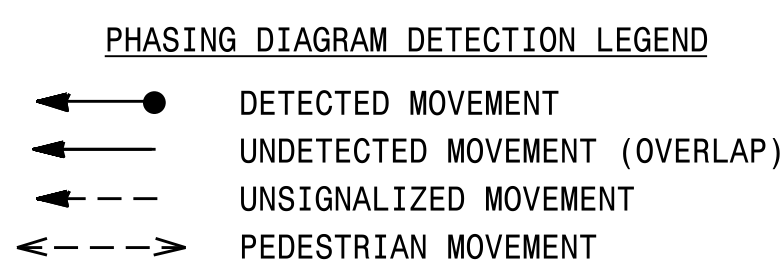
NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Set all detector units to presence mode.
- The Division Traffic Engineer will determine the hours of use for each phasing plan.

OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING				STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
					PHASE	CALLING EXTENSION	FULL TIME DELAY	STRETCH TIME				
1A	6X40	0	2-4-2	-	1	Y	Y	-	15 *	-	-	Y
2A	6X6	400	5	-	2	Y	Y	-	-	-	-	Y
2B	6X6	400	5	-	2	Y	Y	-	-	-	-	Y
4A	6X40	0	2-4-2	Y	4	Y	Y	-	10	-	-	Y
5A	6X40	0	2-4-2	-	5	Y	Y	-	15 *	-	-	Y
6A	6X6	420	5	-	6	Y	Y	-	-	-	-	Y
6B	6X6	420	5	-	6	Y	Y	-	-	-	-	Y
8A	6X40	0	2-4-2	-	8	Y	Y	-	10	-	-	Y

\* Disable Delay During Alternate Phasing Operation.  
# Disable Phase Call For Loop During Alternate Phasing Operation.



LONG VEHICLE EXTENSION FAILURE PREEMPT

FUNCTION	PRE 7
Interval 1 - Green Clear	15
Interval 1 - Yellow Clear	0.0*
Interval 1 - Red Clear	0.0*
Interval 2 - Green Clear	10
Interval 2 - Yellow Clear	0.0*
Interval 2 - Red Clear	0.0*
Interval 3 - Dwell Green	255
Interval 3 - Dwell Yellow	0.0*
Interval 3 - Dwell Red	0.0*
Interval 4 - Exit Green	1
Interval 4 - Yellow	0.0
Interval 4 - Red	0.0
Exit Phase(s)	2+6
Priority	-
Delay Time	0
Min Green Before Pre	14
Ped Clear Before Pre	0
Yellow Clear Before Pre	0.0*
Red Clear Before Pre	0.0*
Dwell Min Time	14
Flash Dwell Interval?	Y
Enable Backup Protection	N
Ped Clear Through Yellow	N
Omit Overlaps	-

OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	4	5	6	8		
Min Green 1 *	7	14	7	7	14	7		
Extension 1 *	2.0	6.0	3.0	2.0	6.0	3.0		
Max Green 1 *	20	120	25	25	120	25		
Yellow Clearance	3.0	5.4	4.3	3.0	5.4	3.6		
Red Clearance	2.3	1.0	1.9	2.8	1.0	2.0		
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0		
Walk 1 *	-	-	-	-	-	-		
Don't Walk 1	-	-	-	-	-	-		
Seconds Per Actuation *	-	1.5	-	-	1.5	-		
Max Variable Initial *	-	46	-	-	46	-		
Time Before Reduction *	-	20	-	-	20	-		
Time To Reduce *	-	50	-	-	50	-		
Minimum Gap	-	3.4	-	-	3.4	-		
Recall Mode	-	MIN RECALL	-	-	MIN RECALL	-		
Vehicle Call Memory	-	YELLOW	-	-	YELLOW	-		
Dual Entry	-	-	ON	-	-	ON		
Simultaneous Gap	ON	ON	ON	ON	ON	ON		

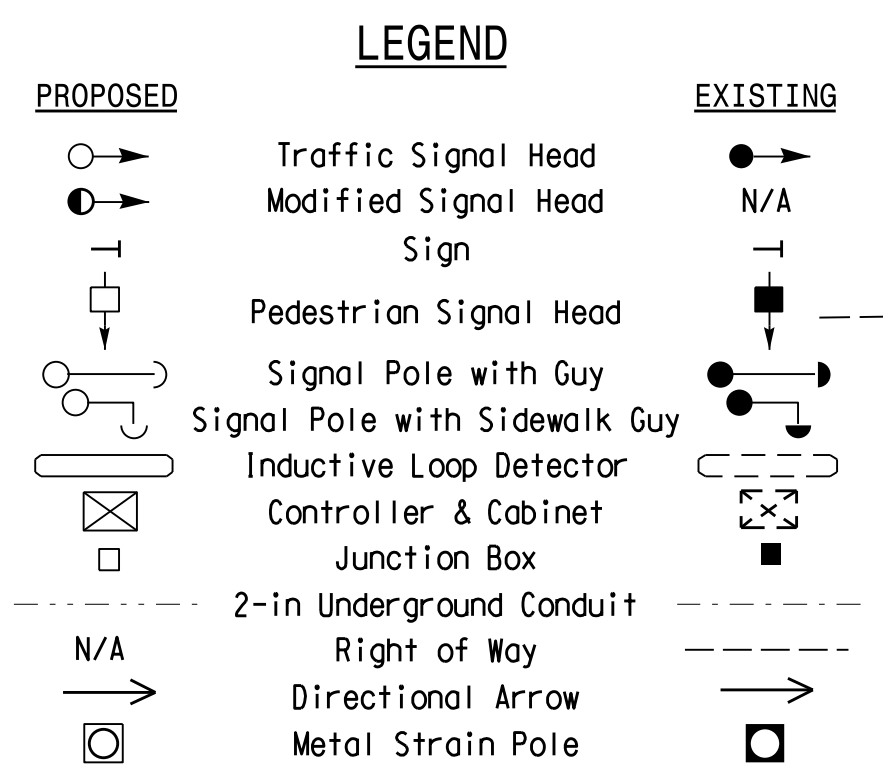
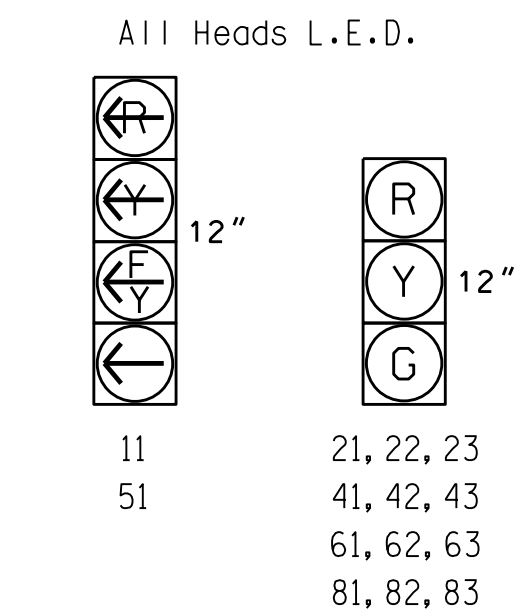
\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LONG VEHICLE OVERSPEED DETECTION SYSTEM LOOP & DETECTOR INSTALLATION CHART

LOOP NO.	SIZE (ft)	TURNS	DIST. FROM STOPBAR (ft)	NEW	EXISTING	LANE NO.	CHANNEL	NEMA PHASE	TIMING		PLACE CALL DURING PHASE	INHIBIT DELAY DURING GREEN?
									FEATURE	TIME		
LV1	6X6	4	1015	X	1	1	1	2*	NONE	- SEC.	ALL	NO
LV2	6X6	4	999	X	1	1	2	2*	NONE	- SEC.	ALL	NO
LV3	6X6	4	1015	X	2	2	2	2*	NONE	- SEC.	ALL	NO
LV4	6X6	4	999	X	2	2	2	2*	NONE	- SEC.	ALL	NO
LV5	6X6	4	1015	X	2	1	1	6*	NONE	- SEC.	ALL	NO
LV6	6X6	4	999	X	2	1	2	6*	NONE	- SEC.	ALL	NO
LV7	6X6	4	1015	X	2	2	1	6*	NONE	- SEC.	ALL	NO
LV8	6X6	4	999	X	2	2	2	6*	NONE	- SEC.	ALL	NO
LVDS THRESHOLD SPEED (MPH)	55								2	6		
LVDS EXTEND TIME	12 sec.								2	6		

\*Phase hold output to controller

SIGNAL FACE I.D.



Signal Upgrade - Final Design

I-85 Bus. /US 29-70 at SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: January 2018 REVIEWED BY: I. O. Umozurike

REVISIONS: [Table with columns for REVISIONS, INIT., DATE]

2/7/2018

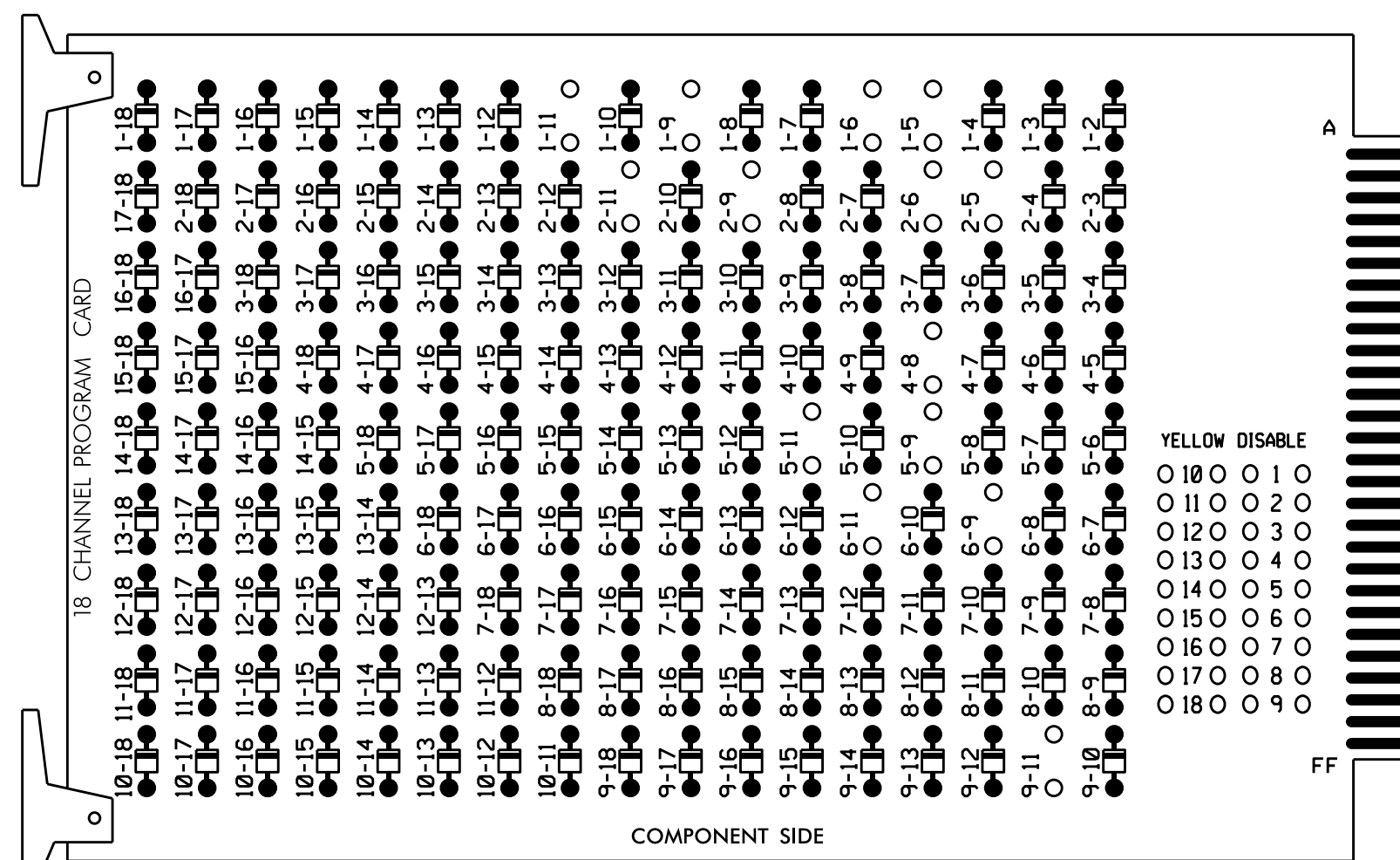
917-1191

07-FEB-2018 17:57  
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 cz:lemo

**EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

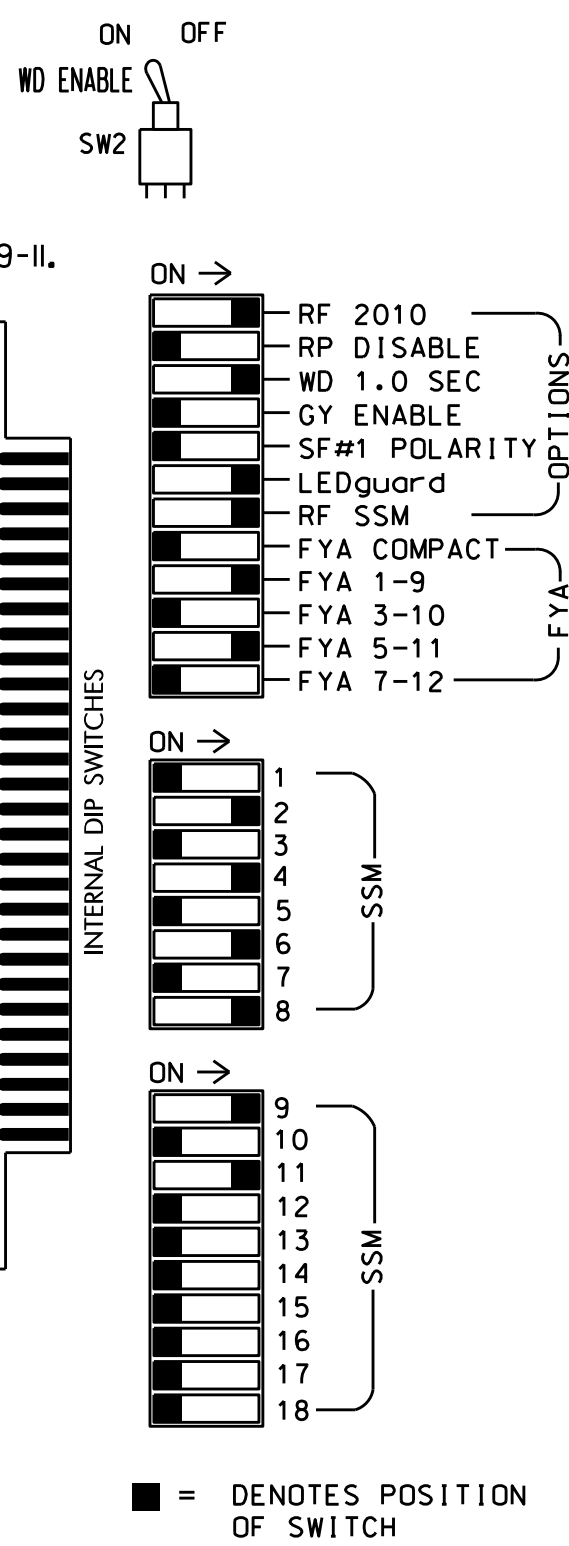
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUX S1,AUX S4  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22,23	NU	NU	41,42,43	NU	51	61,62,63	NU	NU	81,82,83	NU	11	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127							133										

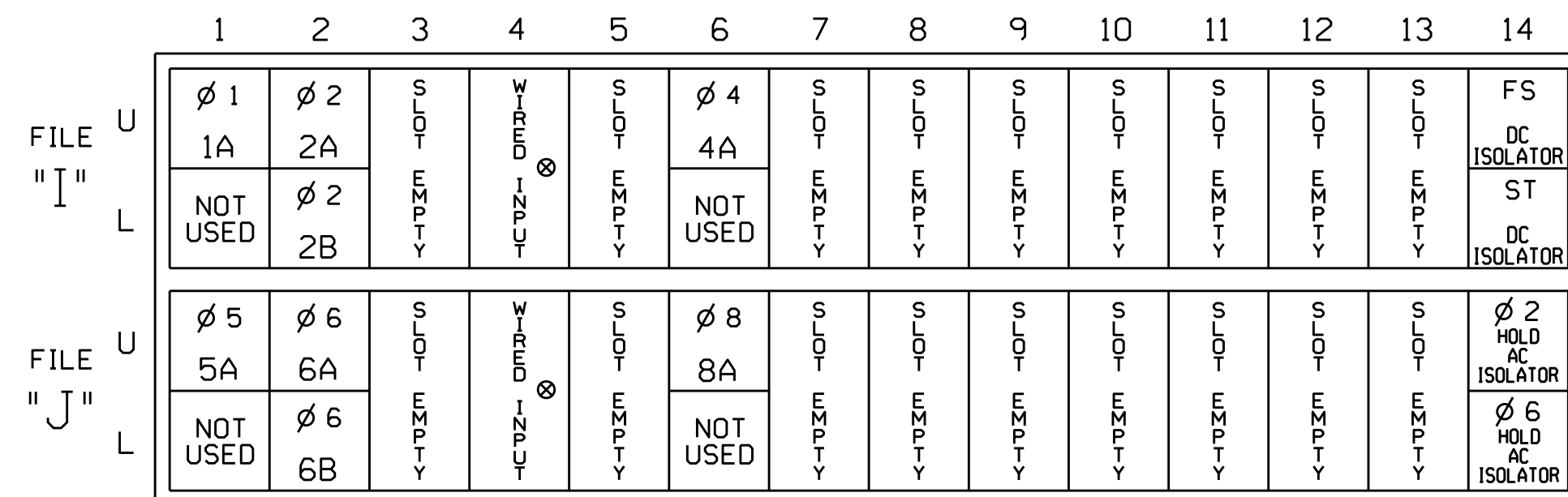
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME

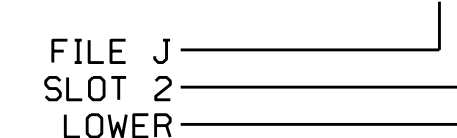
See AC Isolator programming detail on sheet 6.

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9★	22	2	Y	Y	Y		3
	-	J1U	55	17★	55	5	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

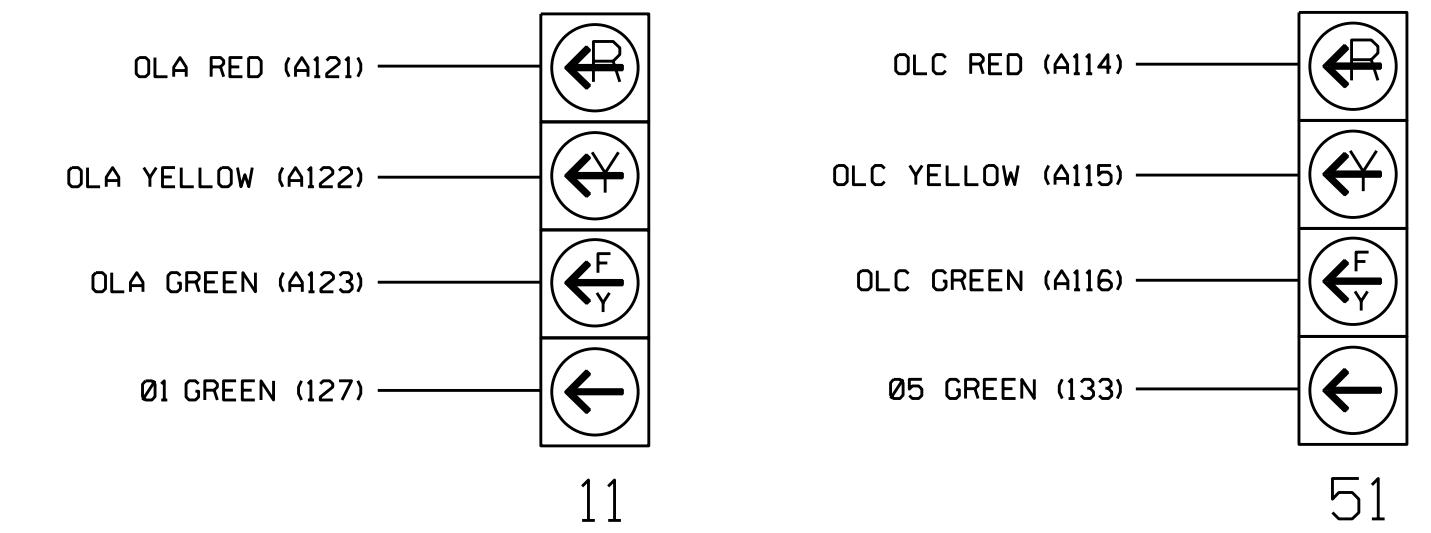
- Add jumper from I1-W to J4-W, on rear of input file.
  - Add jumper from J1-W to I4-W, on rear of input file.
- ★ See Input Page Assignment programming details on sheets 3 and 4.

**INPUT FILE POSITION LEGEND:**



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

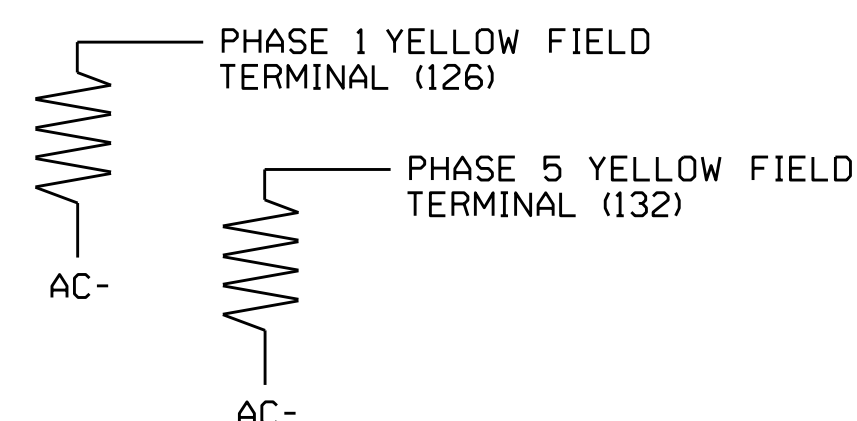
The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Final Design - Sheet 1 of 8

Electrical and Programming Details for: I-85 Bus. / US 29-70 at SR 1144 (River Road)

Prepared in the Offices of: Guilford County, North Carolina

Division 7, Guilford County, Jamestown

PLAN DATE: February 2018 REVIEWED BY: [Signature]  
 PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

REVISIONS: [Table]  
 INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL: KEITH M. MIMS, PROFESSIONAL ENGINEER, No. 036880

DocuSigned by: Keith M. Mims 2/8/2018

SIG. INVENTORY NO. 07-1191

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SEQUENCE, AND TO FLASH INTERSECTION IF LVOD SYSTEMS FAIL**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, AND 7.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #7 (+/-COMMAND#)  
IF INPUT ASSIGNMENT #13 IS ON  
OR INPUT ASSIGNMENT #14 IS ON

↓  
SCROLL DOWN

THEN:  
DELAY FOR 240.0 SECONDS  
SET INPUT ASSIGNMENT #64 ON

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**I/O REFERENCE SCHEDULE**

INPUT 13 = Input from LVODS #1  
INPUT 14 = Input from LVODS #2  
INPUT 64 = Preempt 7  
OUTPUT 42 = Overlap C Red  
OUTPUT 43 = Overlap C Yellow  
OUTPUT 44 = Overlap C Green  
OUTPUT 50 = Overlap A Red  
OUTPUT 51 = Overlap A Yellow  
OUTPUT 52 = Overlap A Green

INPUTS 13, 14, AND 64 HAVE BEEN REASSIGNED. SEE SHEET 6 FOR PROGRAMMING DETAILS.

**OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS), PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: XX  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

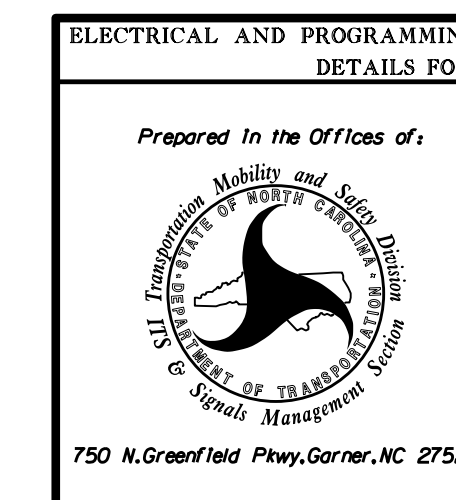
NOTICE PAGE 2 →

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: 12345678910111213141516  
VEH OVL PARENTS: X  
VEH OVL NOT VEH:  
VEH OVL NOT PED:  
VEH OVL GRN EXT:  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN  
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0.0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

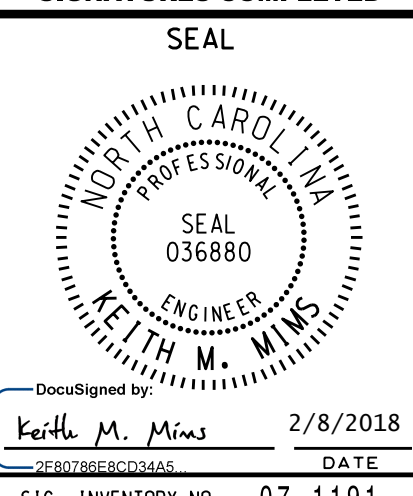
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Final Design - Sheet 2 of 8



DETAILS FOR: I-85 Bus. / US 29-70 at SR 1144 (River Road)	
Division 7	Guilford County
PLAN DATE: February 2018	REVIEWED BY:
PREPARED BY: S. Armstrong	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

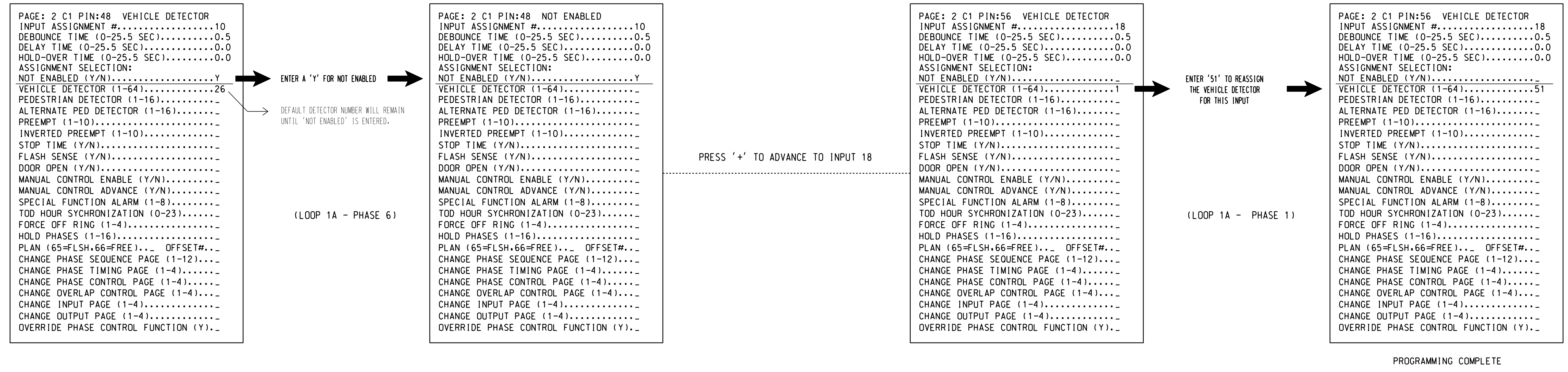


**INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A**

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

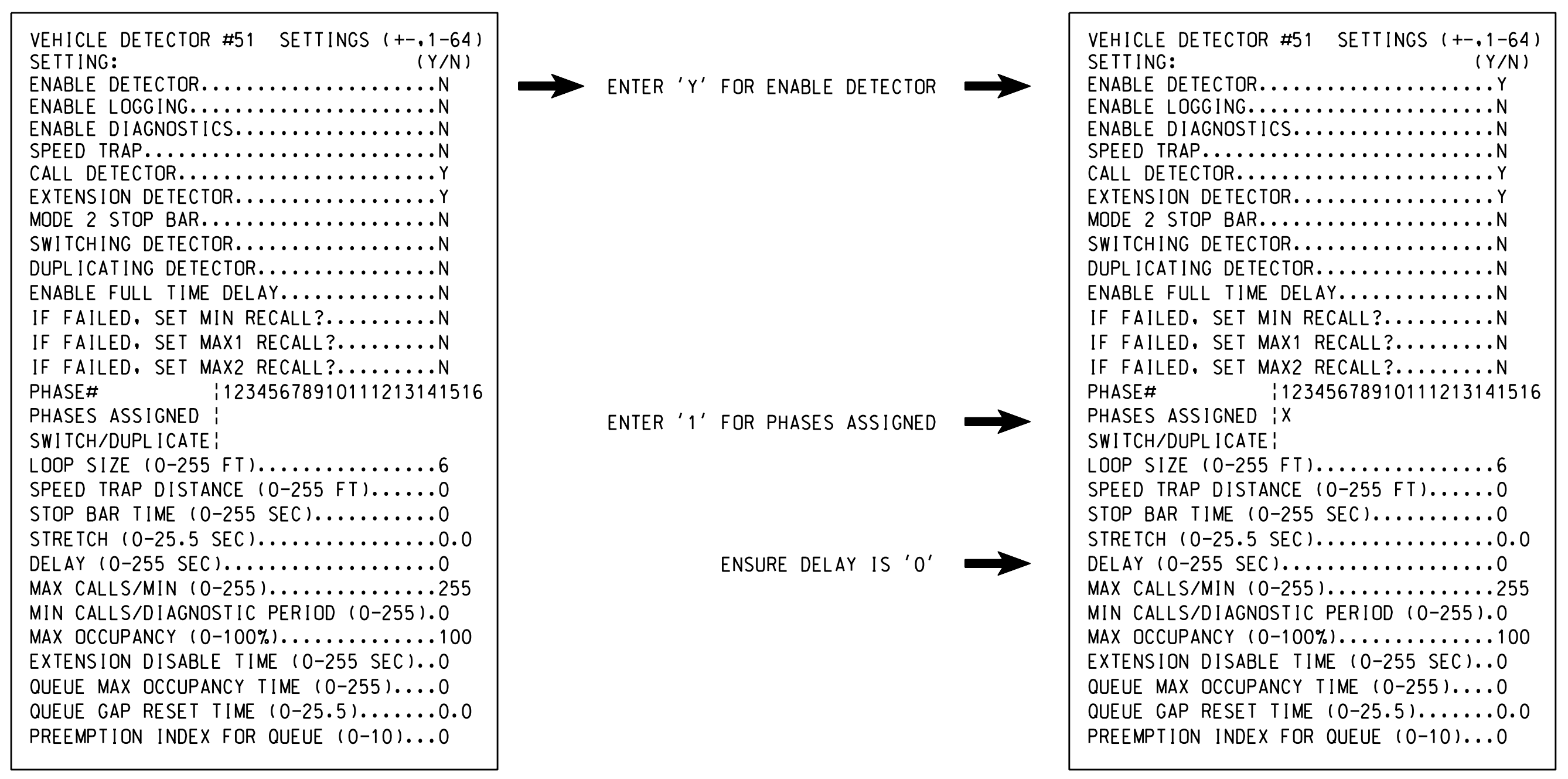
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.



**SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)**

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

08-FEB-2018 11:44 C:\P\TSS\W\TSS\Sig\Signal\work\hgr\oups\sig\Map\hgr\stron\071191\_sml.e\cxxx.dgn sarmstrong

Electrical Detail - Final Design - Sheet 3 of 8

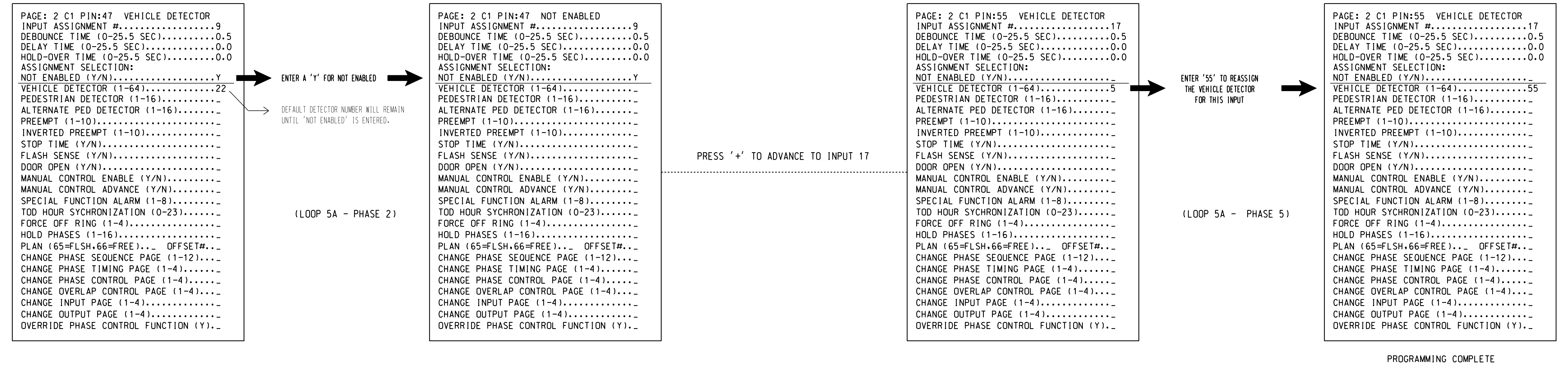
	I-85 Bus. / US 29-70		
	at SR 1144 (River Road)		
Prepared In the Offices of: Guilford County 750 N. Greenfield Pkwy, Garner, NC 27529	PLAN DATE: February 2018 PREPARED BY: S. Armstrong	REVIEWED BY: REVIEWED BY:	Division 7 Guilford County Jamestown
REVISIONS:		INIT. DATE	DocuSigned by: Keith M. Mims 2/8/2018
SIG. INVENTORY NO. 07-1191		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED	

INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 5A

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #9 (DETECTOR 22) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 2 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 55 TO INPUT #17 SO THAT THE DELAY ON LOOP 5A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 9 IS REACHED.

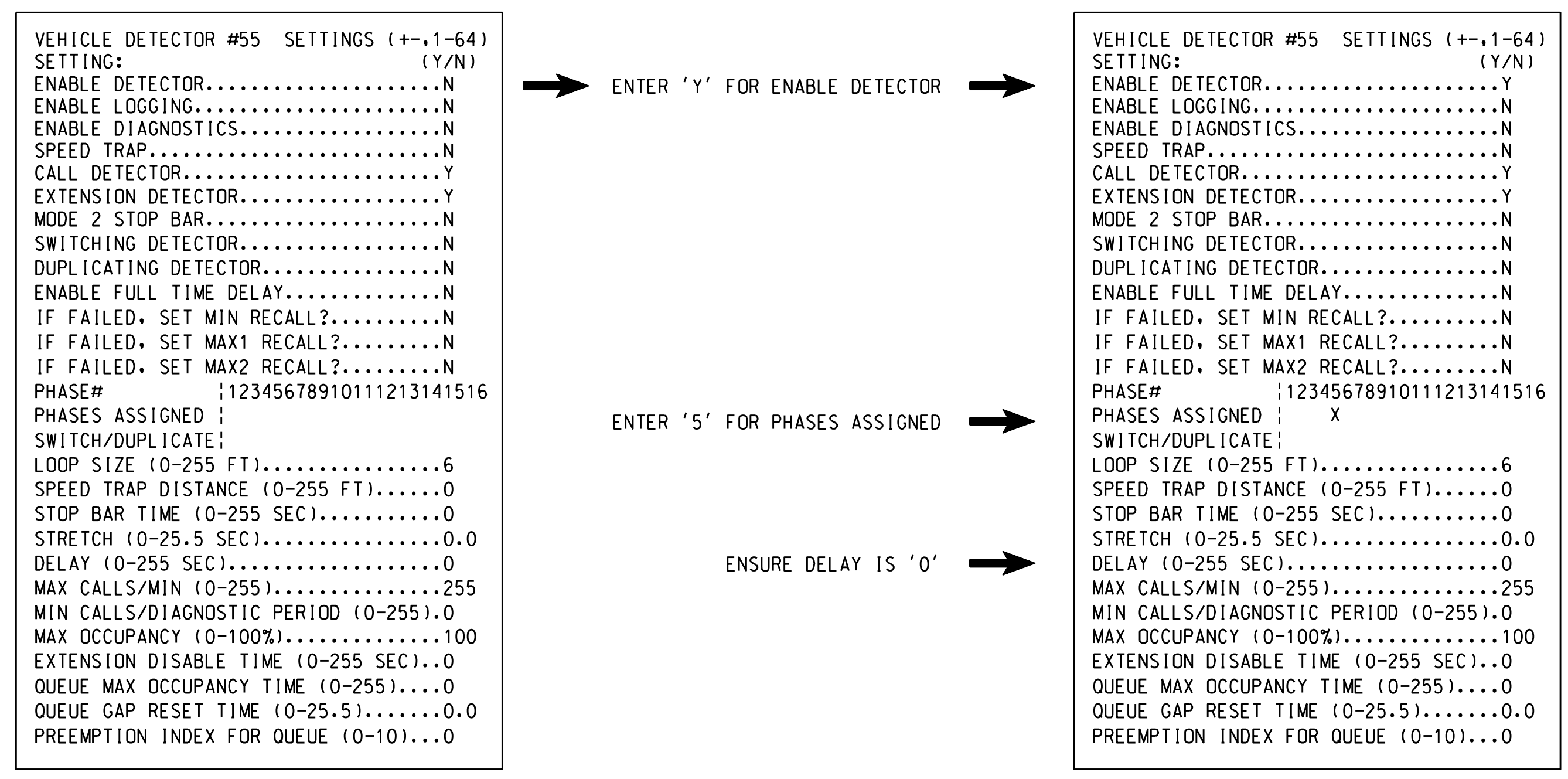


PROGRAMMING COMPLETE

SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 5A (ALT.)

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #55.



DETECTOR PROGRAMMING COMPLETE

NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

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Electrical Detail - Final Design - Sheet 4 of 8

	I-85 Bus. / US 29-70		
	at SR 1144 (River Road)		
Prepared In the Offices of: Keith M. Mims Professional Engineer 750 N. Greenfield Pkwy, Garner, NC 27529	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	Jamestown DATE: 2/8/2018 DATE:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 07-1191



## ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

<u>PHASING</u>	<u>INPUTS PAGE</u>	<u>OVERLAPS PAGE</u>
ACTIVE PAGES REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	1
ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	2

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

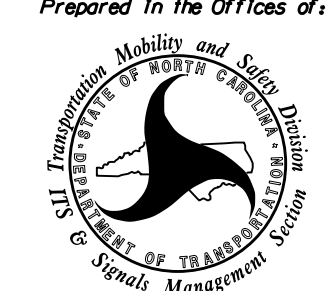
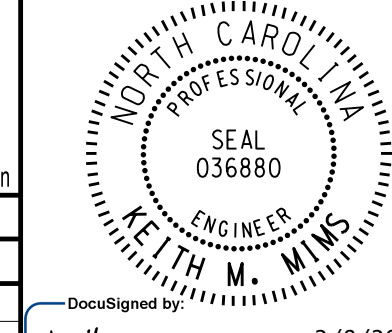
OVERLAPS PAGE 2: Modifies overlap parent phases for heads 11 and 51 to run protected turns only.

INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

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Electrical Detail - Final Design - Sheet 5 of 8		<b>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</b>
ELECTRICAL AND PROGRAMMING DETAILS FOR:  Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	<b>I-85 Bus. / US 29-70</b> at <b>SR 1144 (River Road)</b>  Division 7      Guilford County      Jamestown PLAN DATE: February 2018      REVIEWED BY: PREPARED BY: S. Armstrong      REVIEWED BY: REVISIONS      INIT.      DATE	SEAL  Keith M. Mins      2/8/2018 2F80786E8CD3425      DATE SIG. INVENTORY NO. 07-1191

**INPUT ASSIGNMENT PROGRAMMING DETAIL TO REASSIGN LONG VEHICLE OVERSPEED DETECTION SYSTEM FUNCTION**  
(program controller as shown below)

This programming takes each of the Long Vehicle Overspeed Detection System inputs and reassigns a unique Hold Phase to it.

FROM MAIN MENU PRESS '5' (INPUTS), THEN '+' UNTIL PIN 51 (INPUT 13) IS REACHED.

```

PAGE: 1 C1 PIN:51 HOLD PHASES
INPUT ASSIGNMENT #.....13
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....2
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)...
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+'

INPUT 13 IS THE OUTPUT FROM LVODS #1

```

PAGE: 1 C1 PIN:52 HOLD PHASES
INPUT ASSIGNMENT #.....14
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....6
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)...
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PRESS '+' OR '-' TO REACH INPUT 64

INPUT 14 IS THE OUTPUT FROM LVODS #2

```

PAGE: 1 C1 PIN:0 PREEMPT
INPUT ASSIGNMENT #.....64
DEBOUNCE TIME (0-25.5 SEC).....0.5
DELAY TIME (0-25.5 SEC).....0.0
HOLD-OVER TIME (0-25.5 SEC).....0.0
ASSIGNMENT SELECTION:
NOT ENABLED (Y/N).....
VEHICLE DETECTOR (1-64).....
PEDESTRIAN DETECTOR (1-16).....
ALTERNATE PED DETECTOR (1-16).....
PREEMPT (1-10).....7
INVERTED PREEMPT (1-10).....
STOP TIME (Y/N).....
FLASH SENSE (Y/N).....
DOOR OPEN (Y/N).....
MANUAL CONTROL ENABLE (Y/N).....
MANUAL CONTROL ADVANCE (Y/N).....
SPECIAL FUNCTION ALARM (1-8).....
TOD HOUR SYNCHRONIZATION (0-23).....
FORCE OFF RING (1-4).....
HOLD PHASES (1-16).....
PLAN (65=FLSH,66=FREE)... OFFSET#...
CHANGE PHASE SEQUENCE PAGE (1-12)...
CHANGE PHASE TIMING PAGE (1-4).....
CHANGE PHASE CONTROL PAGE (1-4).....
CHANGE OVERLAP CONTROL PAGE (1-4)...
CHANGE INPUT PAGE (1-4).....
CHANGE OUTPUT PAGE (1-4).....
OVERRIDE PHASE CONTROL FUNCTION (Y)...
    
```

PROGRAMMING COMPLETE

← Notice Preempt 7

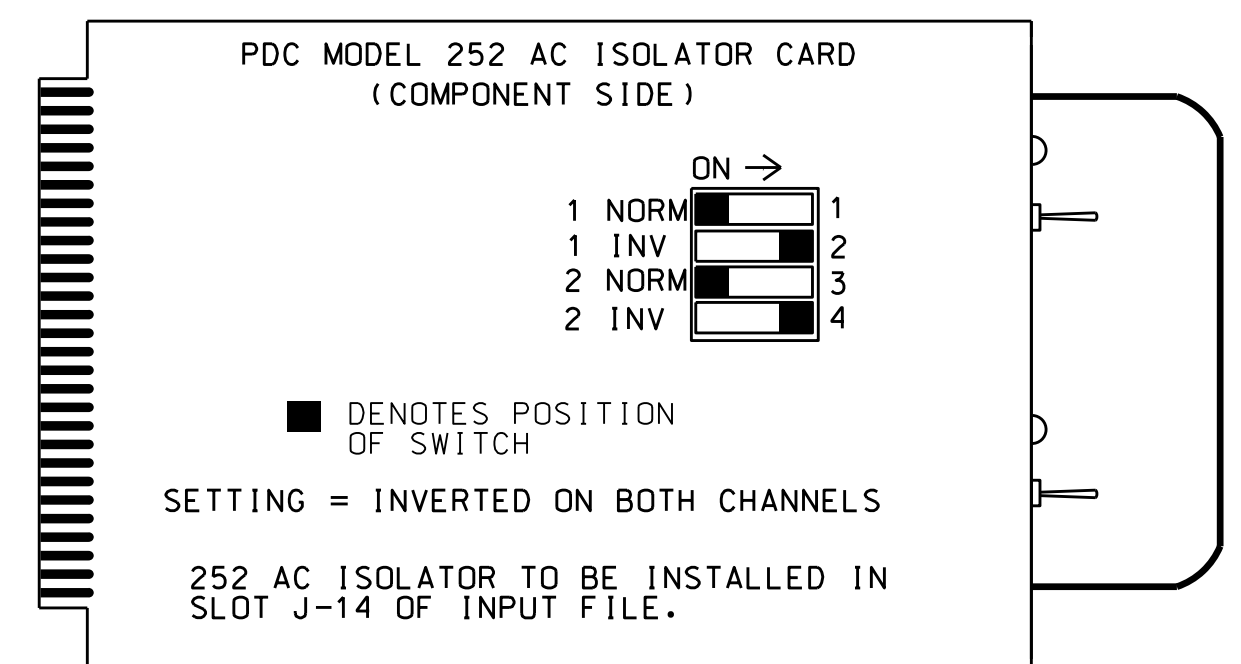
**PREEMPT 7 PROGRAMMING DETAIL**  
(program controller as shown below)

THIS PREEMPT GOES ACTIVE IF EITHER LVODS HAS BEEN ACTIVE FOR MORE THAN 4 CONSECUTIVE MINUTES AND WILL PUT THE INTERSECTION IN FLASH.

FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTIONS), PRESS 'NEXT' UNTIL PREEMPTION #7 IS REACHED.

PREEMPTION #7	SETTINGS (NEXT:1-10)
INTERVAL/TIMING	CLEAR/DWELL PHASES
GRN YEL RED	12345678910111213141516
1 15 0.0 0.0	X X
2 10 0.0 0.0	X X
3 255 0.0 0.0	X X
4 0 0.0 0.0	X X
5 1 0.0 0.0	X X
EXIT CALLS	
OPTIONS	
PRIORITY (Y/N TO SELECT)	.....HIGH
DELAY TIMER (0-255 SEC)	.....0.0
MIN GREEN BEFORE PRE (0= DEFAULT)	.....14
PED CLEAR BEFORE PRE (0= DEFAULT)	.....0
YELLOW CLEAR BEFORE PRE (0= DEFAULT)	.....0.0
RED CLEAR BEFORE PRE (0= DEFAULT)	.....0.0
DWELL MIN TIMER (0-255 SEC)	.....14
DWELL MAX TIMER (0=OFF,1-255MIN)	.....0
DWELL HOLD-OVER TIMER (0-255)	.....0
LATCH CALL?	.....N
LINK TO NEXT PREEMPT?	.....N
ENABLE BACKUP PROTECTION?	.....N
HOLD CLEAR 1 PHASES DURING DELAY?	.....N
FAST GREEN FLASH DWELL PHASES?	.....N
PED CLEARANCE THROUGH YELLOW?	.....N
INHIBIT OVERLAP GREEN EXTENSION?	.....N
SERVICE DURING SOFTWARE FLASH?	.....N
REST IN RED DURING DWELL INTERVAL?	.....N
FLASH DWELL INTERVAL?	.....Y
ALLOW PEDS IN DWELL INTERVAL?	.....Y
RE-TIME DWELL INTERVAL?	.....Y
OVERLAPS:	ABCDEFGHIJKLMNOP
DWELL INT FLASH YELLOW	X X
OMIT OVERLAPS:	

**HOLD PHASE AC ISOLATOR (MODEL 252)**  
**OUTPUT PROGRAMMING DETAIL**  
(set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
DESIGNED: January 2018  
SEALED: 2/7/2018  
REVISED: N/A

Electrical Detail - Final Design - Sheet 6 of 8

Prepared In the Offices of:  
Gulf Transportation Mobility and Safety Division  
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION  
Signal Management Section  
750 N. Greenfield Pkwy, Garner, NC 27529

Electrical and Programming Details For: I-85 Bus. / US 29-70 at SR 1144 (River Road)

Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY:  
PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSigned by: Keith M. Mims 2/8/2018 2:40:17 PM  
SIG. INVENTORY NO. 07-1191

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

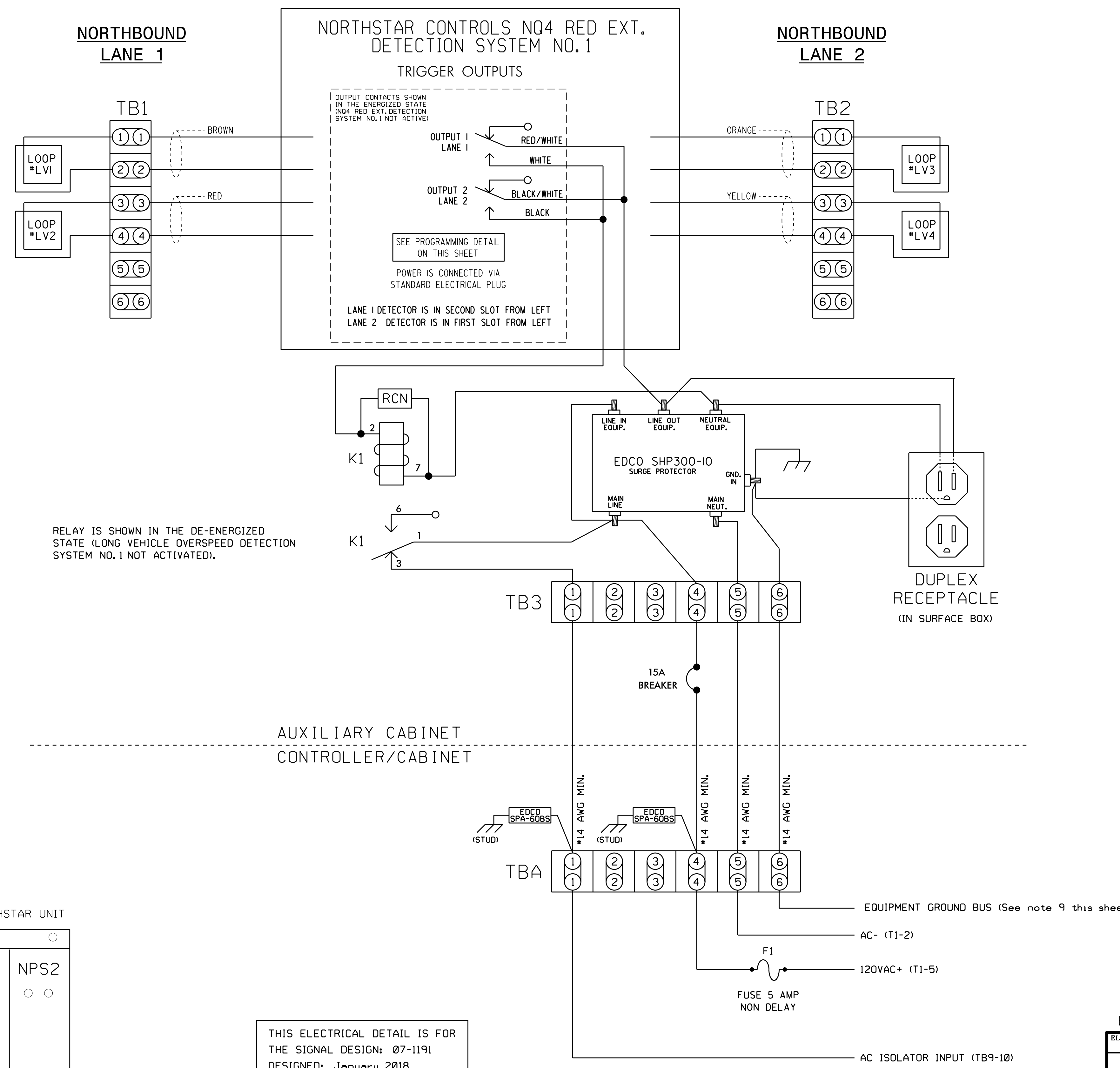
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NORTH CAROLINA PROFESSIONAL ENGINEER  
KEITH M. MIMS  
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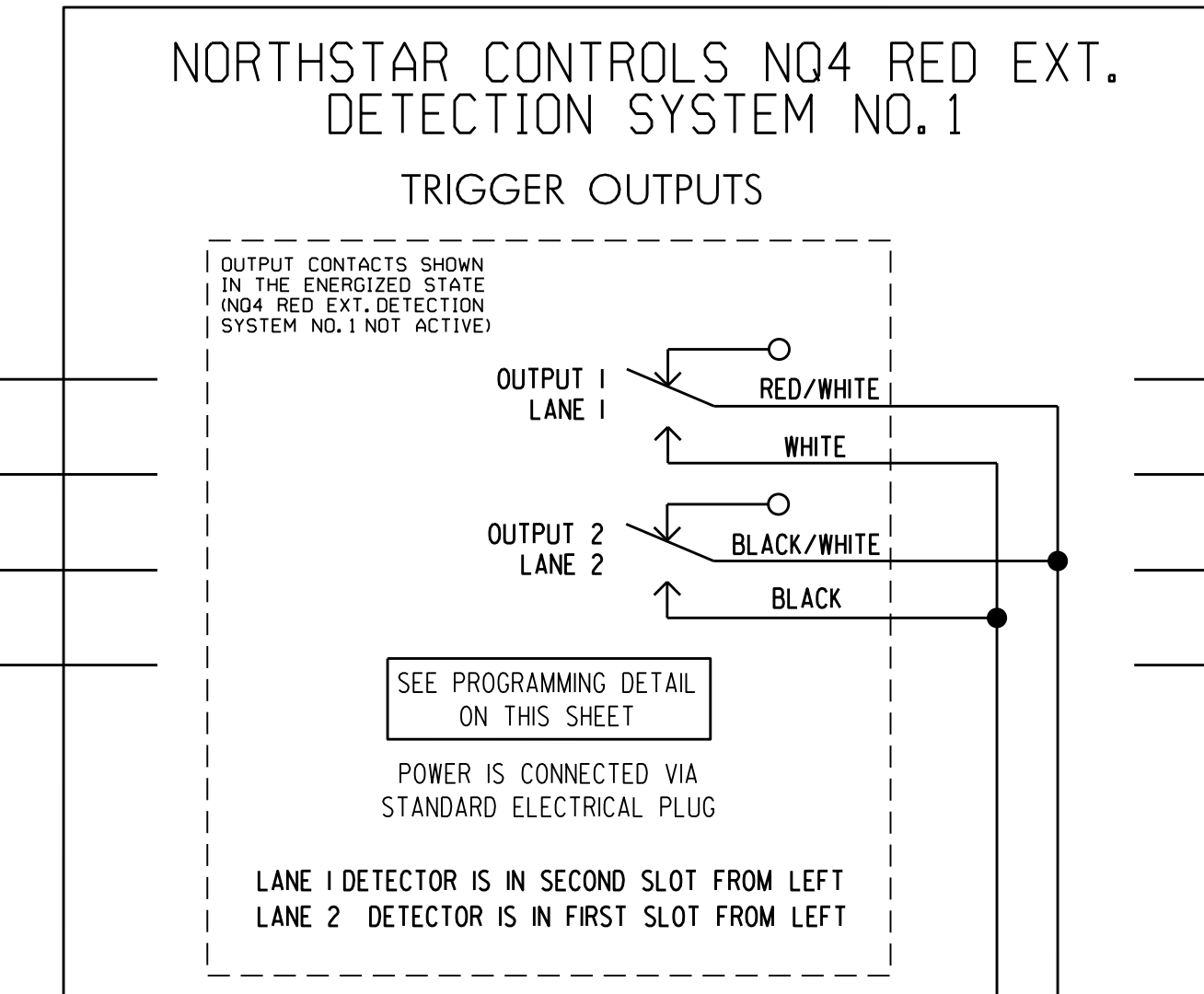
(wire unit as shown below)

**NOTES**

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBA to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.
11. IMPORTANT! A jumper must be installed between Input File terminals J14-E and J14-K.
12. IMPORTANT! For proper operation of the Dynamic Red Extension System, tie TB9-12 to AC neutral.
13. IMPORTANT! Make sure both channels of the AC Isolator card inserted in Input File slot J14 are set for INVERTED OUTPUT operation. See sheet 6 of this electrical detail.



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 1 NOT ACTIVATED).



**NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL**

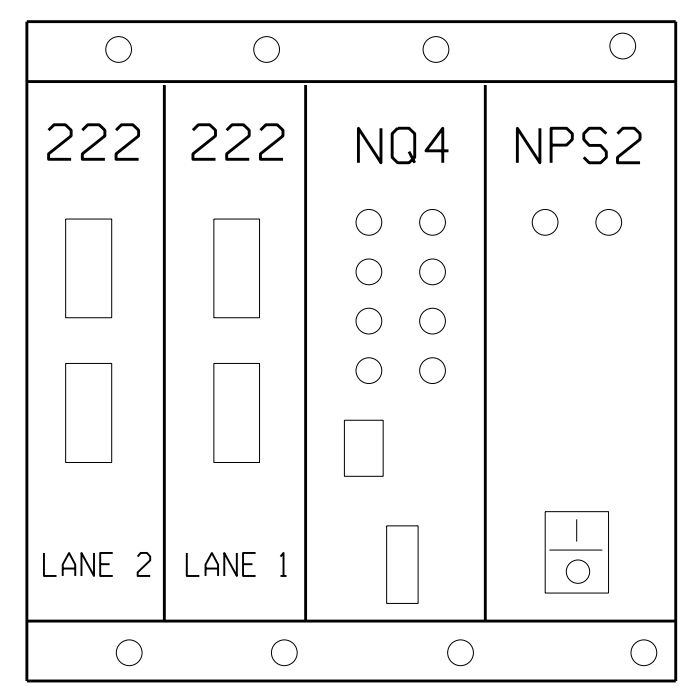
(program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

Program the NQ4 by typing the following commands:

1. SET SPEED = 55
2. SET LENGTH = 22'
3. SET ALARMTIME = 12
4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
5. SET LOOP LENGTH = 6' (program actual measured loop length)
6. SAVE

FRONT VIEW OF NORTHSTAR UNIT



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

Electrical Detail - Final Design - Sheet 7 of 8

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  Keith M. Mims 2/8/2018
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	

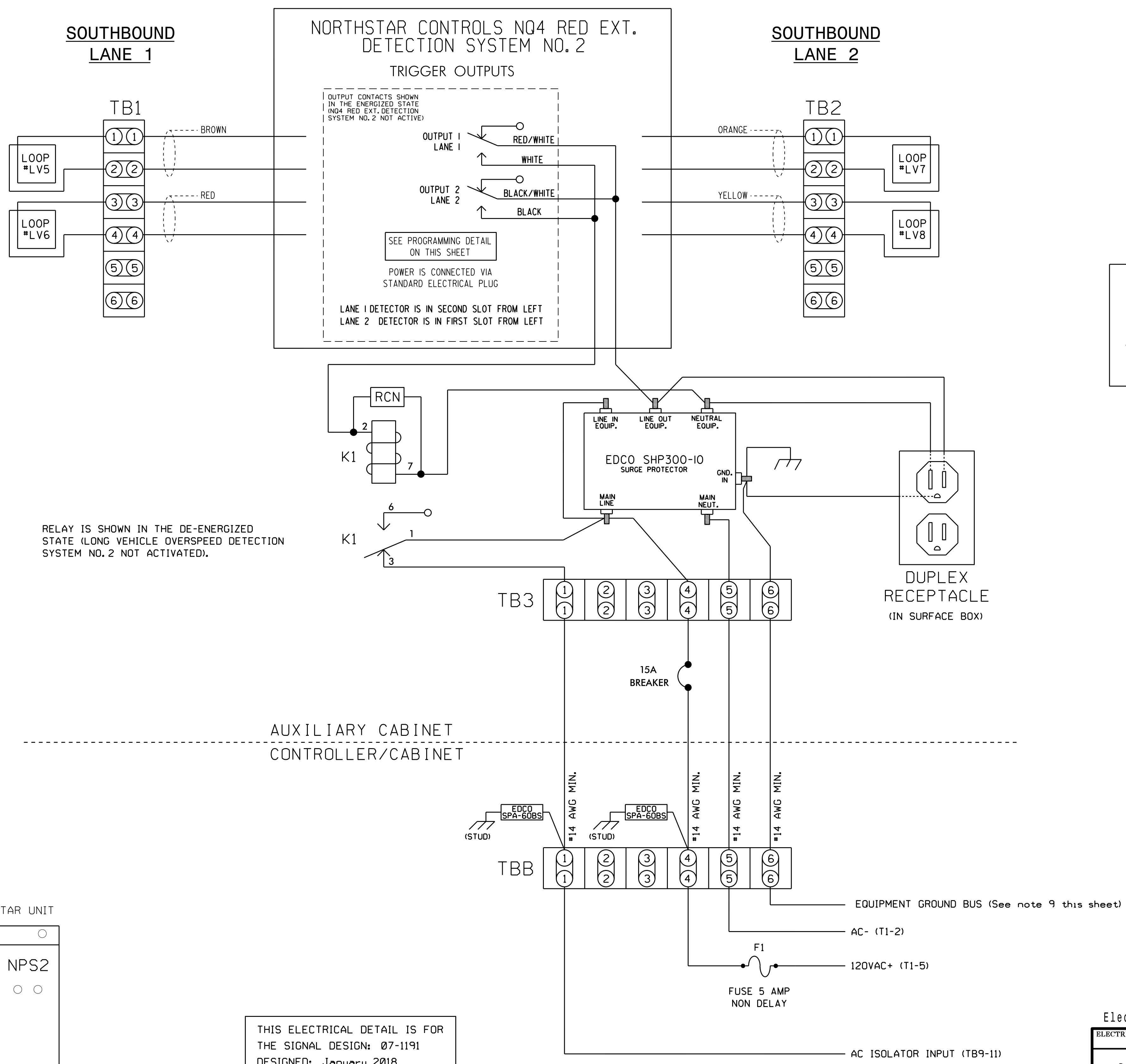
SIG. INVENTORY NO. 07-1191

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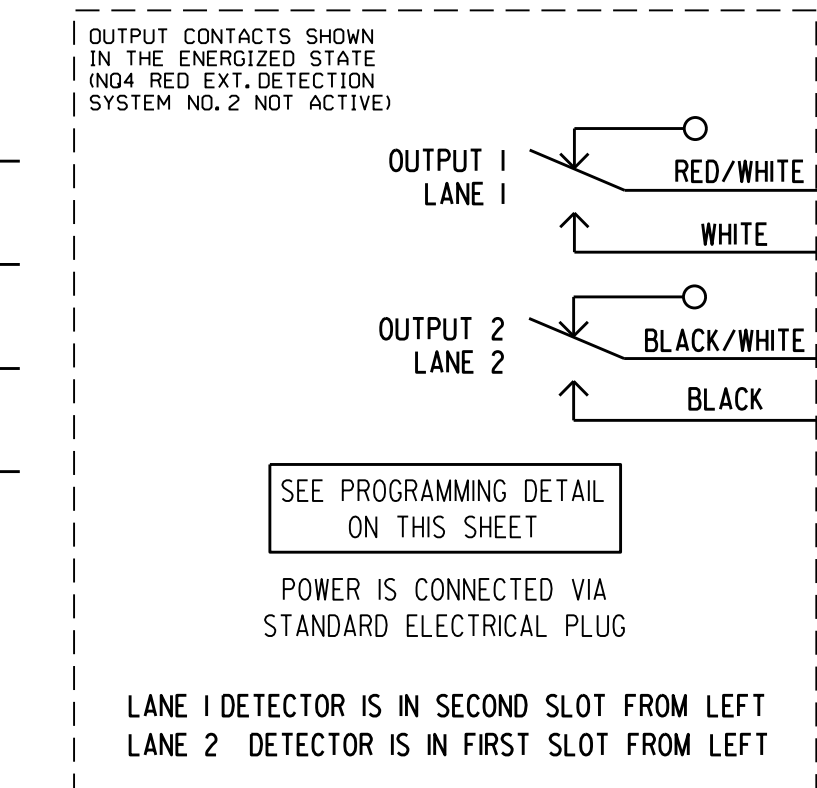
**NOTES**

1. Loop spacing is critical to the proper operation of this Overspeed Detection system. Make sure loop spacing is correctly programmed in the NQ4 unit.
2. The NQ4 unit shall be located in an auxiliary cabinet adjacent to the Long Vehicle Overspeed Detection loops.
3. Relay K1 is an enclosed SPDT general purpose relay with a 120VAC coil, 10A contacts, and octal style plug.
4. The RC network across the coil of K1 is 0.1 micro farad, 100 ohm.
5. EDCO SPA-60BS is a surge protector for 120VAC interconnect circuits.
6. EDCO SHP300-10 is an AC service surge protector.
7. Terminal strips TB1, TB2, TB3, and TBB to be added by the installer.
8. DO NOT install ground rods at the auxiliary cabinet.
9. Install equipment ground from the controller cabinet to the auxiliary cabinet if not already present.
10. Install a disconnect at the auxiliary cabinet if there is no disconnect already present.

(wire unit as shown below)



RELAY IS SHOWN IN THE DE-ENERGIZED STATE (LONG VEHICLE OVERSPEED DETECTION SYSTEM NO. 2 NOT ACTIVATED).



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1191  
 DESIGNED: January 2018  
 SEALED: 2/7/2018  
 REVISED: N/A

**NORTHSTAR CONTROLS MODEL NQ4 PROGRAMMING DETAIL**

(program unit as shown below)

NOTE: Unit must be programmed using a PC and a terminal emulator program. For connection to the terminal emulator, refer to the NQ4 operation manual.

- Program the NQ4 by typing the following commands:
1. SET SPEED = 55
  2. SET LENGTH = 22'
  3. SET ALARMTIME = 12
  4. SET SEPARATION = 16' (leading edge to leading edge - program actual measured separation)
  5. SET LOOP LENGTH = 6' (program actual measured loop length)
  6. SAVE

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Electrical Detail - Final Design - Sheet 8 of 8

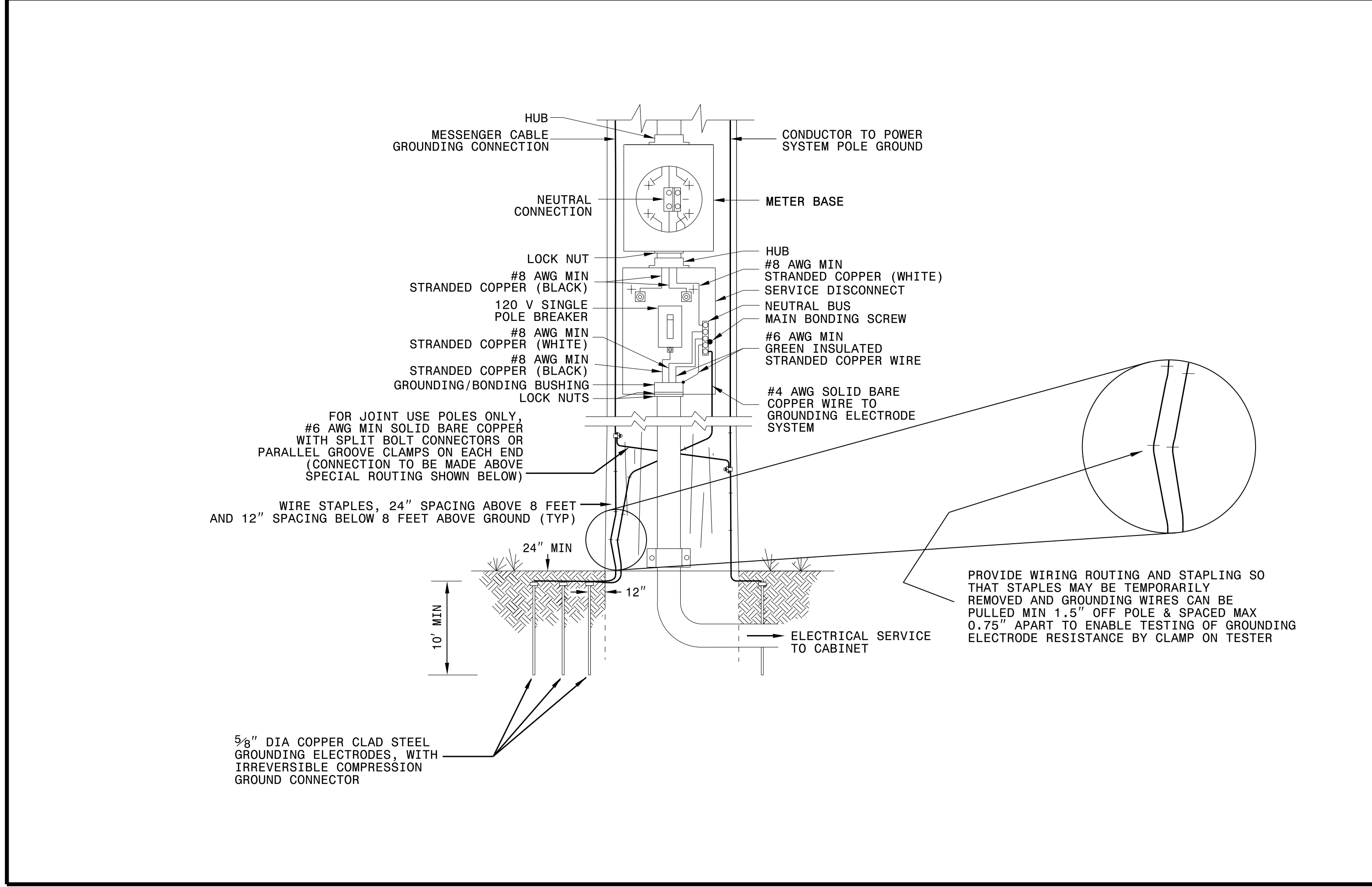
ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  KEITH M. MIMS ENGINEER 2/8/2018 DATE
	PLAN DATE: February 2018 PREPARED BY: S. Armstrong	REVIEWED BY: REVIEWED BY:	

SIG. INVENTORY NO. 07-1191

1-18 STATE OF NORTH CAROLINA DEPT. OF TRANSPORTATION DIVISION OF HIGHWAYS RALEIGH, N.C.

ENGLISH STANDARD DRAWING FOR  
**ELECTRICAL SERVICE GROUNDING**  
GROUNDING AND BONDING

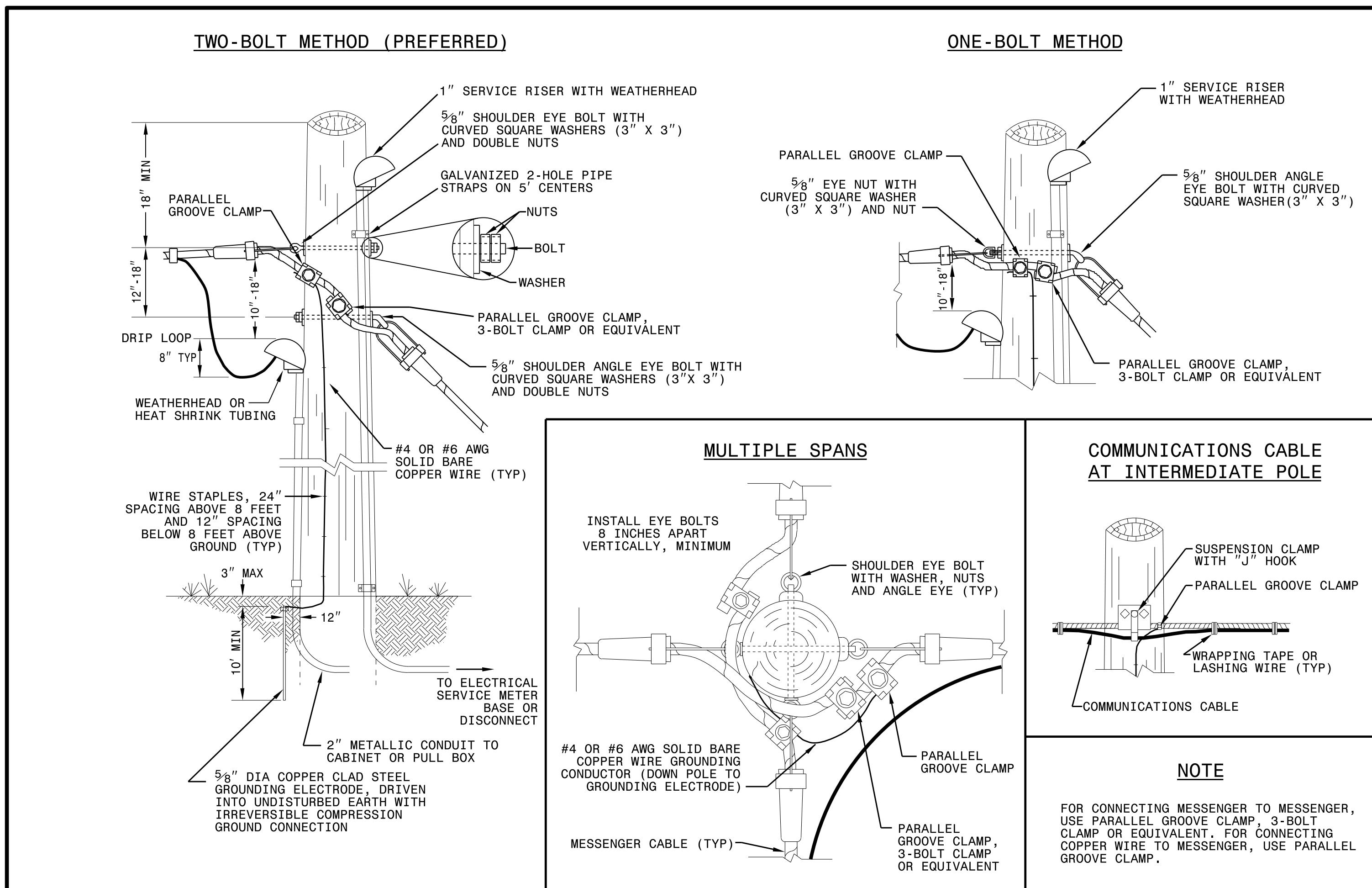
SHEET 1 OF 1  
**1700D01**



1-18 STATE OF NORTH CAROLINA DEPT. OF TRANSPORTATION DIVISION OF HIGHWAYS RALEIGH, N.C.

ENGLISH STANDARD DRAWING FOR  
**WOOD POLES**  
METHODS OF ATTACHMENT AND GROUNDING

SHEET 1 OF 1  
**1720D01**



DOCUMENT NOT CONSIDERED  
FINAL UNLESS ALL  
SIGNATURES COMPLETED

See Plate for Title

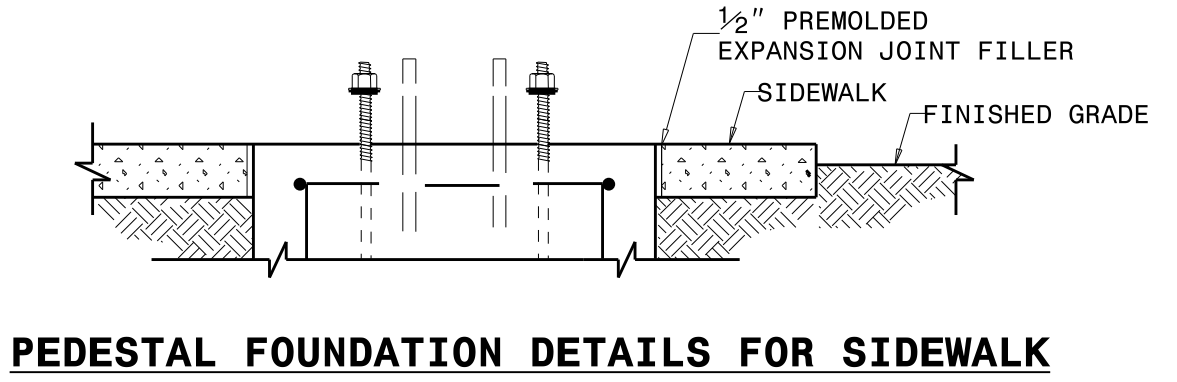
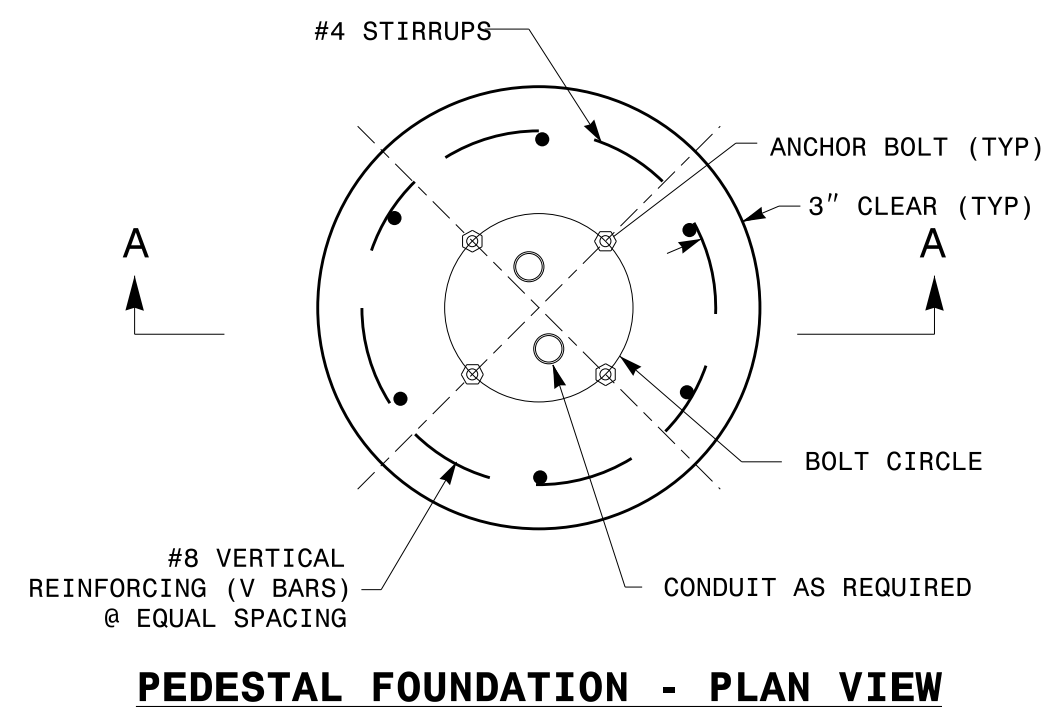
Prepared in the Offices of:

SEAL  
NORTH CAROLINA  
PROFESSIONAL  
ENGINEER  
MOHD A. ASLAMI  
032108

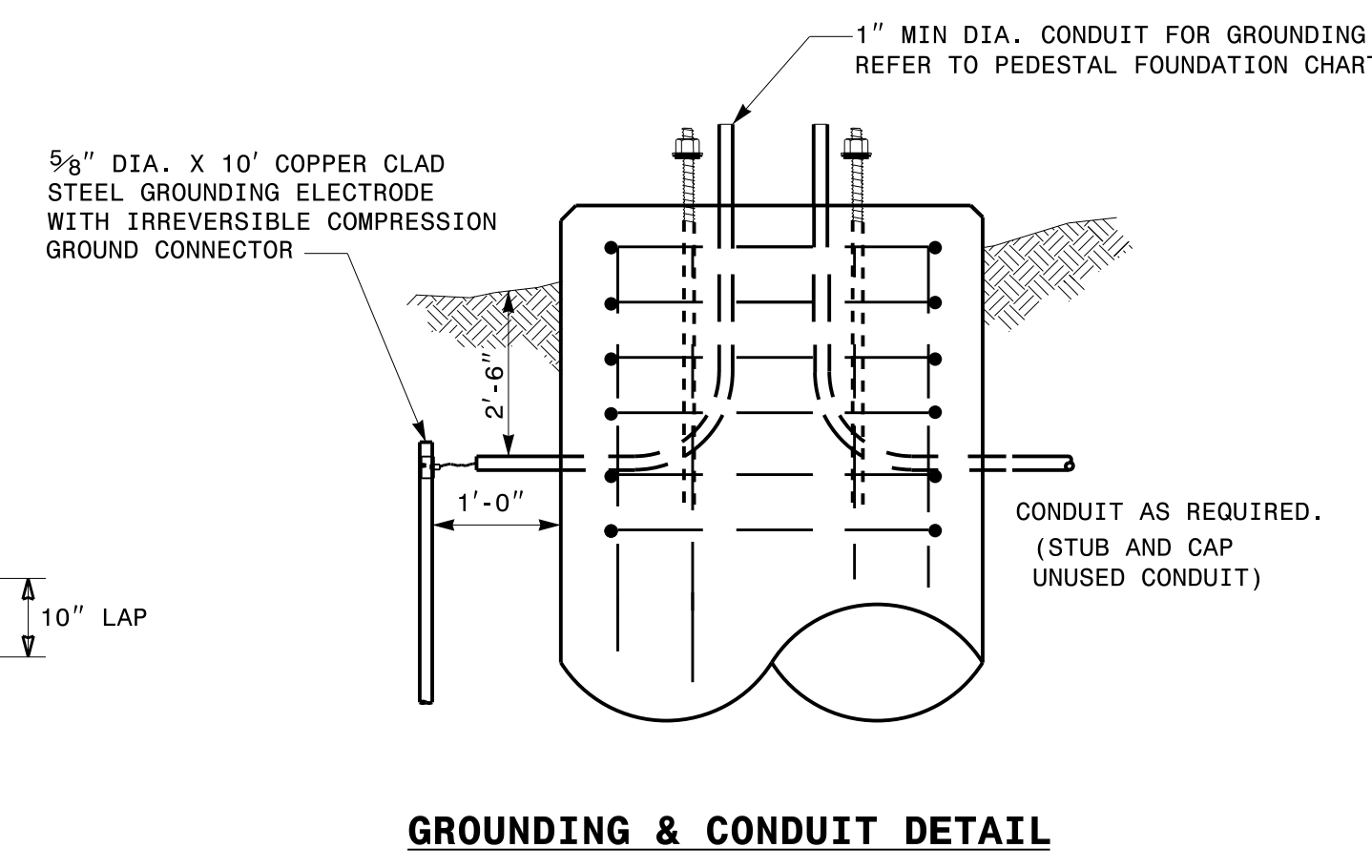
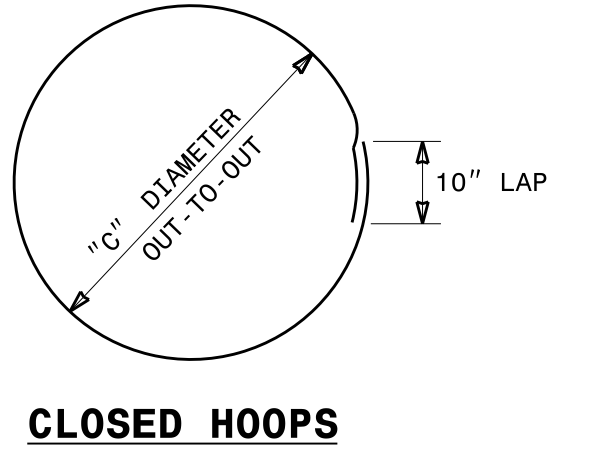
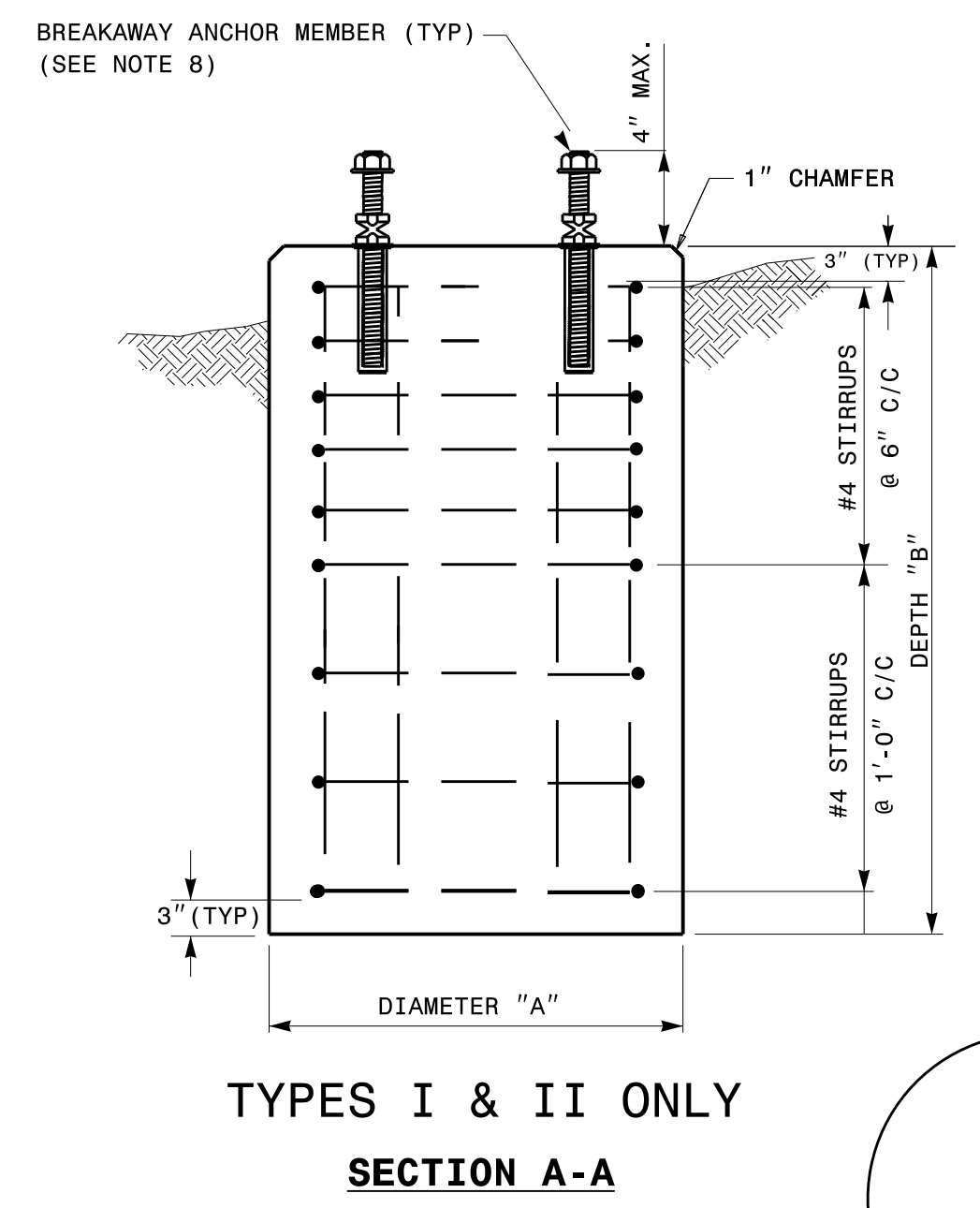
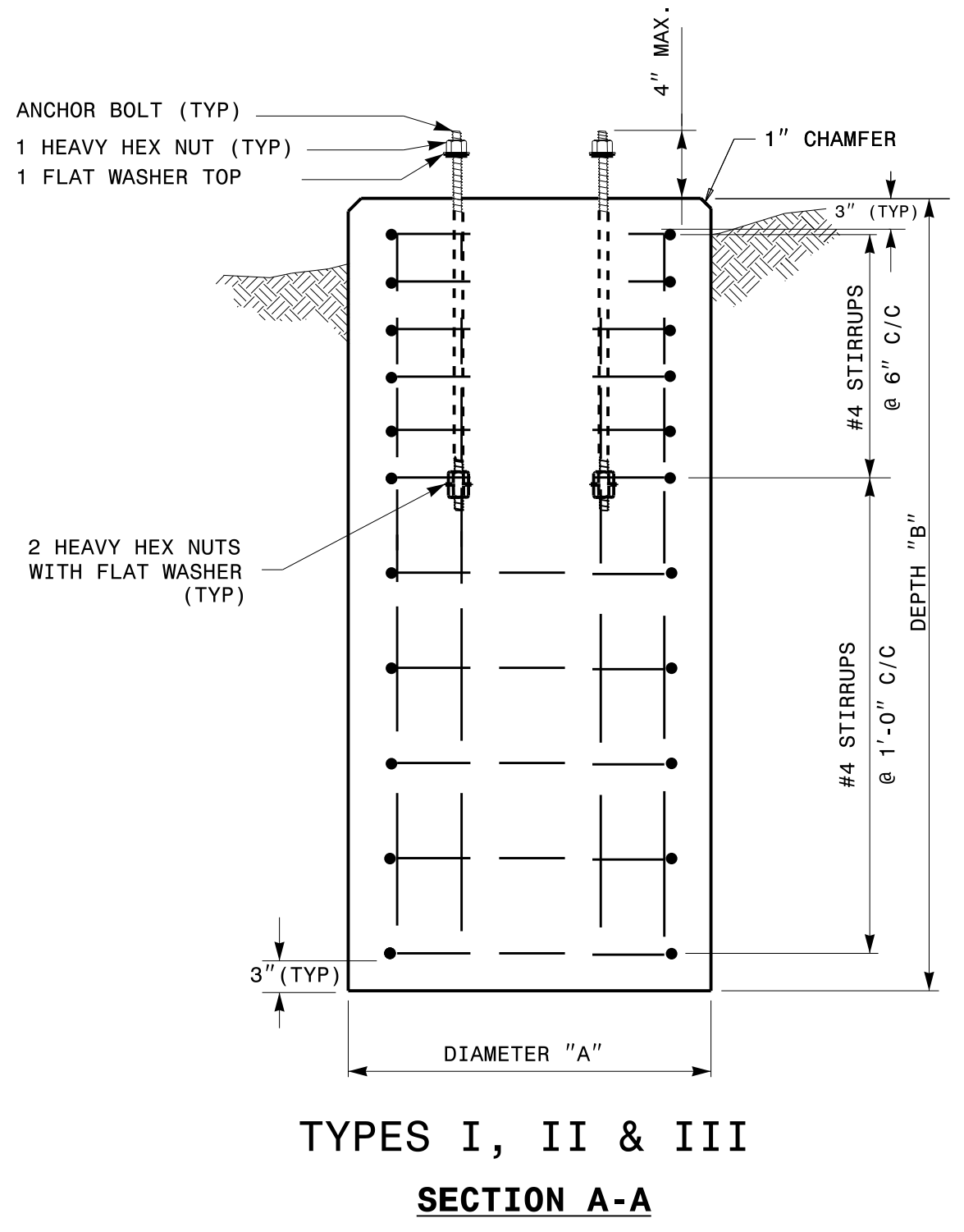
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10/11/2017  
DATE

750 N. Greenfield Parkway  
Garner, NC 27529

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- NOTES:**
- CAST FOUNDATION AGAINST UNDISTURBED SOIL WHEREVER CONDITIONS PERMIT. IN UNSTABLE SOIL, CAST-IN-PLACE TUBE FORMS ARE ALLOWED WITH APPROVAL.
  - COMPLY WITH APPLICABLE PROVISIONS OF SECTION 825 FOR CONCRETE CONSTRUCTION.
  - USE CLASS "A" CONCRETE THAT MEETS THE REQUIREMENTS OF SECTION 1000 WITH A COMPRESSION STRENGTH AT 28 DAYS OF  $F'c = 3000$  PSI (MIN.).
  - USE ASTM GRADE 60 DEFORMED BARS FOR ALL REINFORCING STEEL.
  - GRADE IS ASSUMED TO BE (8H:1V) OR FLATTER. FOUNDATION SIZE AND DEPTHS ARE BASED ON THE FOLLOWING SOIL DESIGN PARAMETERS:
    - A. SANDY TYPE SOIL
    - B. NO GROUND WATER WITHIN 5'-0" OF SURFACE ELEVATION
    - C. WIND SPEED NOT TO EXCEED 140 MPH
 IF ACTUAL CONDITIONS VARY SUBSTANTIALLY FROM THOSE ASSUMED, THE FOUNDATION DEPTH MAY BE ADJUSTED. IN THIS CASE, CONTACT THE ENGINEER.
  - MAINTAIN AT LEAST 3" COVER ON ALL REINFORCEMENT.
  - ORIENT CONDUIT AS REQUIRED BY THE DESIGN OR AS DICTATED BY FIELD CONDITIONS.
  - USE ADHESIVE ANCHOR FOR THREADED COUPLING INSERT. FOR TYPE I MINIMUM DEPTH NECESSARY IS 0'-4 1/2" AND FOR TYPE II MINIMUM DEPTH NECESSARY IS 0'-6 5/8". FOLLOW MANUFACTURER'S INSTALLATION INSTRUCTIONS.



PEDESTAL FOUNDATION TYPE AND SIZE							
TYPE	PEDESTAL DESCRIPTION	SIZE			ANCHOR BOLT		INSTALL GROUNDING SYSTEM (YES/NO)
		DIAMETER "A" FT	DEPTH "B" FT	CONCRETE VOLUME CY	DIAMETER (MIN.) IN	LENGTH FT-IN	
I	PEDESTRIAN PUSHBUTTON	2'-0"	3'-6"	.41	1/2	1'-6"	NO
II	NORMAL-DUTY	2'-0"	5'-0"	.58	3/4	2'-0"	YES
III	HEAVY-DUTY	2'-6"	7'-0"	1.27	1	4'-0"	YES

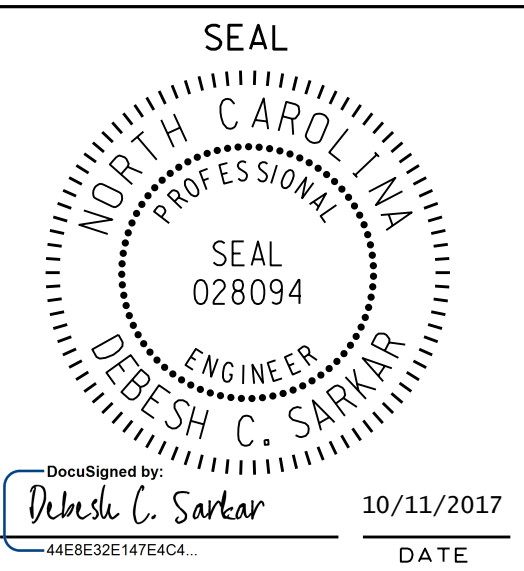
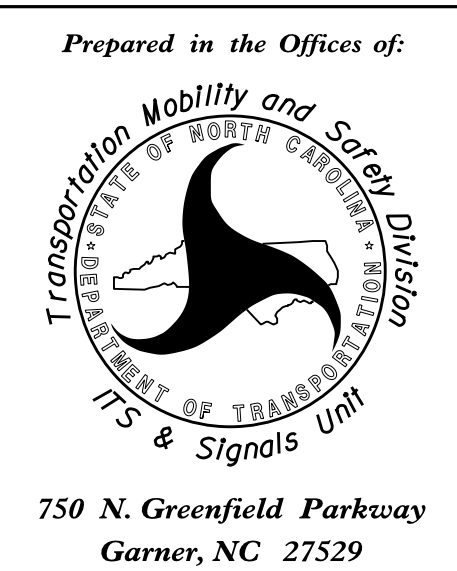
REINFORCING STEEL SCHEDULE												
TYPE	V-BAR				STIRRUP							
	SIZE #	QTY	LENGTH	WEIGHT LBS	QUANTITY			LENGTH	DIAMETER "C" FT	OVERLAP MIN.	WEIGHT LBS	TOTAL STEEL WEIGHT LBS
					ON 6" CENTERS	ON 12" CENTERS	TOTAL					
I	8	6	3'-0"	56	4	0	4	5'-7"	1'-6"	0'-10"	15	71
II	8	6	4'-6"	86	4	5	3	5'-7"	1'-6"	0'-10"	30	116
III	8	6	6'-6"	122	4	7	4	7'-2"	2'-0"	0'-10"	53	175

STATE OF NORTH CAROLINA  
DEPT. OF TRANSPORTATION  
DIVISION OF HIGHWAYS  
RALEIGH, N.C.

ENGLISH STANDARD DRAWING FOR  
**PEDESTALS**  
FOUNDATIONS

SHEET 1 OF 1  
**1743D01**

See Plate for Title



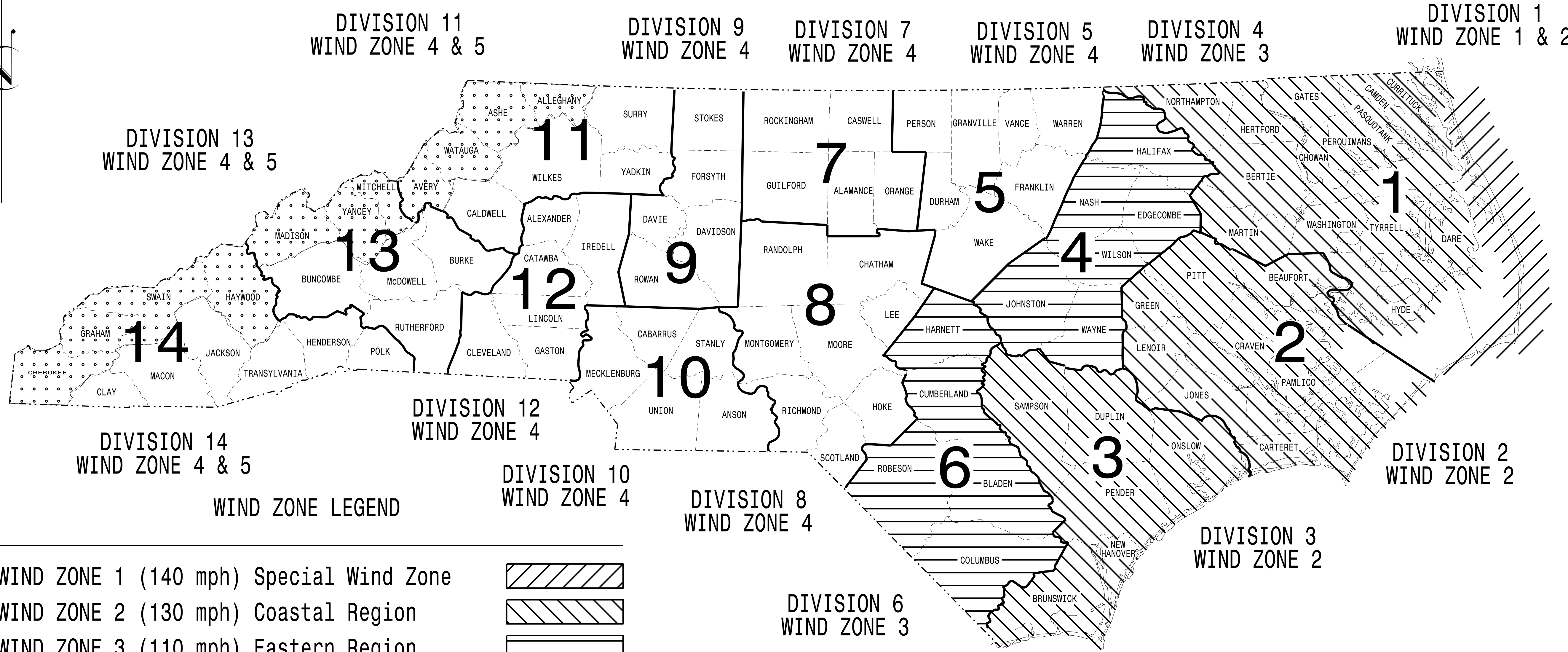
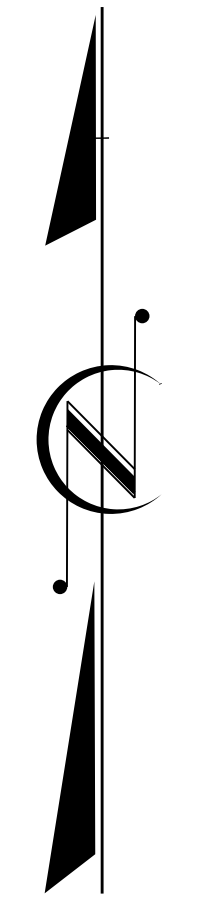
DOCUMENT NOT CONSIDERED  
FINAL UNLESS ALL  
SIGNATURES COMPLETED

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# STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

PROJECT I.D. NO. B-5351	SHEET NO. Sig.M1
----------------------------	---------------------

## STANDARD DRAWINGS FOR ALL METAL POLES



**WIND ZONE LEGEND**

WIND ZONE 1 (140 mph) Special Wind Zone	
WIND ZONE 2 (130 mph) Coastal Region	
WIND ZONE 3 (110 mph) Eastern Region	
WIND ZONE 4 (90 mph) Central & Mtn. Region	
WIND ZONE 5 (120 mph) Special Wind Zone	

<https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx>

Prepared In the Offices of:

750 N. Greenfield Pkwy.  
Garner, NC 27529

Designed in conformance  
with the latest  
2015 Interim to the  
6th Edition 2013  
**AASHTO**  
Standard Specifications for  
Structural Supports for  
Highway Signs, Luminaires,  
and Traffic Signals

DRAWING NUMBER	DESCRIPTION
Sig. M 1	Statewide Wind Zone Map
Sig. M 2	Typical Fabrication Details-All Metal Poles
Sig. M 3	Typical Fabrication Details-Strain Poles
Sig. M 4	Typical Fabrication Details-Mast Arm Poles
Sig. M 5	Typical Fabrication Details-Mast Arm Connection
Sig. M 6	Typical Fabrication Details-Strain Pole Attachments
Sig. M 7	Construction Details-Foundations
Sig. M 8	Standard Strain Pole Foundation-All Soil Conditions

**NC DOT CONTACTS:**

**MOBILITY AND SAFETY DIVISION - ITS AND SIGNALS UNIT**

---

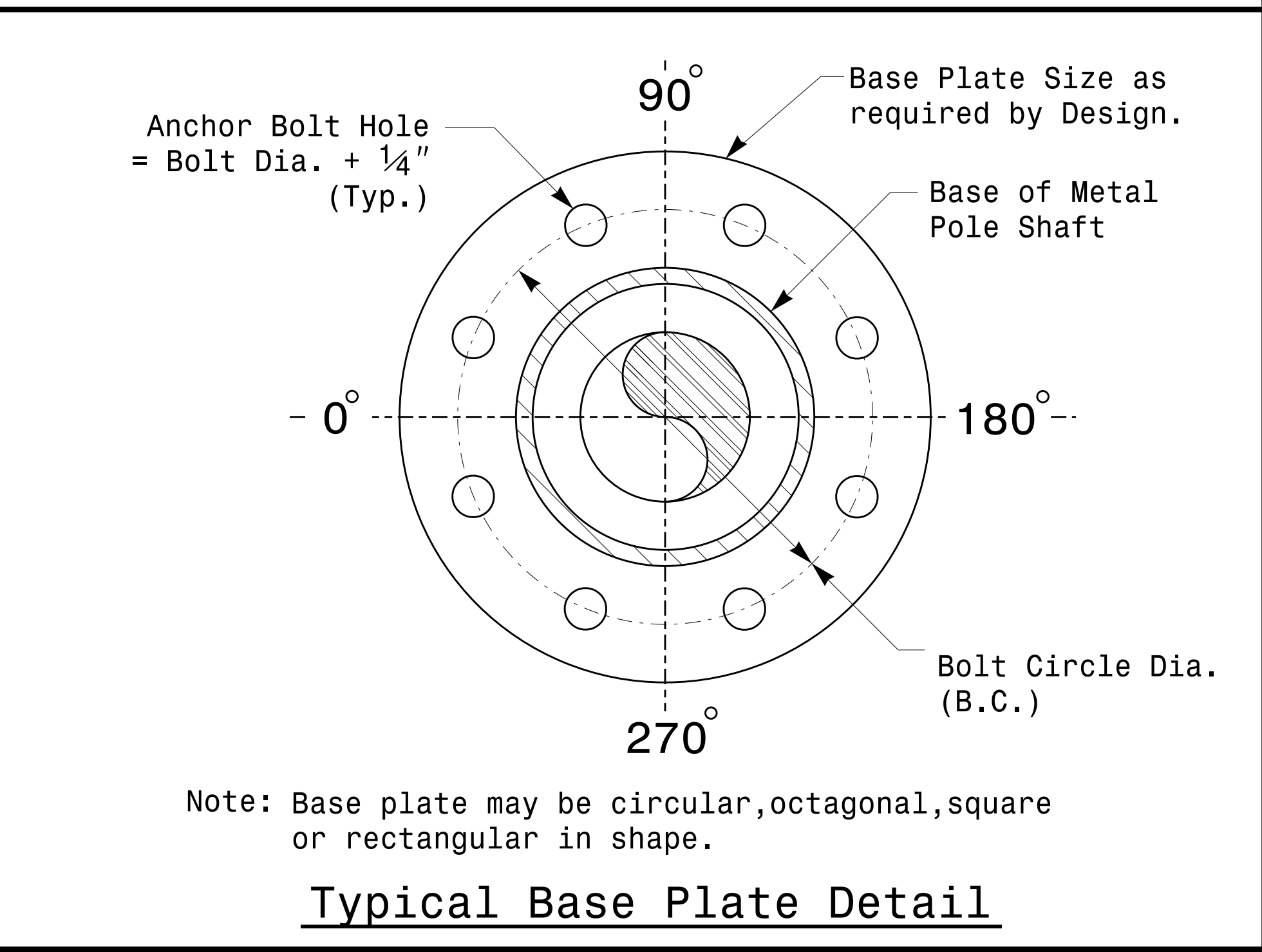
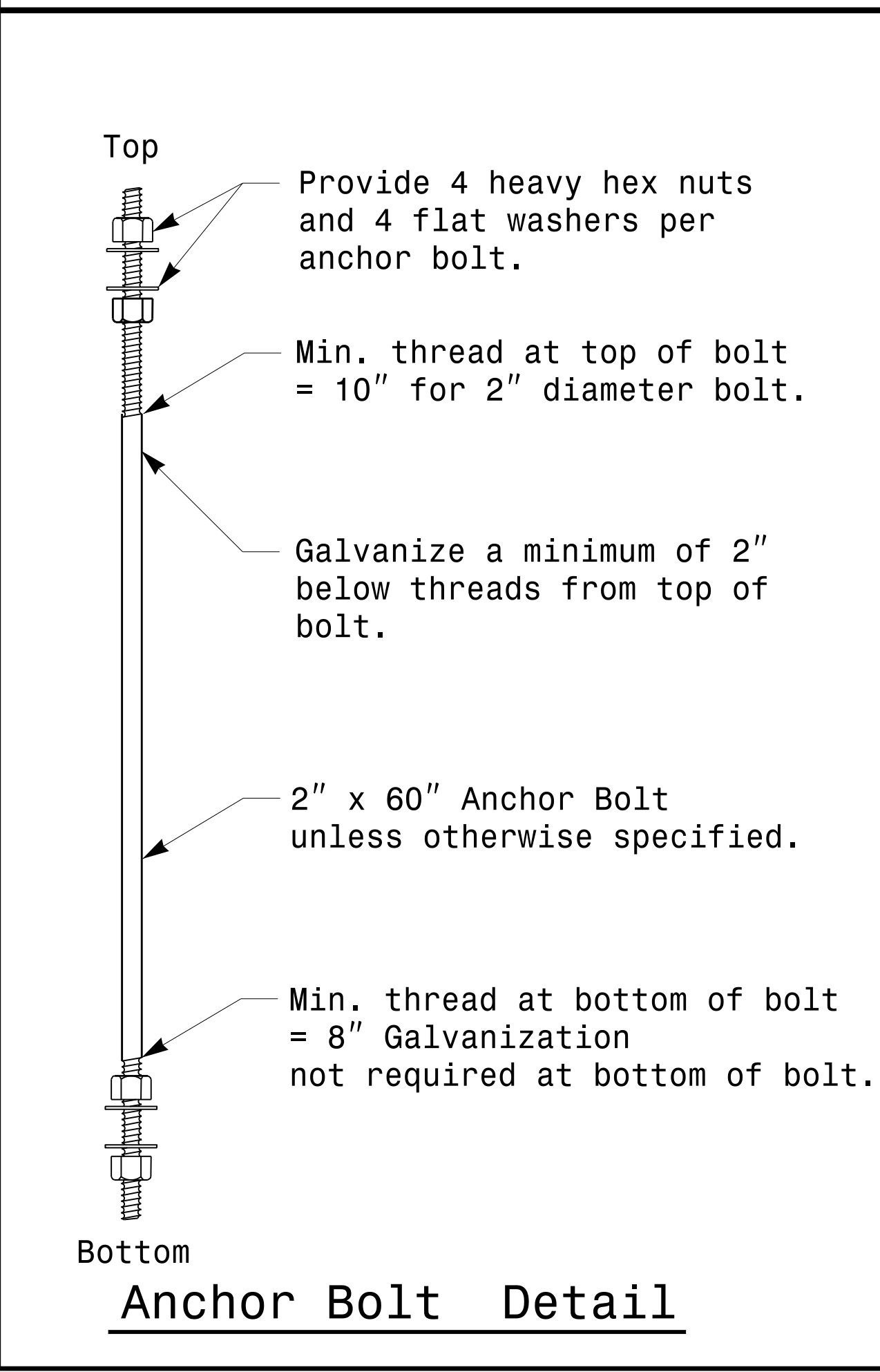
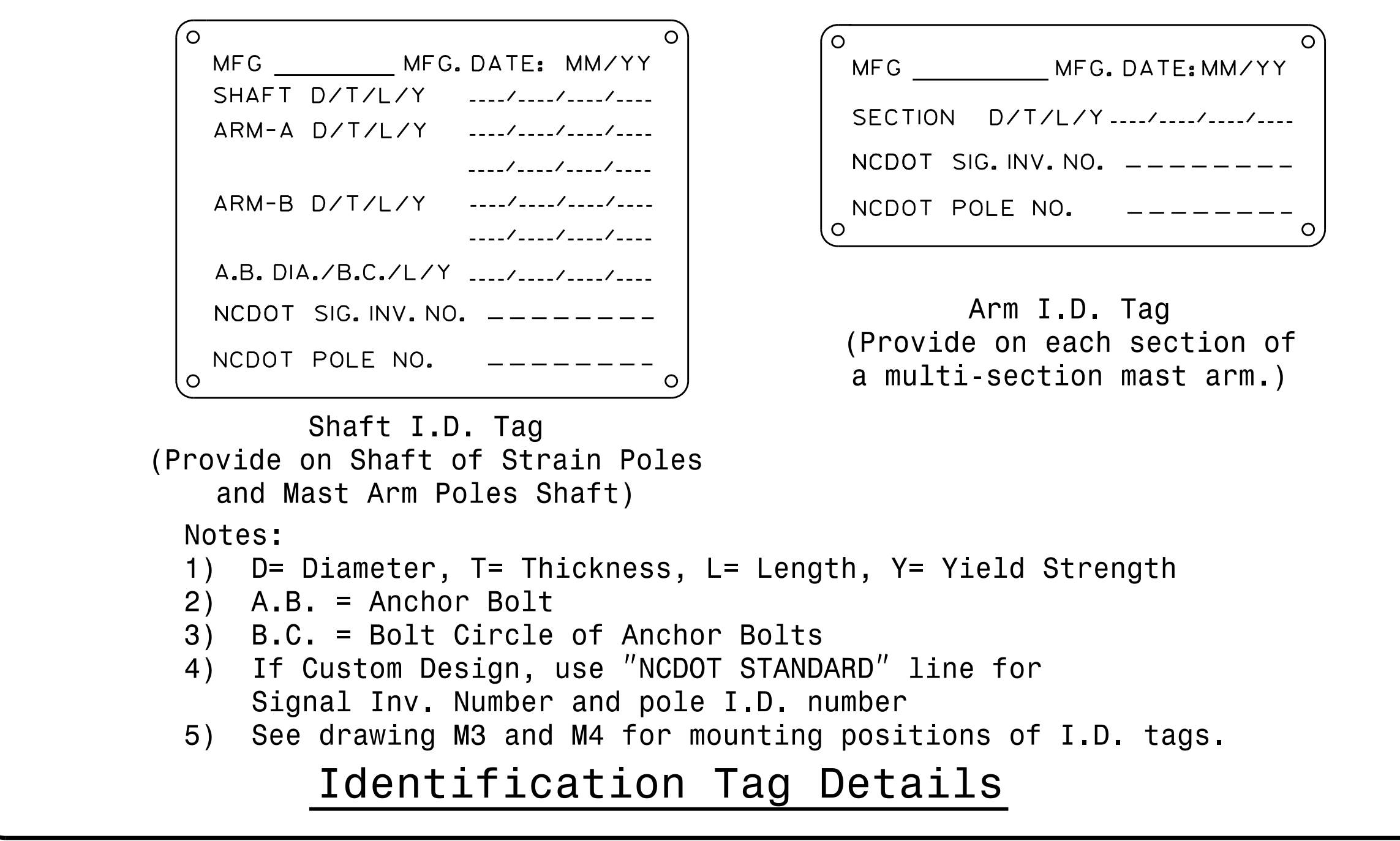
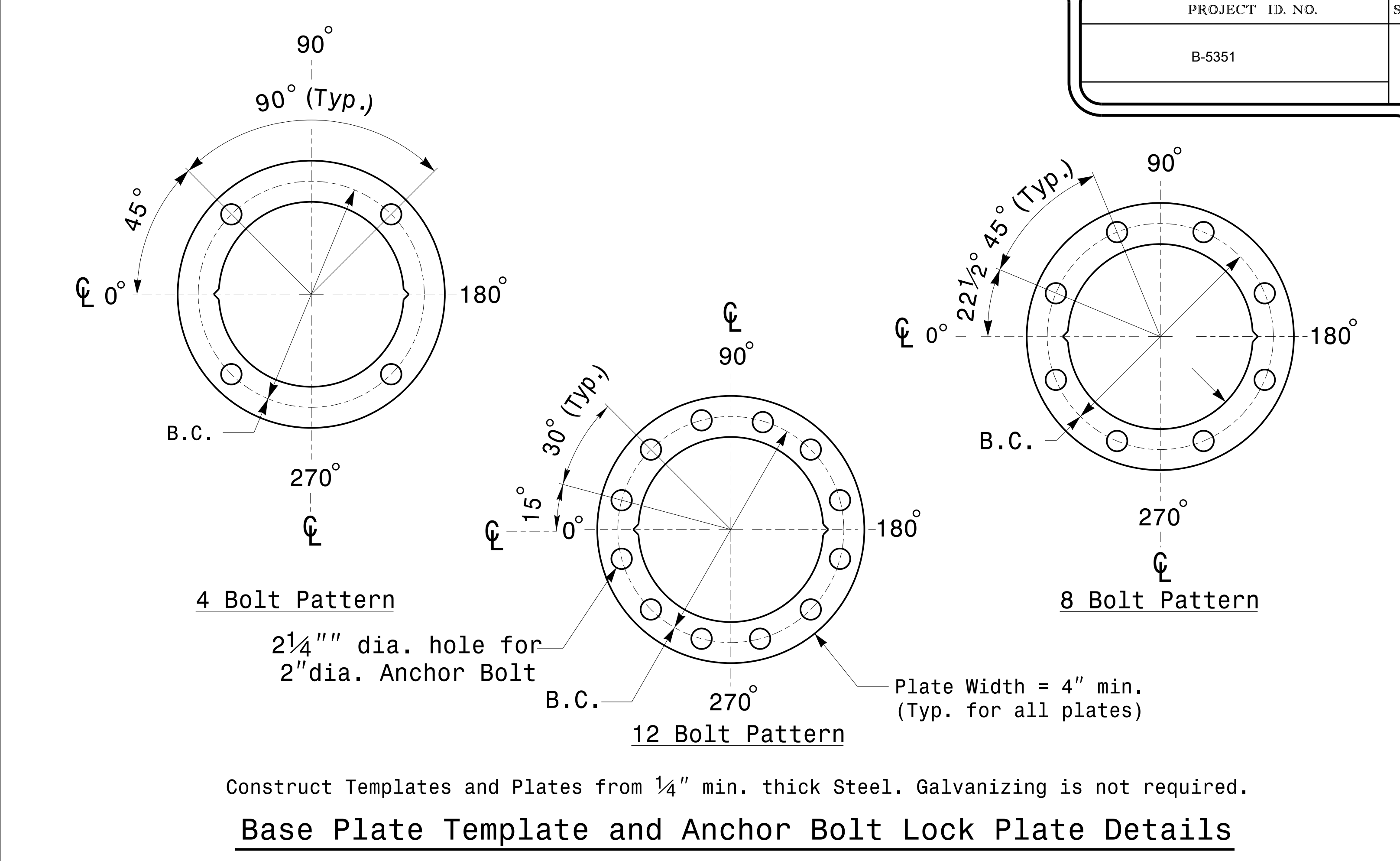
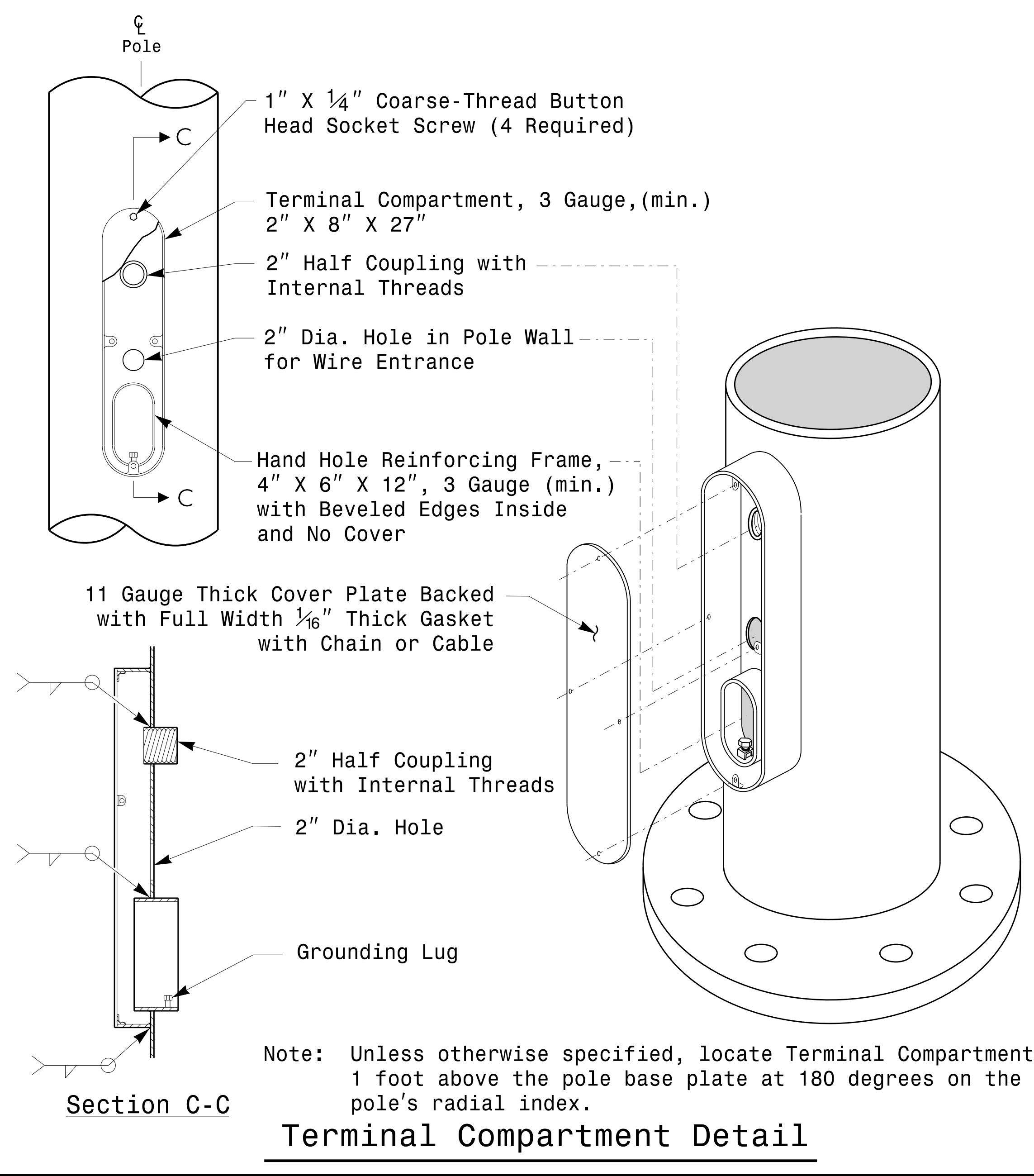
M.M. MCDIARMID, P.E. - STATE ITS AND SIGNALS ENGINEER

J.P. GALLOWAY, P.E. - STATE SIGNALS ENGINEER

D.C. SARKAR, P.E. - ITS AND SIGNALS SENIOR STRUCTURAL ENGINEER

SEAL

DocuSigned by:  
Debesh C. Sarkar  
DATE 10/11/2017

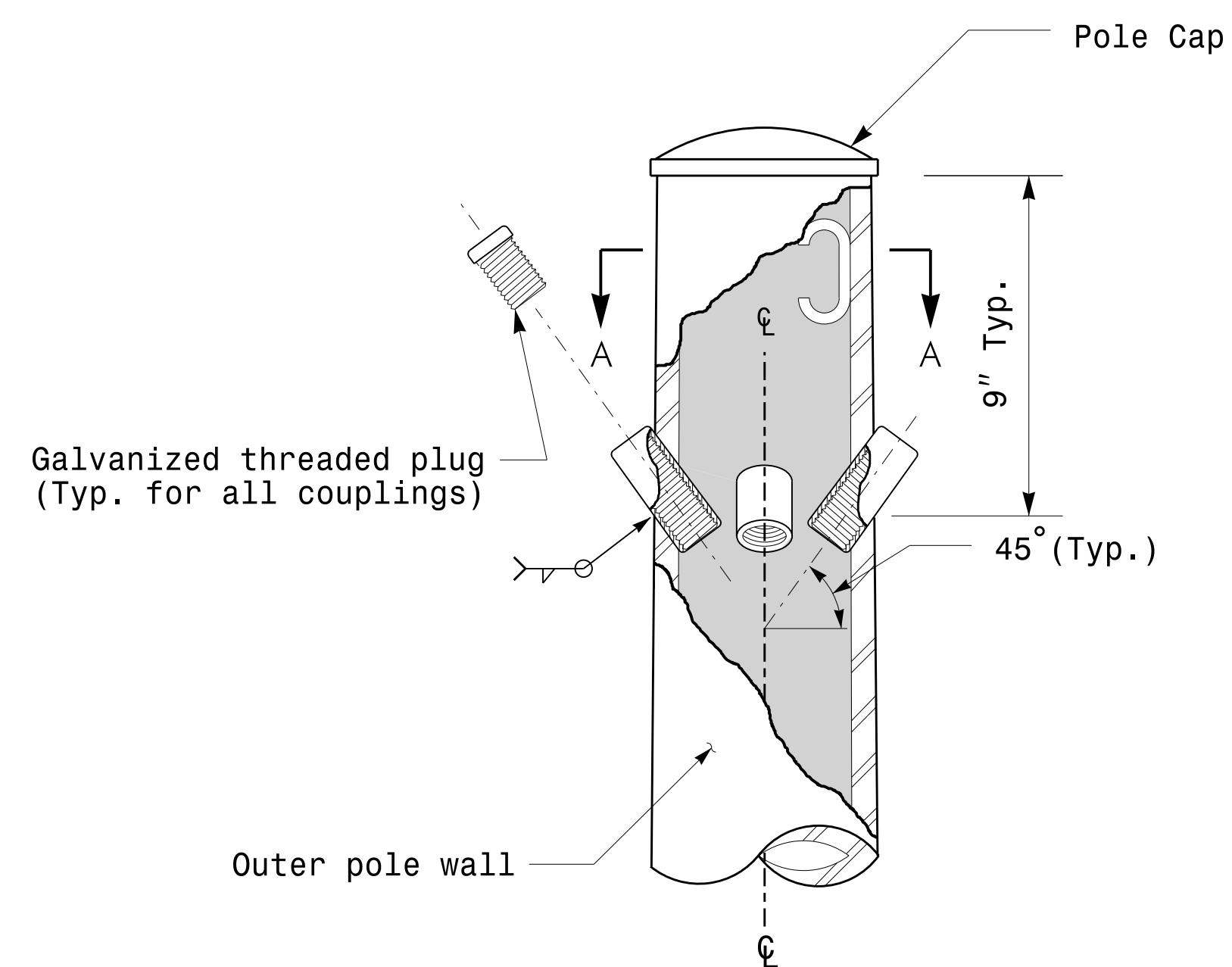


	<b>Typical Fabrication Details For All Metal Poles</b>	
	PLAN DATE: OCTOBER 2017 DESIGNED BY: C.F. ANDREWS	PREPARED BY: N. BITTING REVIEWED BY: D.C. SARKAR
SCALE: NONE	REVISIONS:	INITI: DATE:
DocuSign by: <i>Dibesh C. Sarkar</i>		10/11/2017 DATE

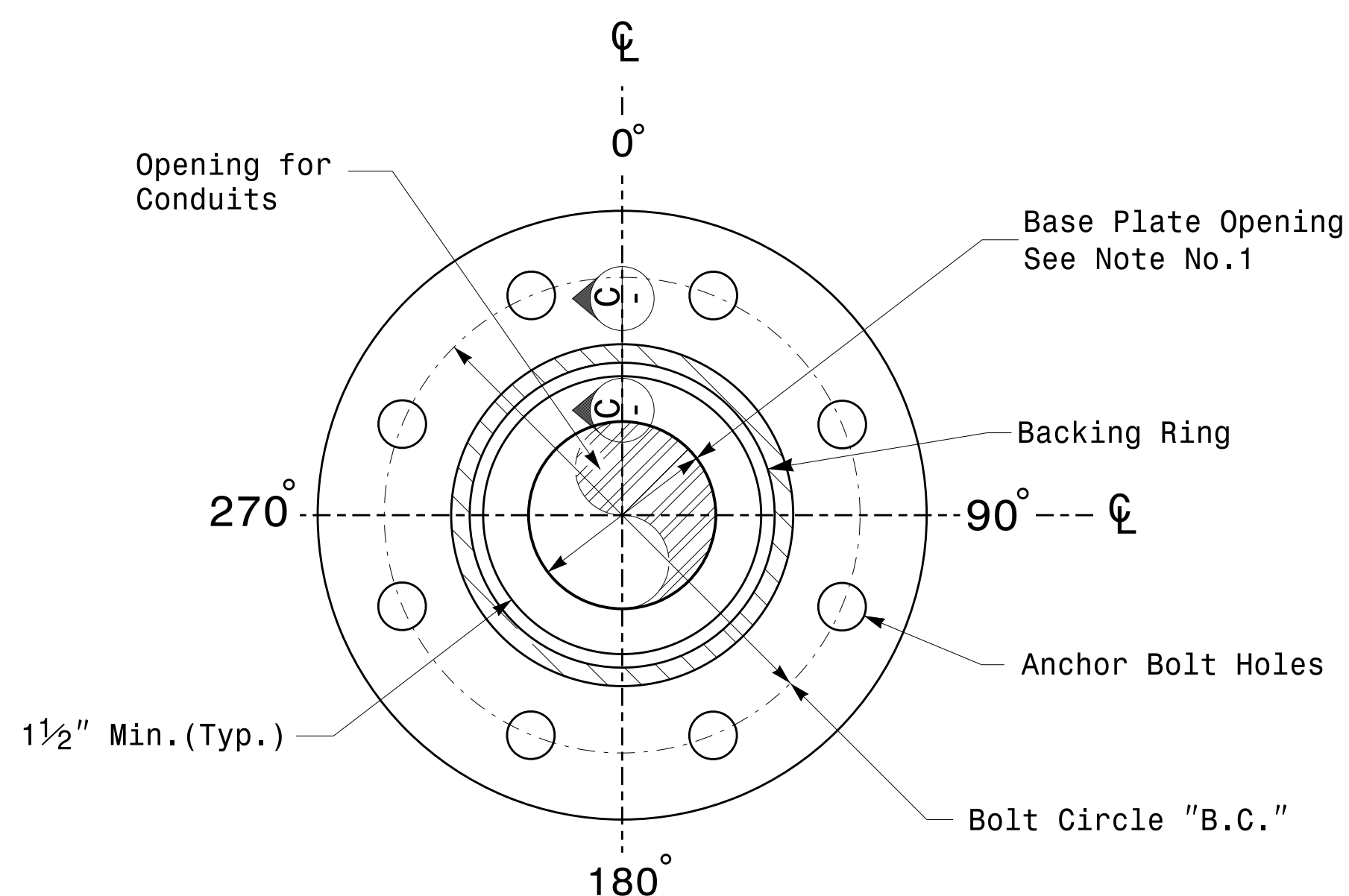
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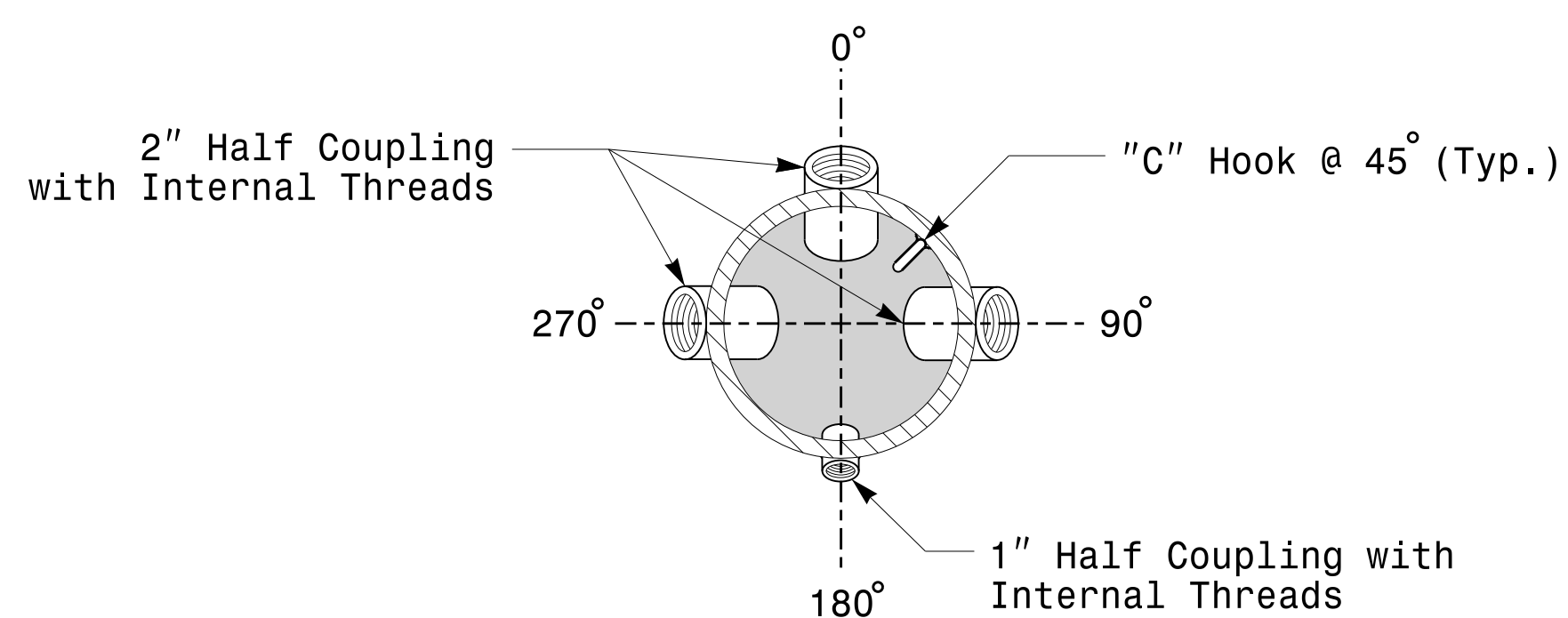
Note:  
 1. Opening in pole base plate shall be equal to pole base inside diameter minus 3 1/2" but shall not be less than 8 1/2".



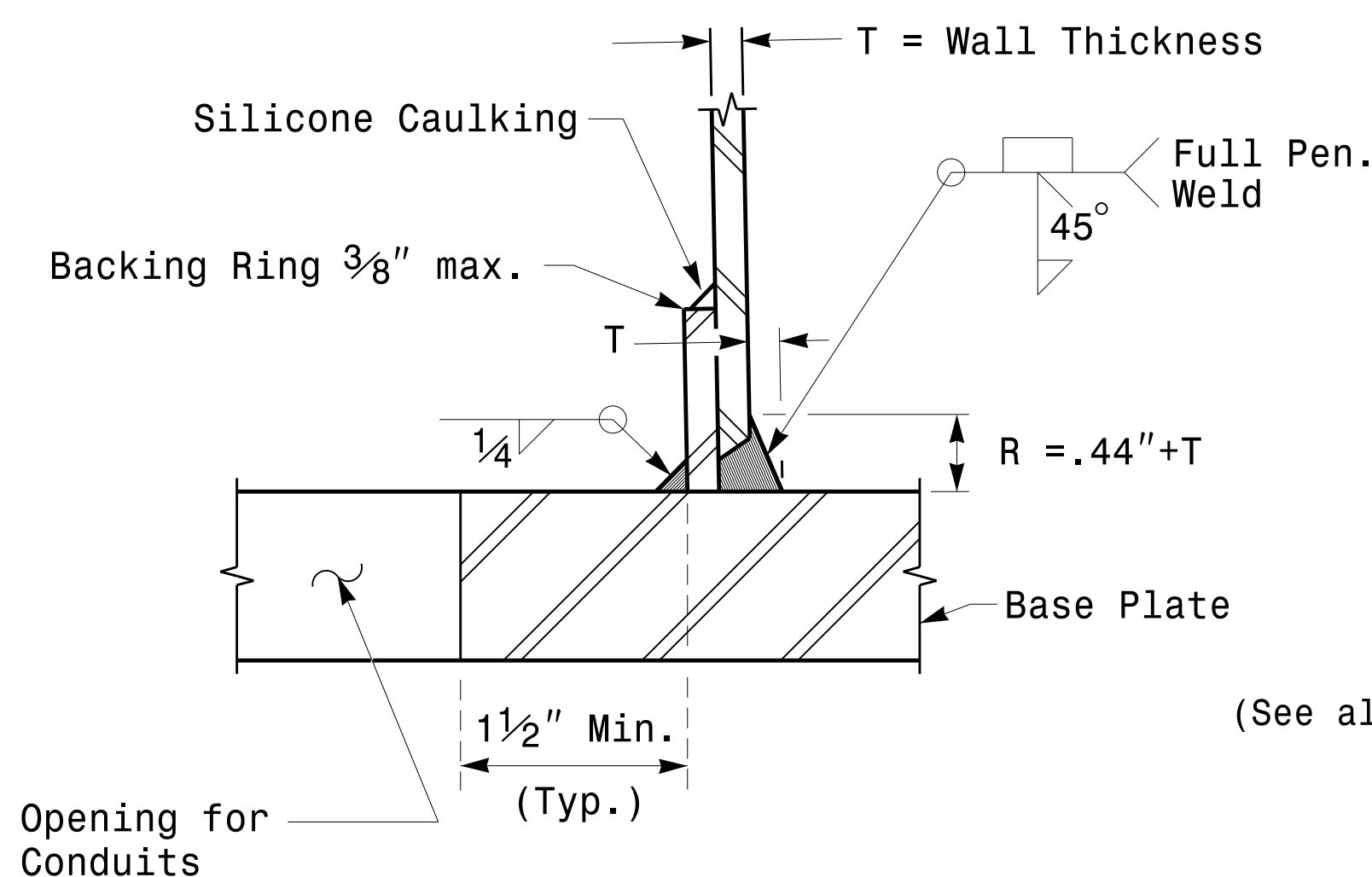
Cable Entrances at Top of Pole



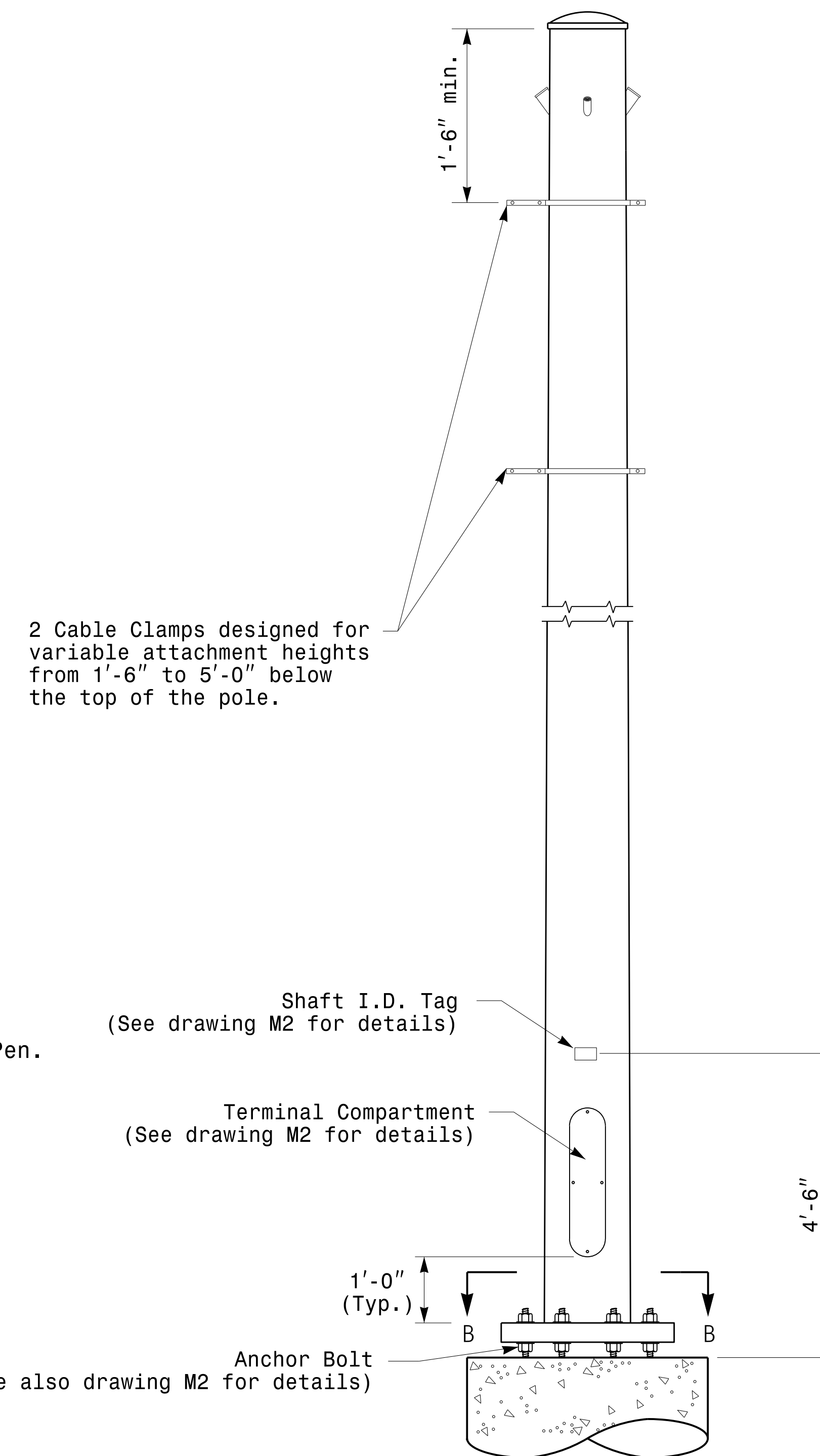
Section B-B  
Pole Base Plate Details  
(8 and 12 Bolt Pattern)



Section A-A  
Radial Orientation for Factory Installed  
Accessories at Top of Pole



Section C-C  
(Pole Attachment to Base Plate)  
Full-Penetration  
Groove Weld Detail



Monotube Strain Pole

Prepared in the Offices of:  
  
 750 N. Greenfield Pkwy, Garner, NC 27529

SCALE: 0 NONE

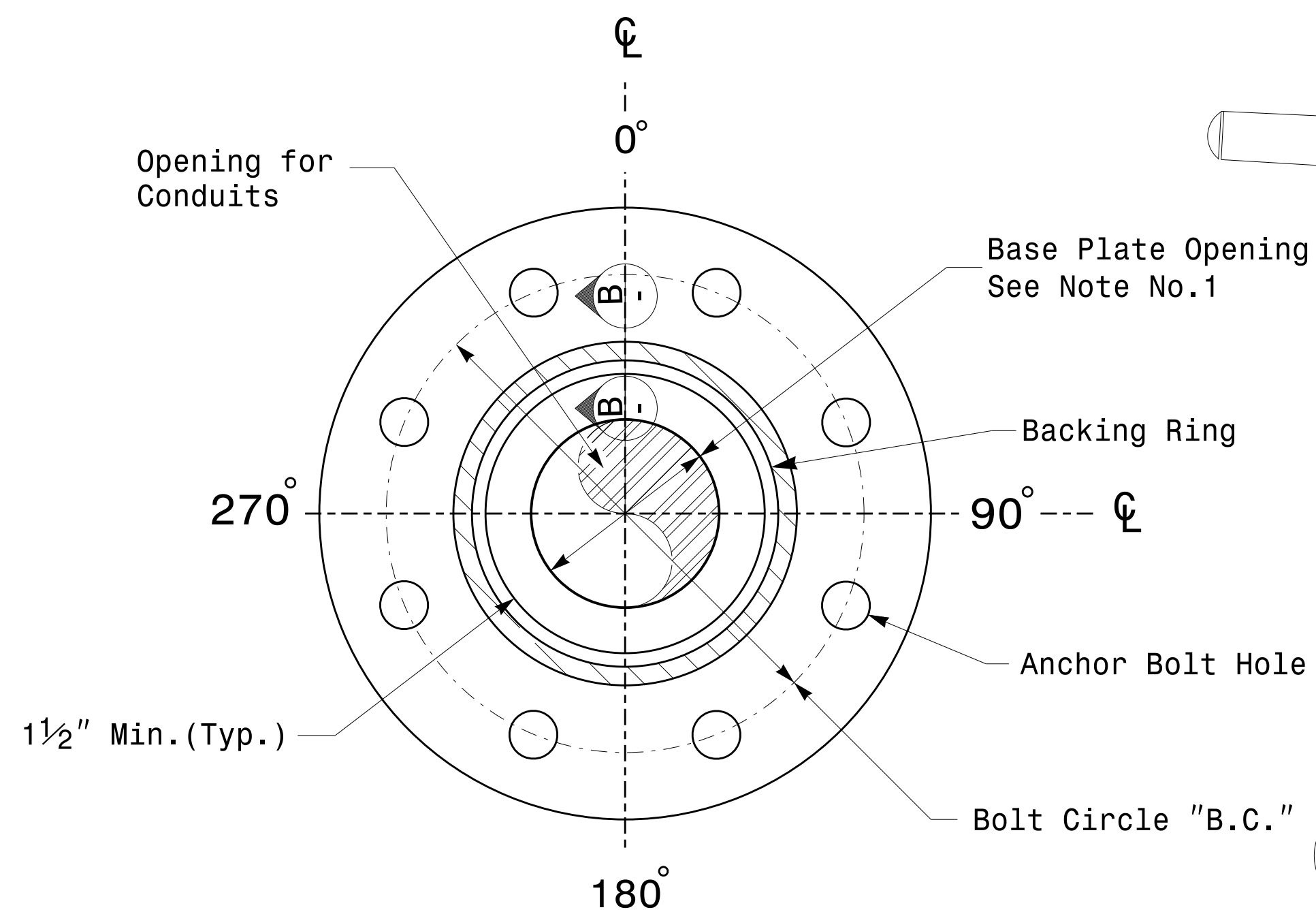
Typical Fabrication Details For Strain Poles

PLAN DATE: OCTOBER 2017	DESIGNED BY: K.C. DURIGON
PREPARED BY: N. BITTING	REVIEWED BY: D.C. SARKAR
REVISIONS	INIT. DATE

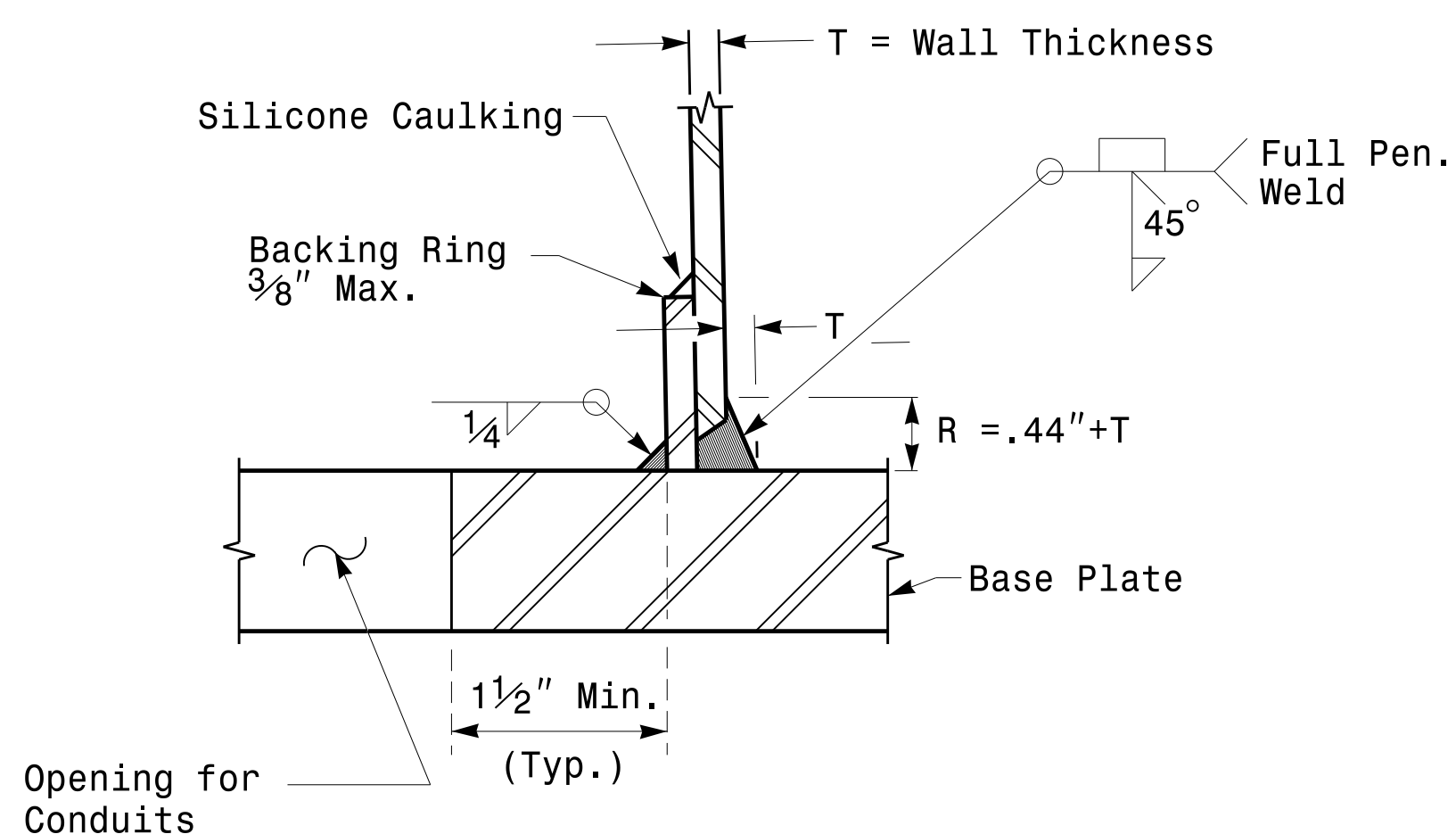
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 10/11/2017  
 DATE

Fabrication Details – Strain Poles

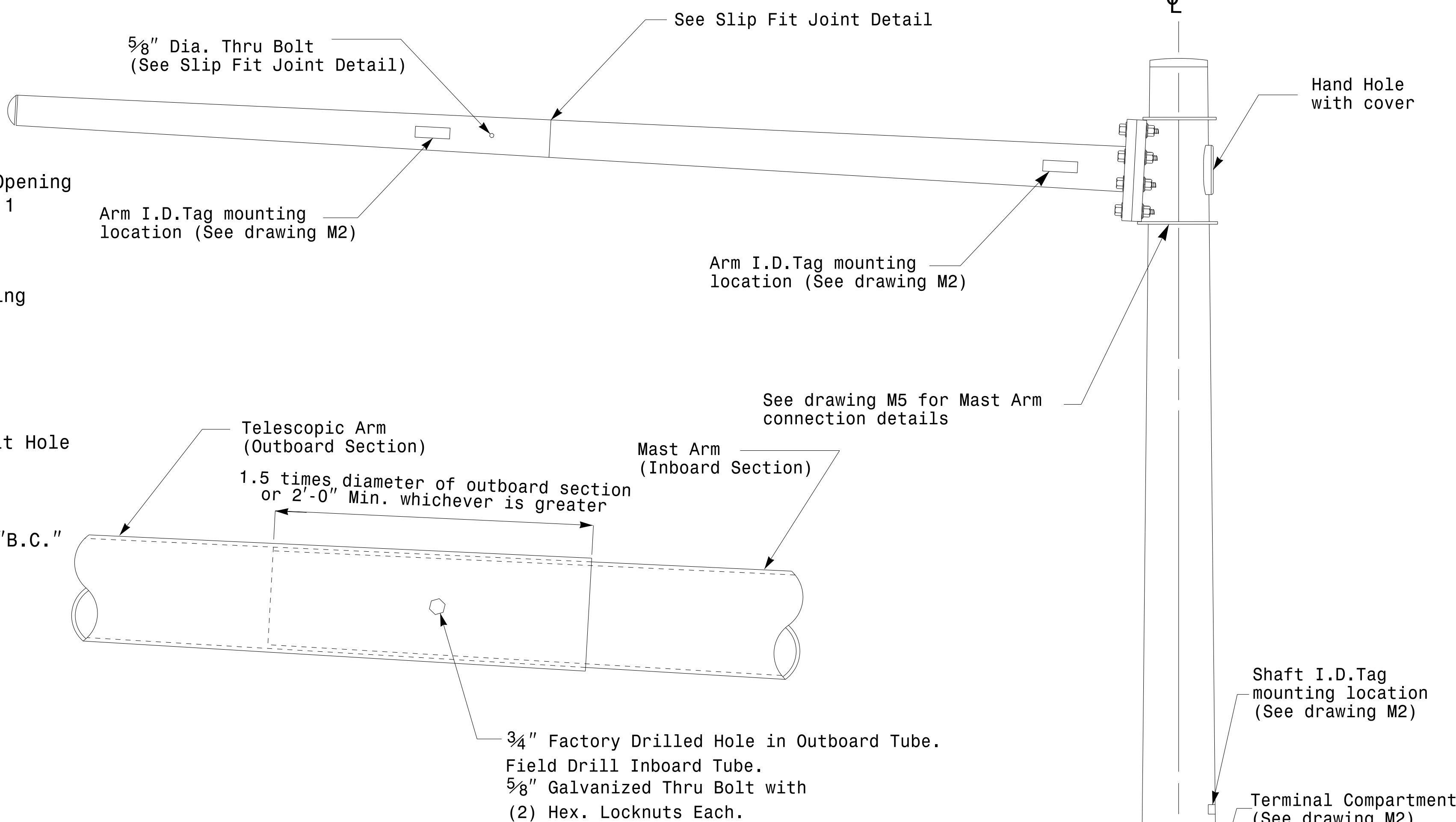
Note:  
 1. Opening in pole base plate shall be equal to pole base inside diameter minus 3 1/2" but shall not be less than 8 1/2".



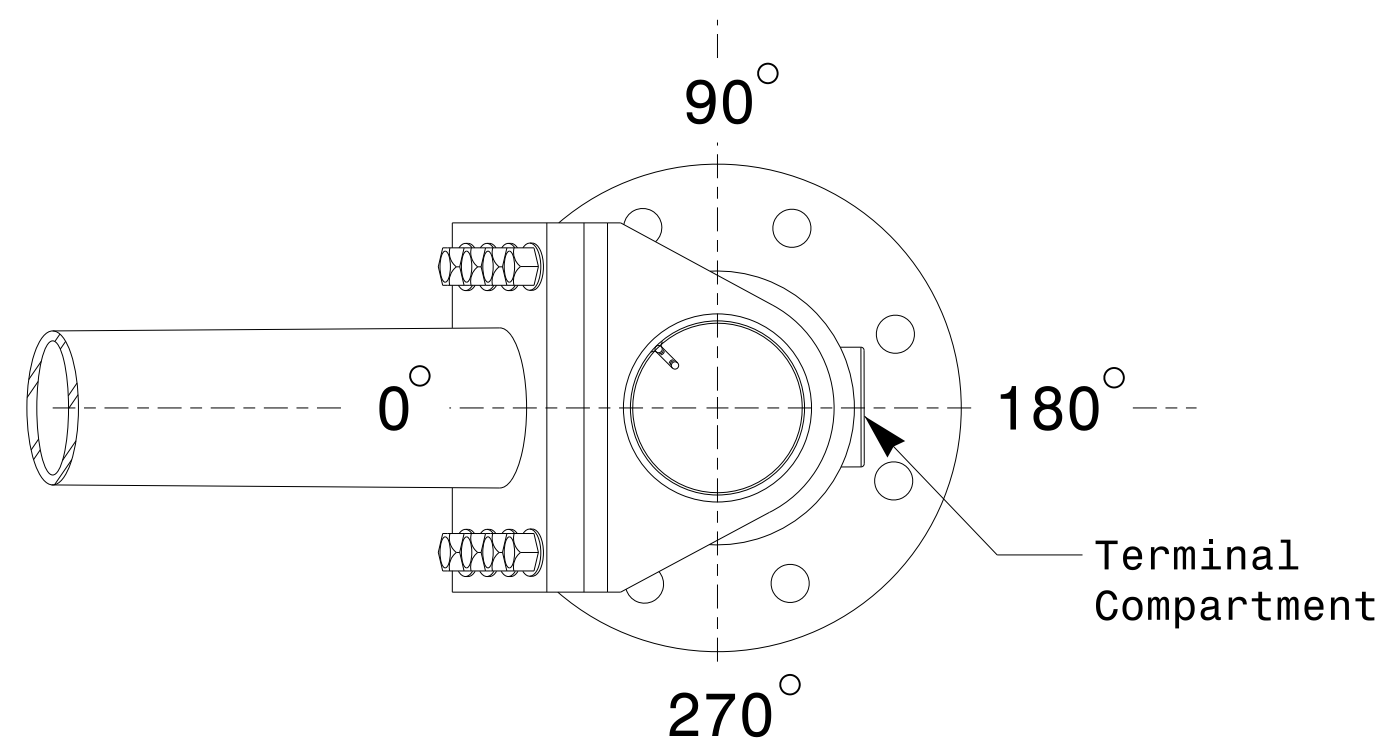
**Section A-A**  
**Pole Base Plate Details**



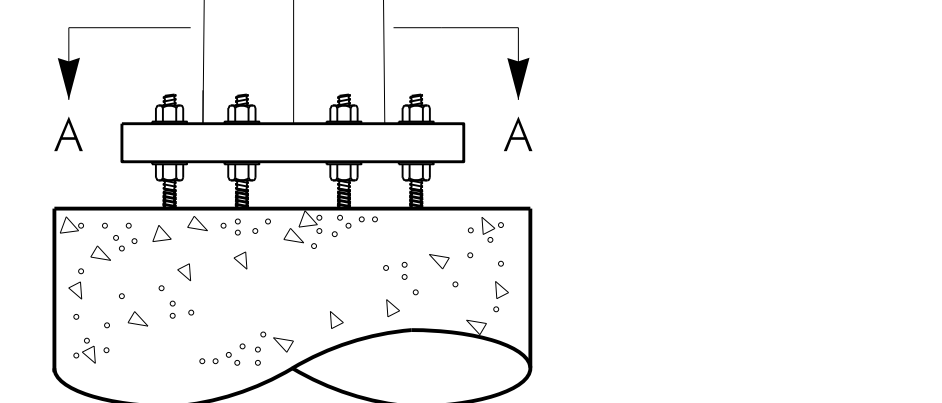
**Section B-B**  
 (Pole Attachment to Base Plate)  
**Full-Penetration Groove Weld Detail**



**Slip Fit Joint Detail for Mast Arm**



**Mast Arm Radial Orientation**



**Mast Arm Pole**

**Fabrication Details – Mast Arm Poles**

<p>Prepared in the Offices of:</p> <p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>Typical Fabrication Details                  For                  Mast Arm Poles</p>		<p>SEAL</p>
	<p>PLAN DATE: OCTOBER 2017</p>	<p>DESIGNED BY: K.C. DURIGON</p>	
<p>SCALE: 0 NA NONE</p>	<p>PREPARED BY: N. BITTING</p>	<p>REVIEWED BY: D.C. SARKAR</p>	<p>10/11/2017</p>
<p>REVISIONS</p>	<p>INIT.</p>	<p>DATE</p>	<p>DATE</p>

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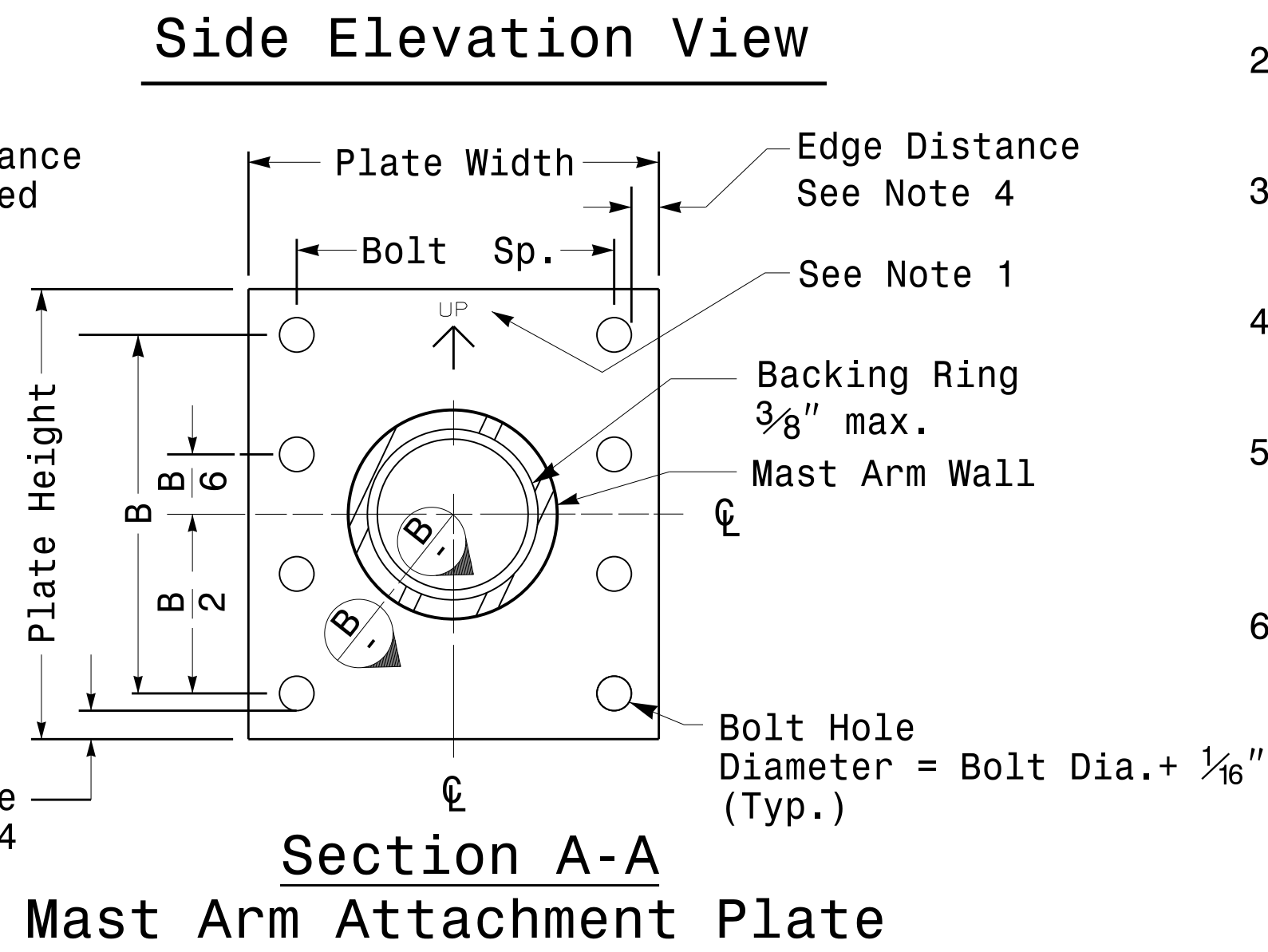
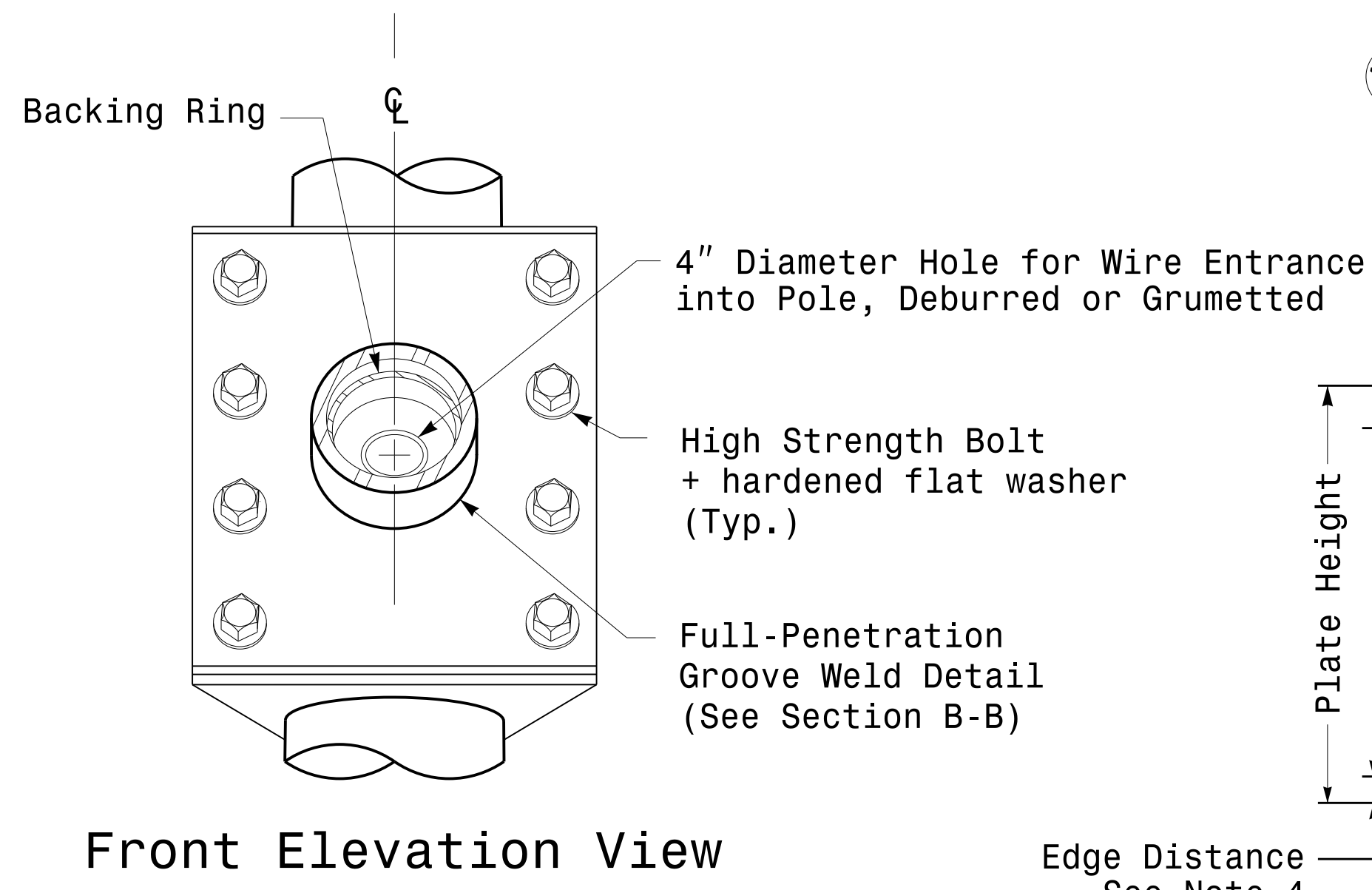
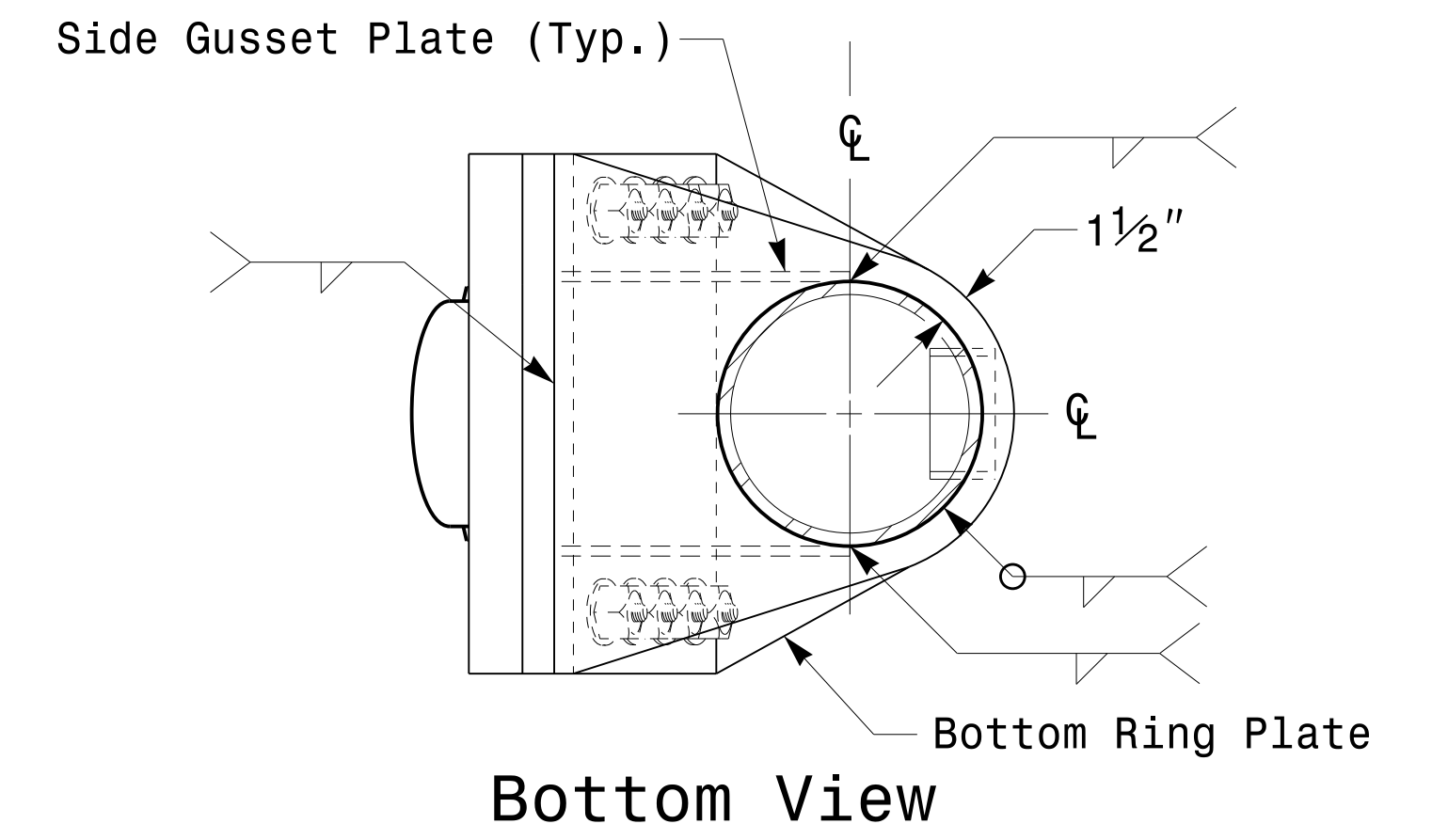
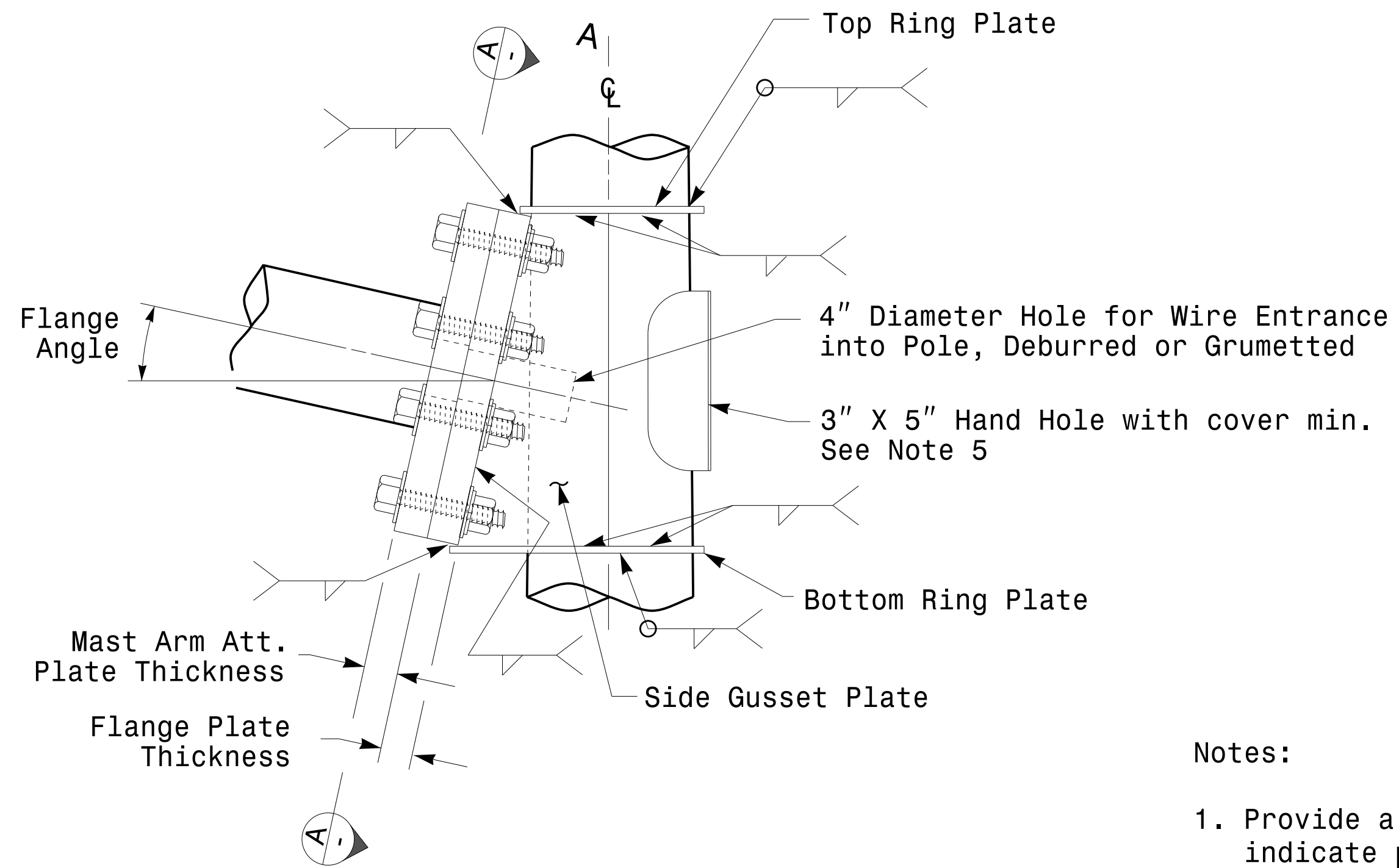
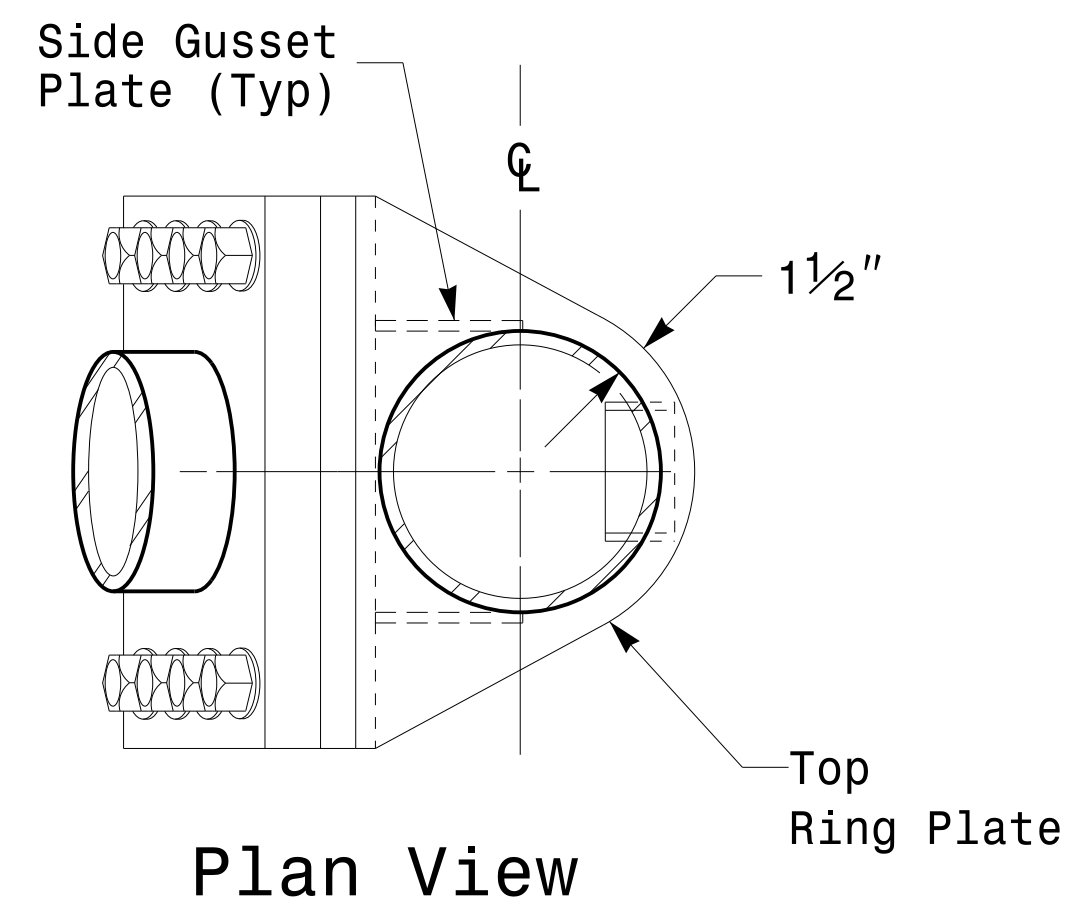
# Welded Ring Stiffened Mast Arm Connection

PROJECT ID. NO.

SHEET NO.

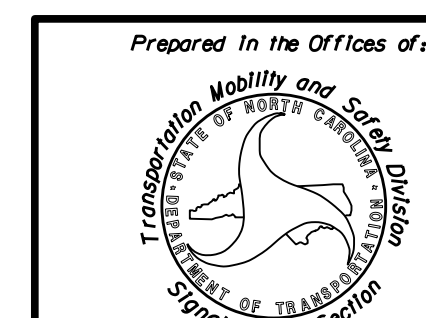
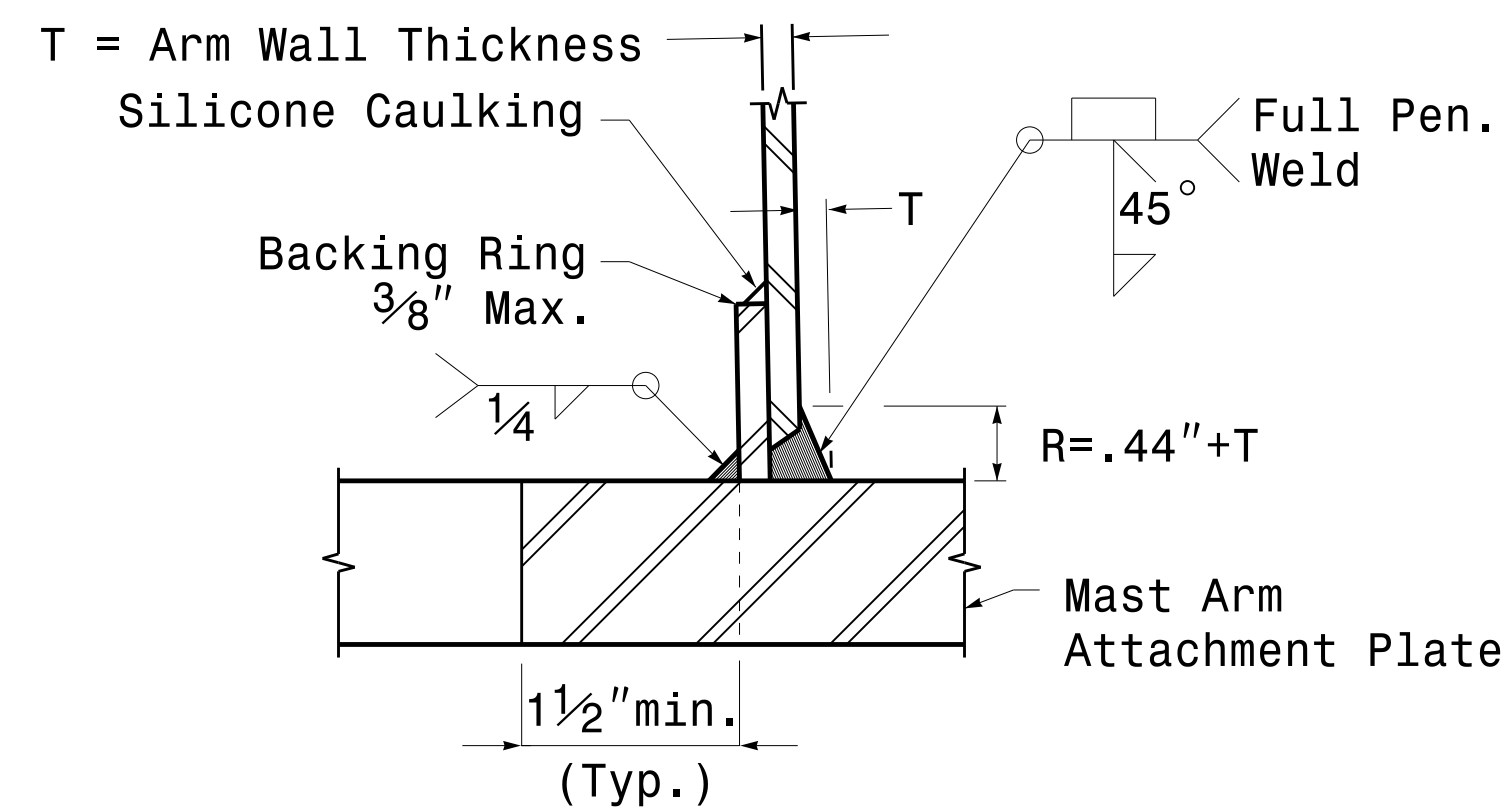
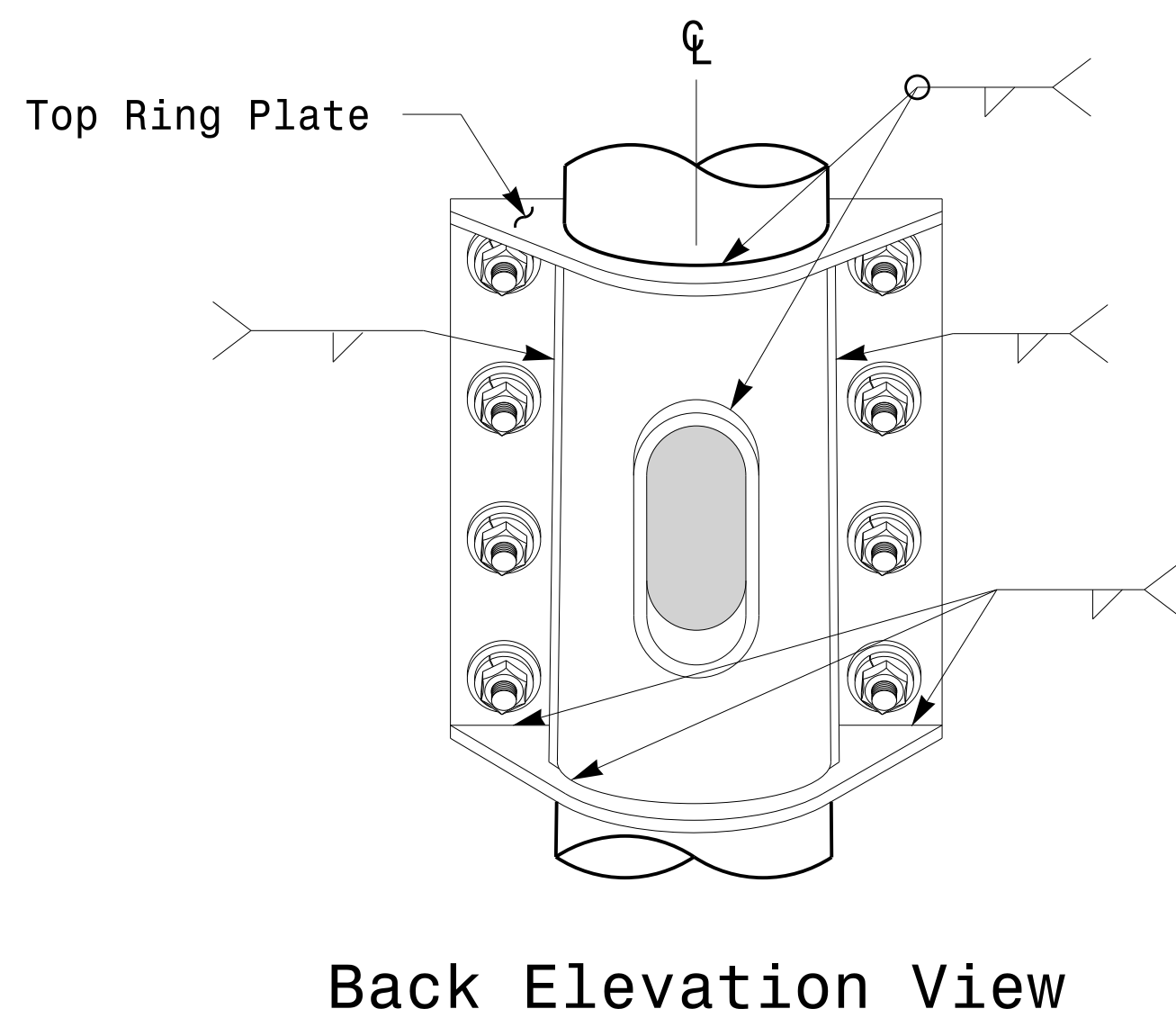
B-5351

Sig.M5



**Notes:**

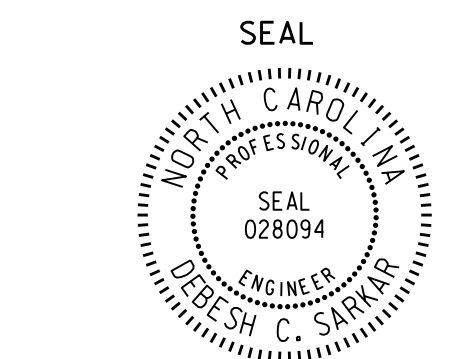
1. Provide a permanent means of identification above the mast arm to indicate proper attachment orientation of the mast arm.
2. Designer will determine the size of all structural components, plates, fasteners, and welds shown unless they are already specified.
3. Fabricator is responsible for providing appropriate holes at drainage points to drain galvanizing materials.
4. For minimum edge distance follow AISC Table J3.4 and J3.5. For nominal bolt hole size use Table J3.3.
5. Provide upper handhole as necessary when shaft extensions are required for luminaire arms or camera. For poles without luminaires/camera, wiring can be done through the top of pole.
6. Allowable range of flange tilt angle will vary from 0° to as required.



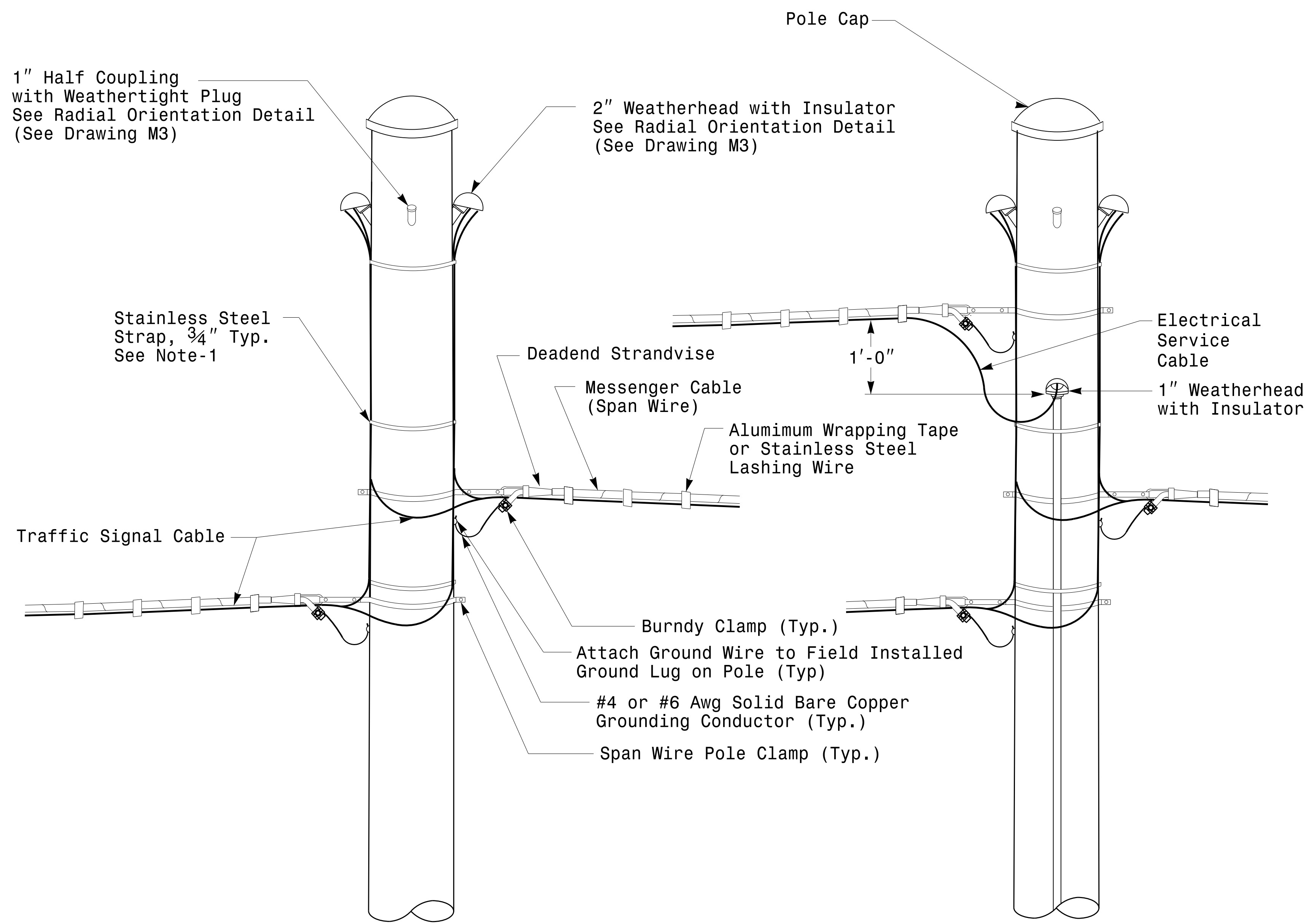
Typical Fabrication Details For Mast Arm Connection To Pole

PLAN DATE: OCTOBER 2017 DESIGNED BY: C.F. ANDREWS  
 PREPARED BY: N. BITTING REVIEWED BY: D.C. SARKAR

REVISIONS	INIT.	DATE



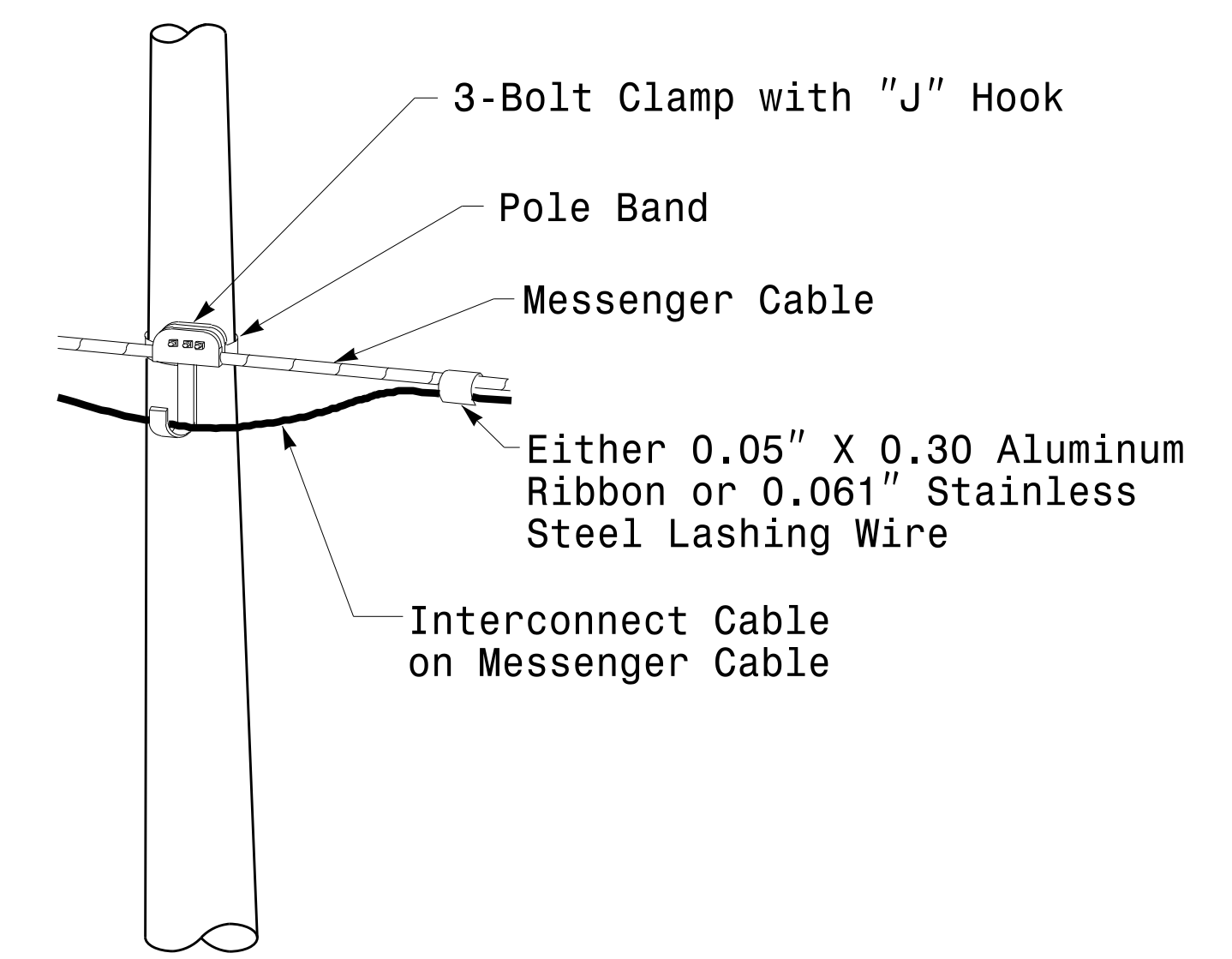
Designed by: Debesh C. Sarkar  
 DATE: 10/11/2017



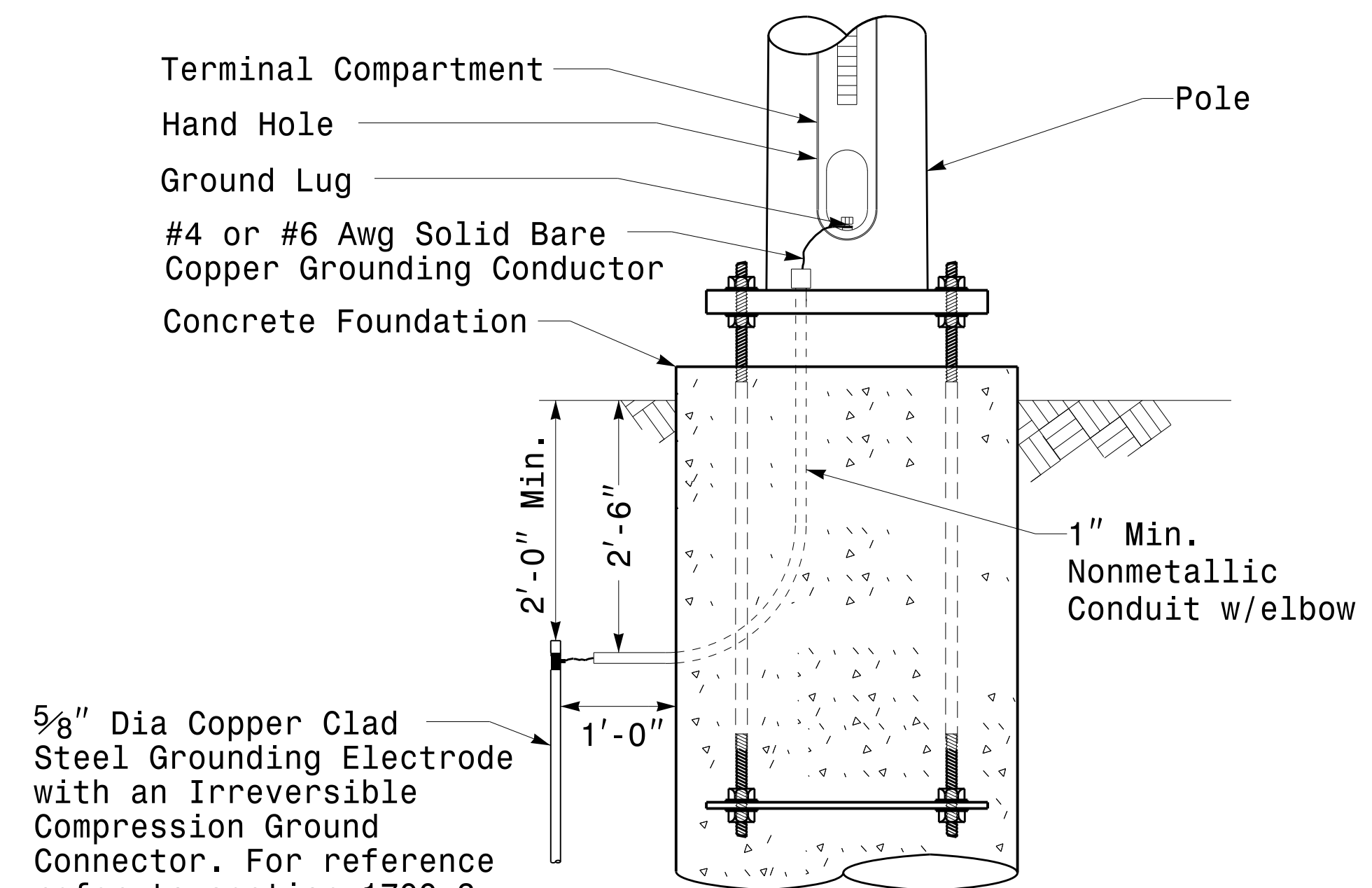
**Strain Pole Attachments**

**NOTE:**

1. Strap all signal cables to the side of the pole with 3/4" stainless steel straps when the distance between the spanwire attachment clamp and the weatherheads exceeds 3'-0".
2. Provide minimum two spanwire pole clamps per pole.
3. It is prohibited to attach two span wires at one pole clamp.
4. For general requirements refer to NCDOT Standard Specifications for Roadway and Structures, January 2018.



**Attachment of Cable to Intermediate Metal Pole**

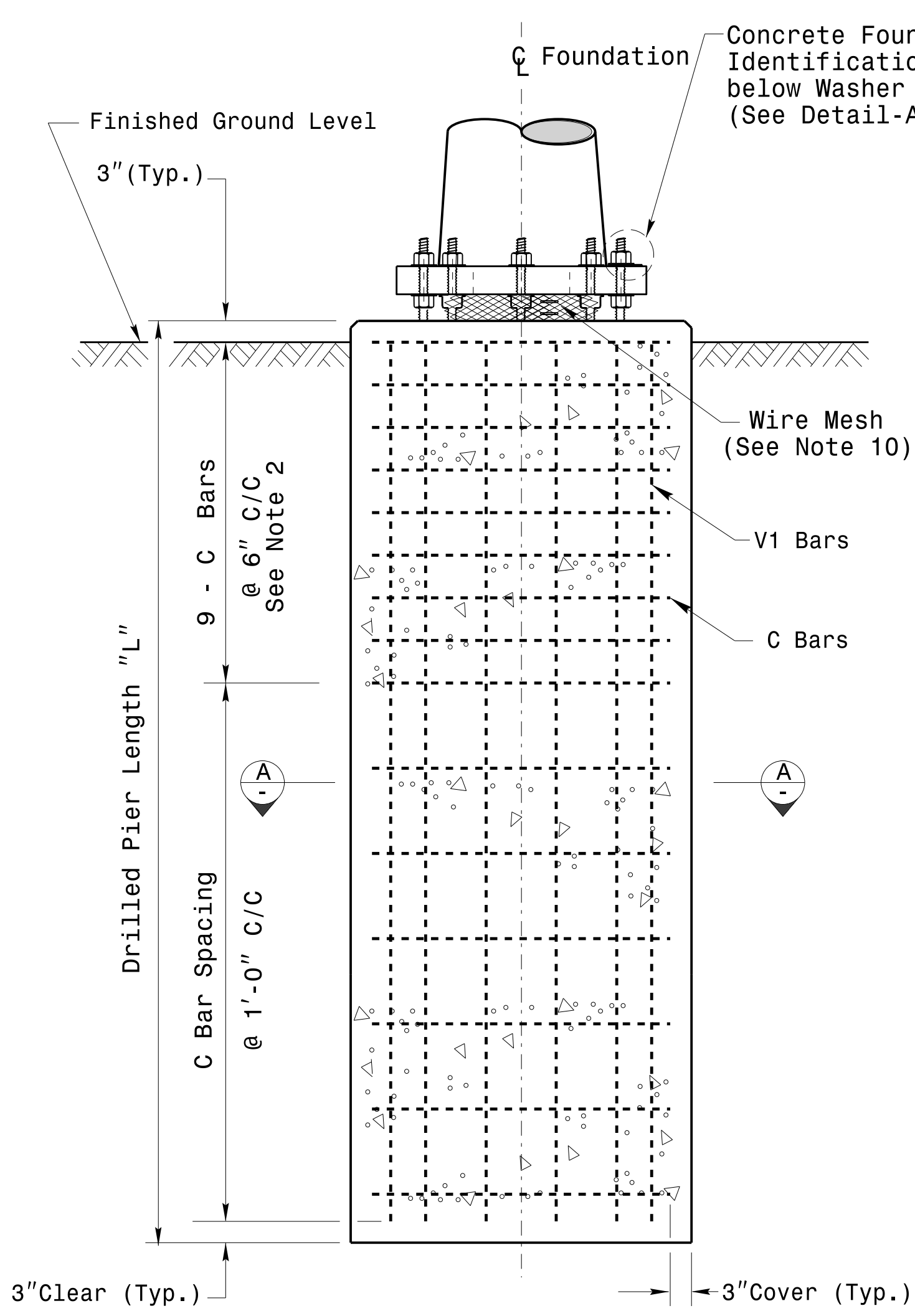


5/8" Dia Copper Clad Steel Grounding Electrode with an Irreversible Compression Ground Connector. For reference refer to section 1700-3 K and L for electrical grounding and bonding requirements, See Note 4.

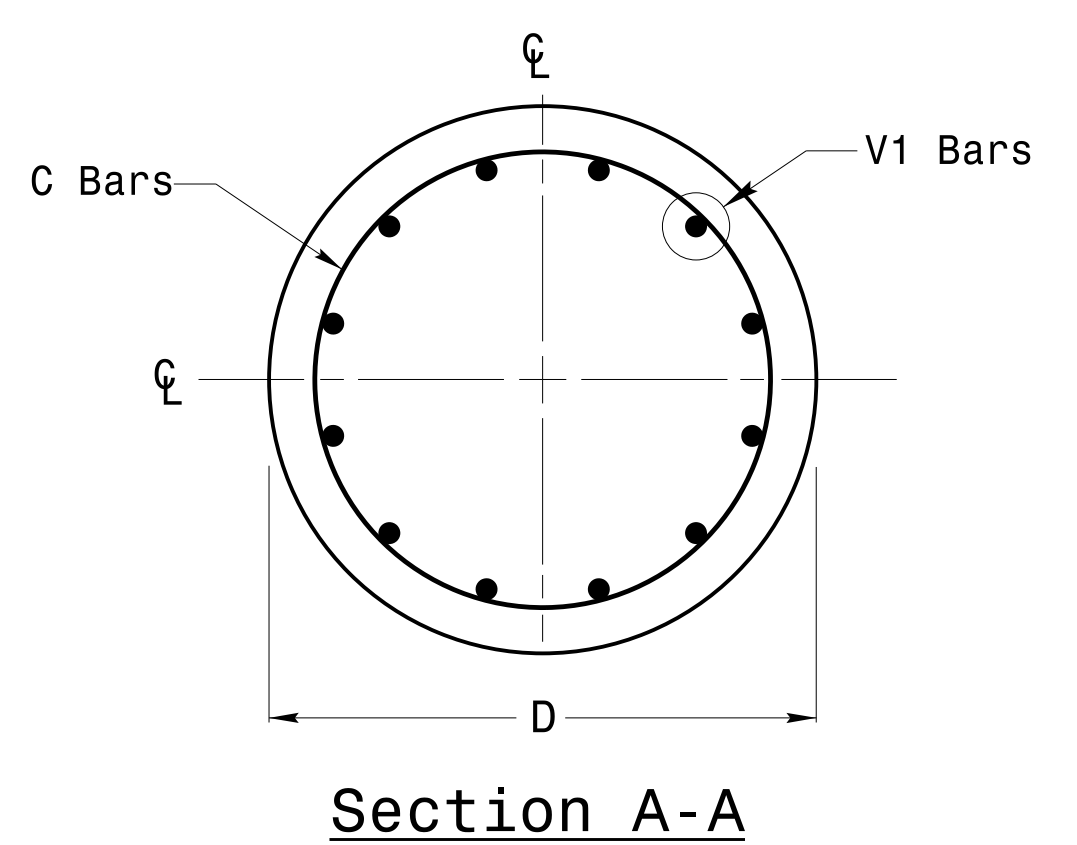
**Metal Pole Grounding Detail For Strain Pole and Mast Arm**

	<b>Typical Fabrication Details For Strain Pole Attachments</b>		
	PLAN DATE: OCTOBER 2017 PREPARED BY: N. BITTING	DESIGNED BY: C.F. ANDREWS REVIEWED BY: D.C. SARKAR	
REVISIONS: _____ INIT. DATE		DocuSigned by: <i>Deshi C. Sarkar</i> 10/11/2017 DATE	

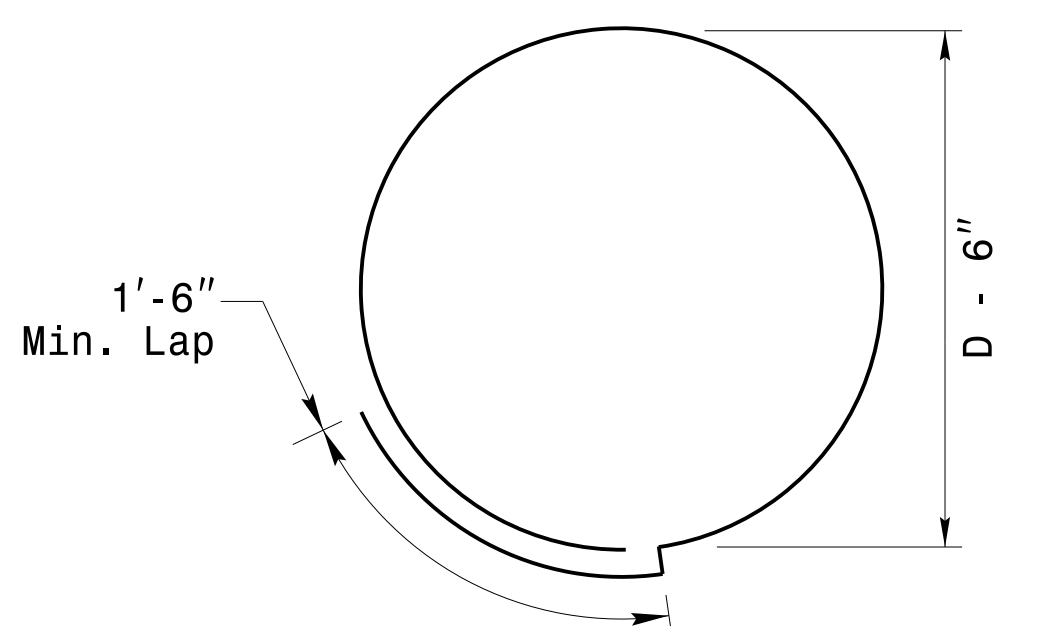
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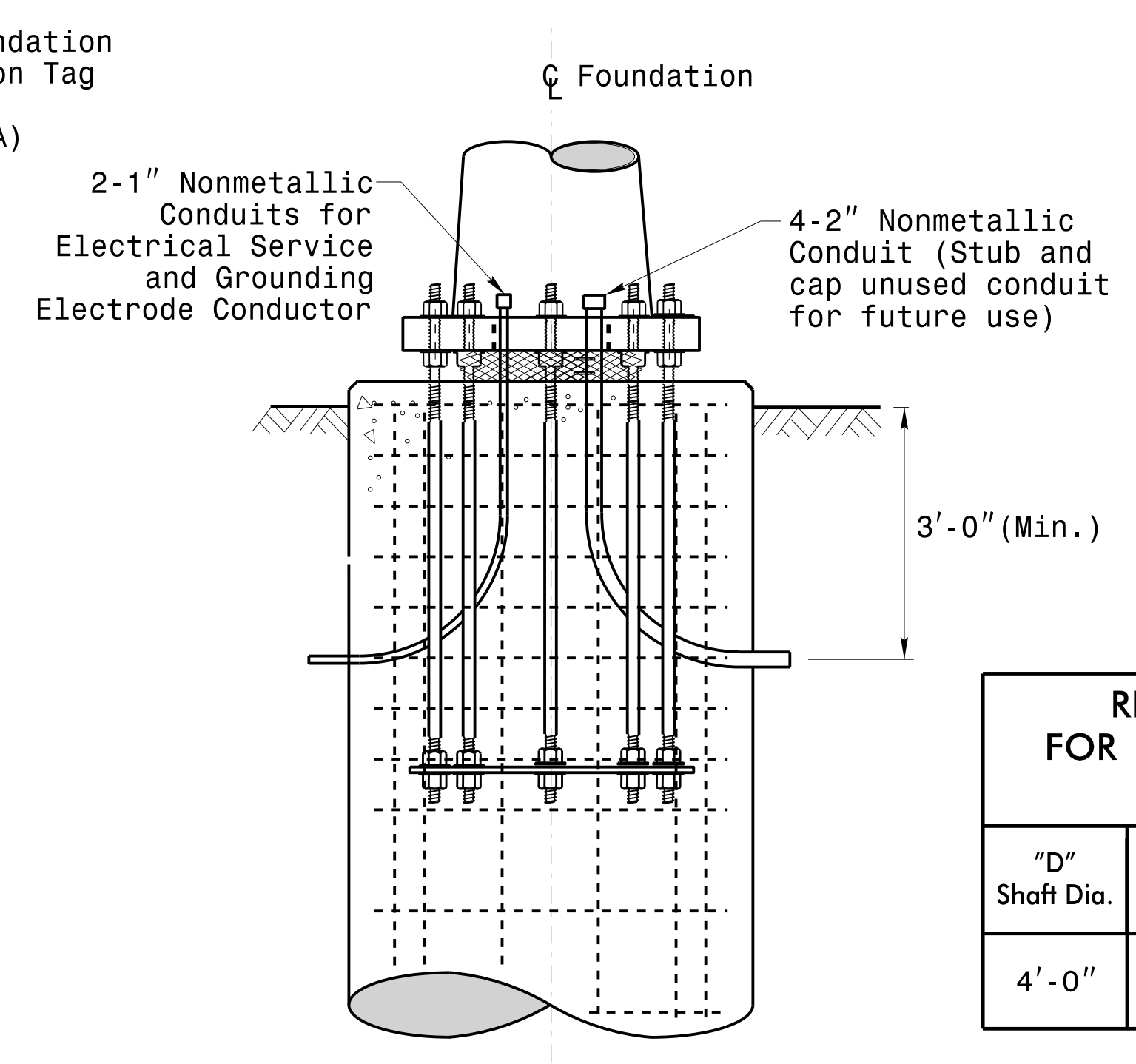
**Concrete Shaft Elevation**



**Section A-A**



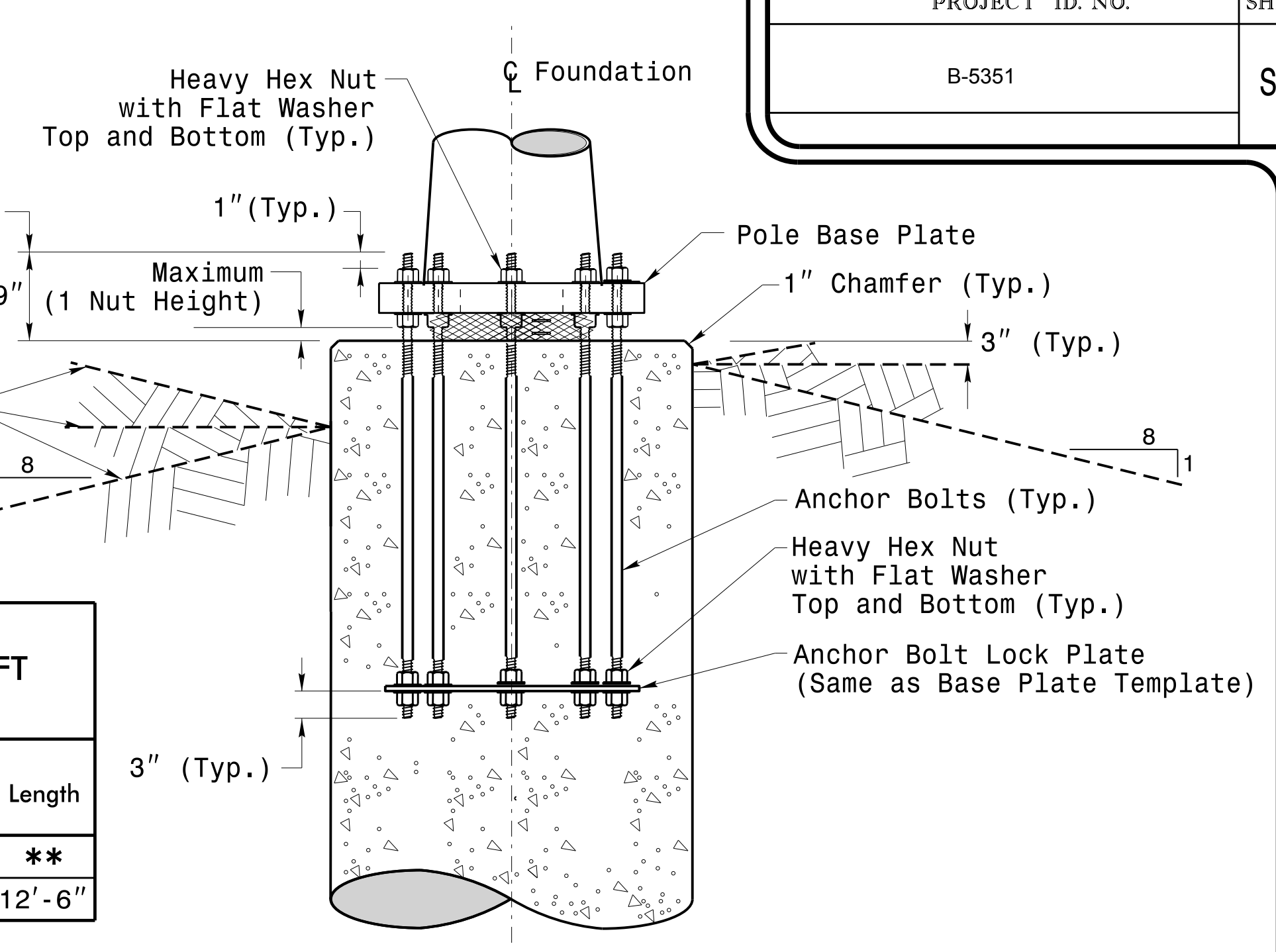
**Typical "C" Bar Detail**



**Typical Foundation Conduit Details**

"D" Shaft Dia.	Conc. Volume (cu. yds.)	Bar Name	MIN.	Size	Type	Length
4'-0"	.465 x L	V1	-	#8	STR.	**
		C	*	#4	CIR.	12'-6"

\* See Note No. 2  
\*\* See Note No. 3

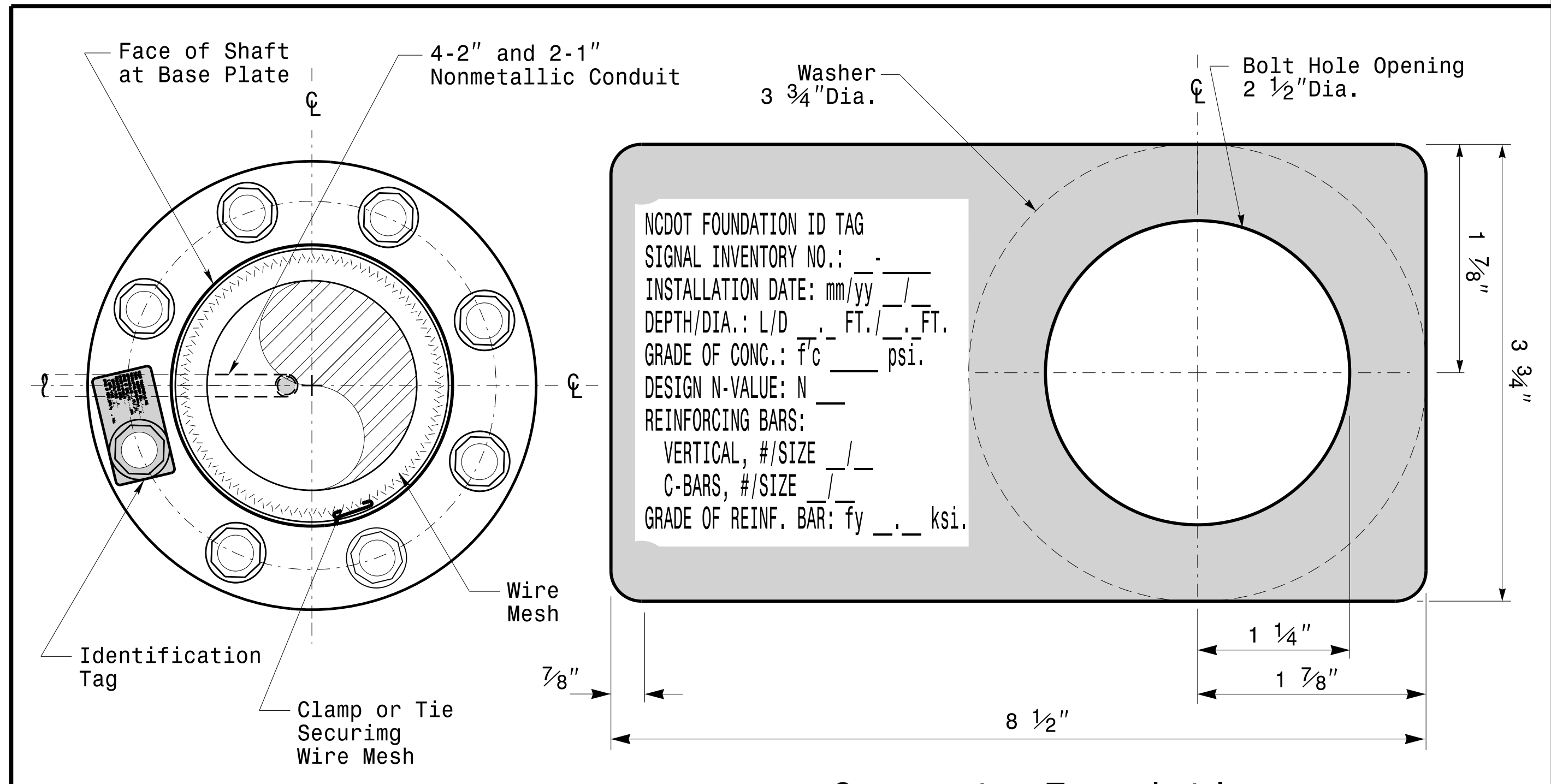


**Typical Foundation Anchor Bolt Details**

(Reinforcing Cage Not Shown for Clarity)

**General Notes:**

1. If actual subsurface conditions differ significantly from boring data contact the Engineer before excavating or placing concrete.
2. Circular tie reinforcing rings may be vertically adjusted by +/-3" at a depth between 2'-0" and 3'-0" to facilitate the installation of electrical conduit entering in the cage.
3. For standard foundations, see sheet Sig. M8 for details. Vertical reinforcing bars (V1) may be horizontally adjusted by +/-3" to facilitate the installation of electrical conduit entering into the cage.
4. Provide 2" to 5" foundation projection above ground level depending on the ground slope.
5. Unless otherwise shown, foundation designs are based on non-sloping level ground surfaces with slope ratios of 8:1 (H:V) or flatter. If actual ground line slopes are steeper contact the Engineer before excavating or placing concrete.
6. Construct foundations in accordance with NCDOT Standard Provisions SP09 R005- Foundations and Anchor Rod Assemblies for Metal Poles. All applicable 2018 NCDOT Standard Specifications are referenced in this provision. Refer to the NCDOT Resources/Specifications page located on the Connect NCDOT website.  
[https://connect.ncdot.gov/resources/Specifications and Special Provisions.aspx](https://connect.ncdot.gov/resources/Specifications%20and%20Special%20Provisions.aspx)
7. Use air entrained AA concrete mix with a compression strength of f'c=4500 psi.(min.) after 28 days.
8. Use ASTM A615 grade 60 deformed bars for all reinforcing steel. Maintain at least 3" cover on all reinforcement.
9. Locate the Identification Tag on the top of the base plate, directly above the conduit's entry point.
10. Provide two layers of galvanized welded 23 gauge (0.25) 6" wide 4 mesh wire around pipes under the base plate and secure it with ties if necessary.
11. Preferred location for the I.D. Tag is as shown in Detail-A; directly above the conduit entering the foundation.



**Concrete Foundation Identification Tag Details**

**Detail-A**

D = Diameter  
L = Length/Depth  
mm = Month  
yy = Year

<p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p><b>Construction Details For Foundations</b></p>			
	<p>PLAN DATE: OCTOBER 2018</p>	<p>DESIGNED BY: C.B. COGDILL</p>		<p>SCALE: NONE</p>
	<p>PREPARED BY: N. BITTING</p>	<p>REVIEWED BY: D.C. SARKAR</p>		
<p>REV. NO. 1</p>	<p>COMMENTS: Revised Foundation Tag Details</p>	<p>INIT. N.B.</p>	<p>DATE: 5/11/2015</p>	

11-001-2017-08:33T / S:\S60\W115-Stipolis\sigal Design Section\Eastern Region\W115-Sig.M7-Stipolis-Construction-Details-Poles.dgn

**Construction Details - Foundations**

# SOIL CONDITION

PROJECT ID. NO. B-5351	SHEET NO. Sig.M8
---------------------------	---------------------

		STANDARD STRAIN POLES					STANDARD FOUNDATIONS 48" Diameter Drilled Pier Length (L) - Feet							Reinforcement				
		Case No.	Pole Height (Ft.)	Base Plate BC (In.)	Reactions at the Pole Base			Clay				Sand			Longitudinal		Stirrups	
					Axial (kip)	Shear (kip)	Moment (ft-kip)	Medium N-Value 4-8	Stiff N-Value 9-15	Very Stiff N-Value 16-30	Hard N-Value >30	Loose N-Value 4-10	Medium N-Value 11-30	Dense N-Value >30	Bar Size (#)	Quantity (ea.)	Bar Size (#)	Spacing (in.)
WIND ZONE 1	LIGHT	S26L3	26	25	2	11	270	19	13	10	8	17	14.5	12.5	8	12	4	12
		S30L3	30	25	2	11	300	19.5	13.5	10	8	17.5	15	13	8	14	4	12
		S35L3	35	25	3	11	320	20	13.5	10.5	8	17.5	15	13	8	14	4	12
	HEAVY	S30H3	30	29	3	16	450	24.5	16	12	9	21	17.5	15	8	16	4	6
		S35H3	35	29	4	16	515	26	17	12.5	9.5	22	18.5	16	8	16	4	6
WIND ZONE 2	LIGHT	S26L2	26	23	2	10	245	18	12.5	9.5	8	16.5	14	12	8	12	4	12
		S30L2	30	23	2	10	270	18.5	12.5	10	8	16.5	14	12.5	8	12	4	12
		S35L2	35	23	3	10	300	19.5	13	10	8	17	14.5	13	8	12	4	12
	HEAVY	S30H2	30	29	3	15	415	23	15.5	11.5	9	20	17	14.5	8	16	4	6
		S35H2	35	29	4	15	475	25	16.5	12	9.5	21	17.5	15.5	8	16	4	6
WIND ZONE 3	LIGHT	S26L2	26	23	2	10	245	18	12.5	9.5	8	16.5	14	12	8	12	4	12
		S30L2	30	23	2	10	270	18.5	12.5	10	8	16.5	14	12.5	8	12	4	12
		S35L2	35	23	3	10	300	19.5	13	10	8	17	14.5	13	8	12	4	12
	HEAVY	S30H2	30	29	3	15	415	23	15.5	11.5	9	20	17	14.5	8	16	4	6
		S35H2	35	29	4	15	475	25	16.5	12	9.5	21	17.5	15.5	8	16	4	6
WIND ZONE 4	LIGHT	S26L1	26	22	2	8	190	16	11.5	8.5	8	15	12.5	11	8	12	4	12
		S30L1	30	22	2	8	205	16.5	11.5	9	8	15	13	11.5	8	12	4	12
		S35L1	35	22	3	8	230	17	12	9	8	15.5	13.5	11.5	8	12	4	12
	HEAVY	S30H1	30	25	3	12	320	20.5	13.5	10.5	8	18	15	13.5	8	16	4	6
		S35H1	35	25	4	12	350	21	14	10.5	8.5	18.5	15.5	13.5	8	16	4	6
WIND ZONE 5	LIGHT	S26L2	26	23	2	10	245	18	12.5	9.5	8	16.5	14	12	8	12	4	12
		S30L2	30	23	2	10	270	18.5	12.5	10	8	16.5	14	12.5	8	12	4	12
		S35L2	35	23	3	10	300	19.5	13	10	8	17	14.5	13	8	12	4	12
	HEAVY	S30H2	30	29	3	15	415	23	15.5	11.5	9	20	17	14.5	8	16	4	6
		S35H2	35	29	4	15	475	25	16.5	12	9.5	21	17.5	15.5	8	16	4	6

### General Notes:

- Values shown in the "Reactions at the Pole Base" column represent the minimum acceptable capacity allowed for design using a design CSR of 1.00.
- Use chairs and spacers to maintain proper clearance.
- For foundation, always use air-entrain concrete mix.

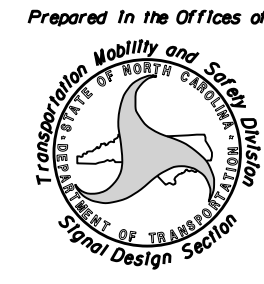

### Foundation Selection:

- Perform a standard penetration test at each proposed foundation site to determine "N" value.
- Select the appropriate wind zone from M 1 drawing.
- Select the soil type (Clay or Sand) that best describes the soil characteristics.
- Get the appropriate standard pole case number from the plans or from the Engineer.
- Select the appropriate column under "Standard Foundations" based on soil type and "N" value. Select the appropriate row based on the pole load case.
- The foundation depth is the value shown in the "Standard Foundations" category where the column and the row intersect.
- Use Construction Procedures and Design Methods prescribed by FHWA-NHI-10-016 for Reference Drilled Shafts.

48" Dia. Foundations Concrete Volume (cubic yards) = (0.465) x Drilled Pier Length

**Standard Strain Pole Foundation-All Soil Condition**

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	<p><b>Standard Strain Pole Foundation for All Soil Conditions</b></p> <p>PLAN DATE: OCTOBER 2017    DESIGNED BY: C.B. COGDILL                  PREPARED BY: N. BITTING    REVIEWED BY: D.C. SARKAR</p>	
SCALE: 0 NA NONE	REVISIONS: Changed "Foundation Depth" to "Drilled Pier Length" in Conc. Egn. N.B. 7/12/2015	DATE: 10/11/2017