

**OVERLAP PROGRAMMING DETAIL
FOR DEFAULT PHASING**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN
'1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      :12345678910111213141516
VEH OVL PARENTS: :X
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW - GREEN
-----
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

PRESS '+' TWICE

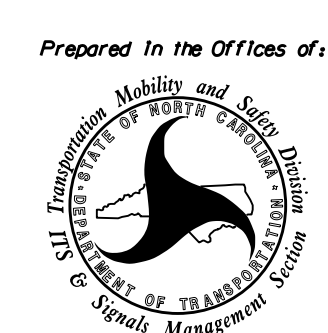
```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      :12345678910111213141516
VEH OVL PARENTS: : X
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW - GREEN
-----
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1191T3
DESIGNED: January 2018
SEALED: 2/7/2018
REVISED: N/A

08-FEB-2018 11:37
S:\MITSAS\1515\Signal\work\groups\Sig_MonHArmsrstrong071191_sm.ele.xxx.dgn
sarmstrong

Electrical Detail - Temporary Design 3 (TMP Phase III) Sheet 2 of 5		DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED	
ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	I-85 Bus. / US 29-70 at SR 1144 (River Road)		
	Division 7 PLAN DATE: February 2018 PREPARED BY: S. Armstrong	Guilford County REVIEWED BY: REVIEWED BY:	Jamestown REVIEWED BY:
REVISIONS _____ _____ _____	INIT. _____ _____ _____	DATE _____ _____ _____	SIG. INVENTORY NO. 07-1191T3