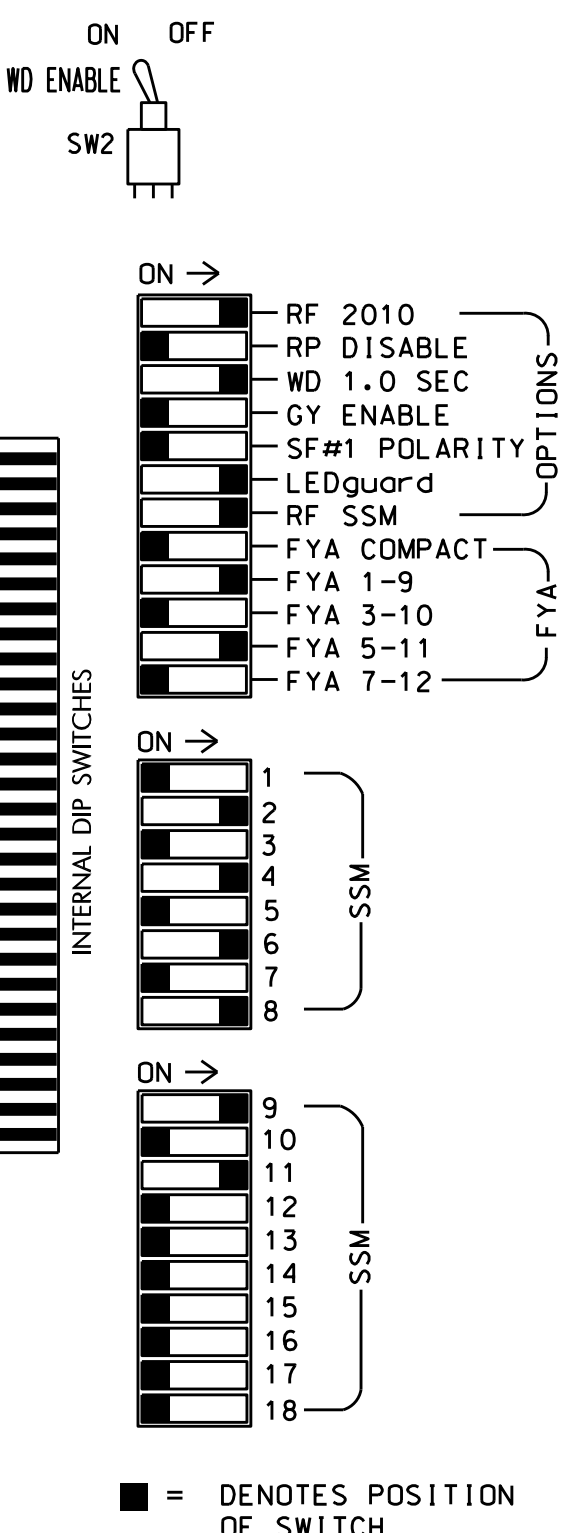
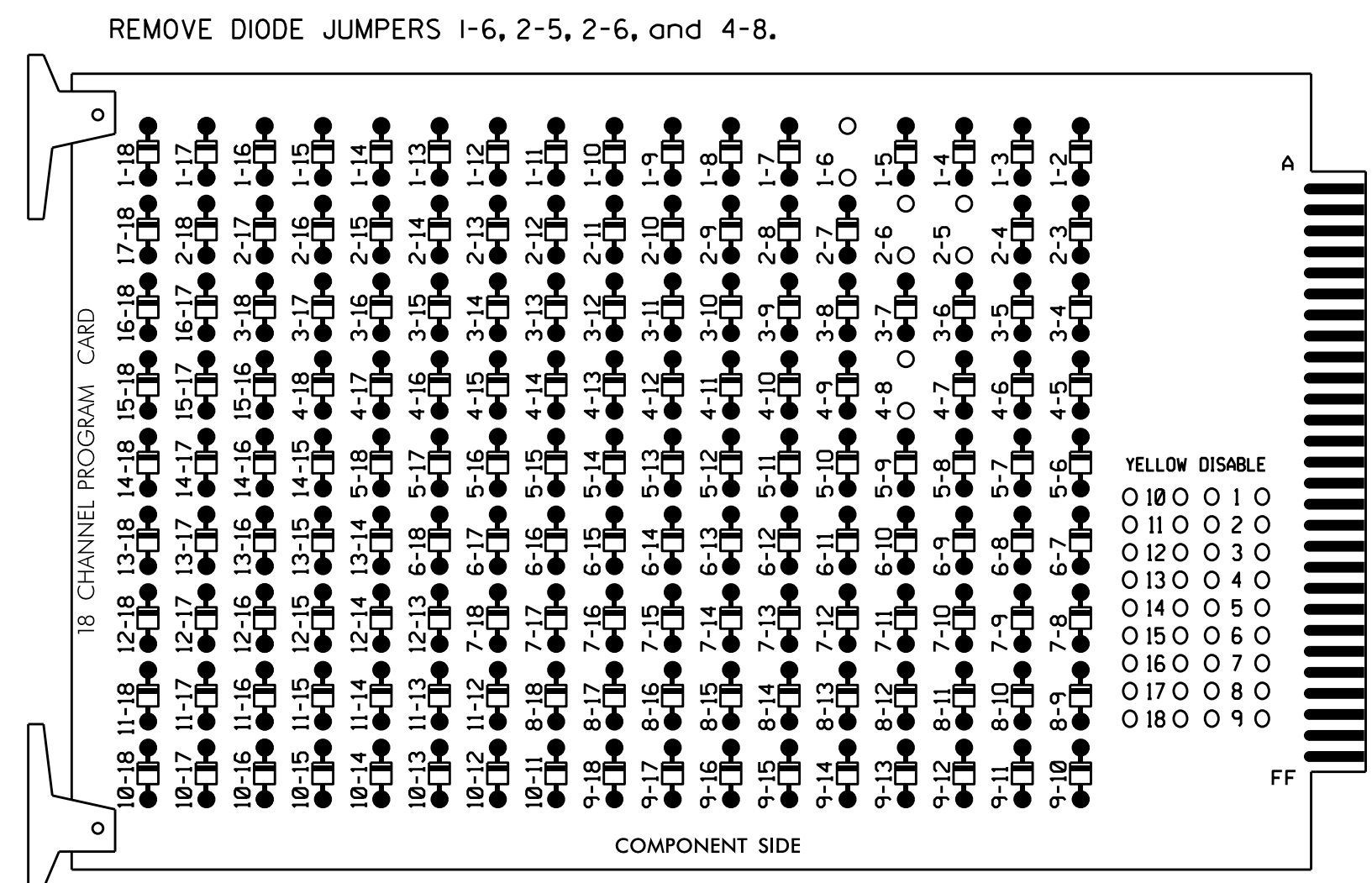


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



- NOTES:**
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Return controller to Factory Defaults before programming per this electrical detail.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

EQUIPMENT INFORMATION

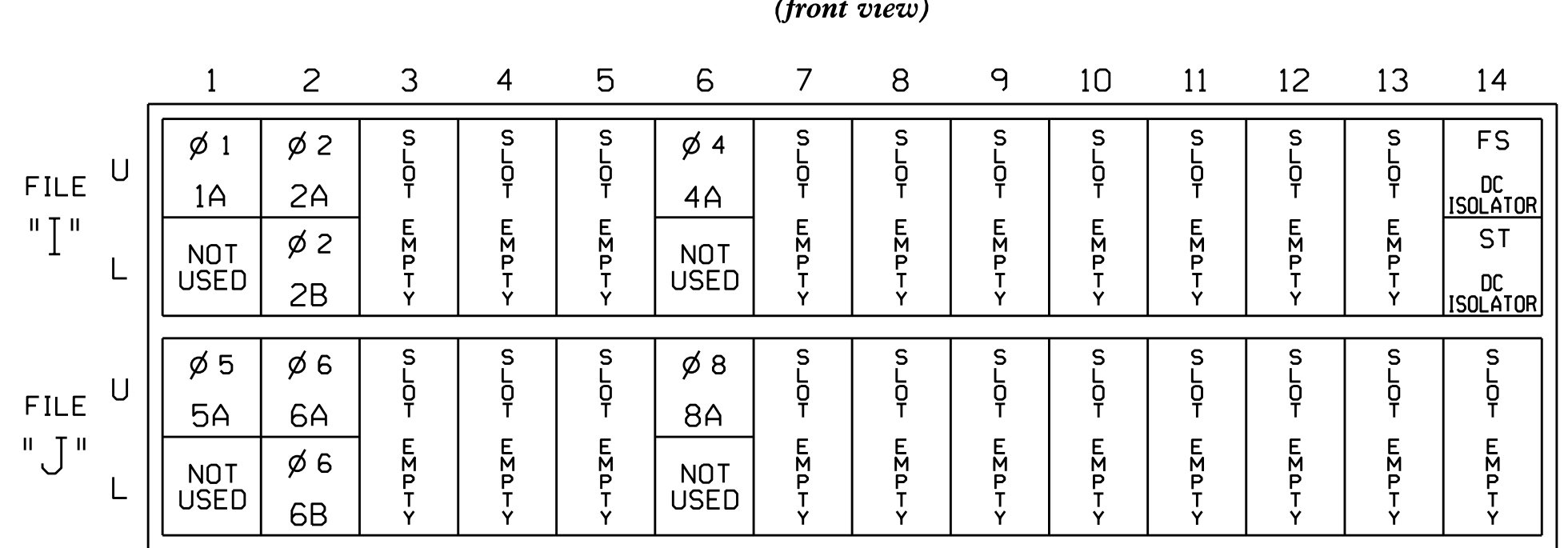
CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUX S1,AUX S4
 PHASES USED.....1,2,4,5,6,8
 OVERLAP "A".....1
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22,23	NU	NU	41,42,43	NU	51	61,62,63	NU	NU	81,82,83	NU	11	NU	NU	51	NU	NU	
RED		128			101			134			107								
YELLOW	*	129			102		*	135			108								
GREEN		130			103			136			109								
RED ARROW													A121				A114		
YELLOW ARROW														A122				A115	
FLASHING YELLOW ARROW																			A116
GREEN ARROW	127							133											

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT



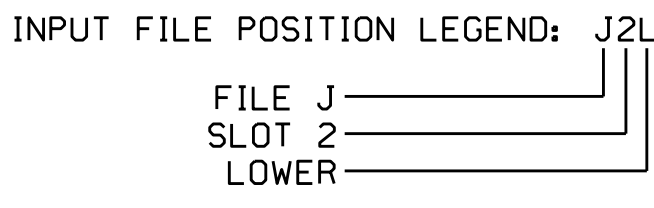
EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

See AC Isolator programming detail on sheet 3.

INPUT FILE CONNECTION & PROGRAMMING CHART

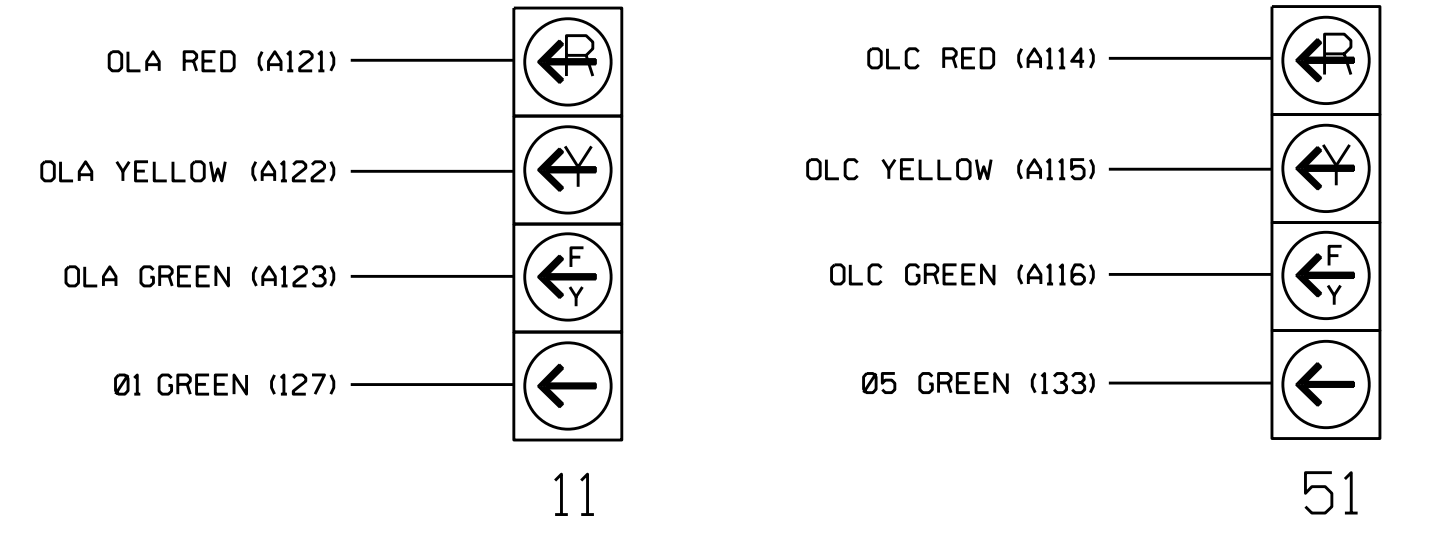
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

Remove the following wires, if present:
 From I1-W to J4-W, on rear of input file.
 From J1-W to I4-W, on rear of input file.



FYA SIGNAL WIRING DETAIL

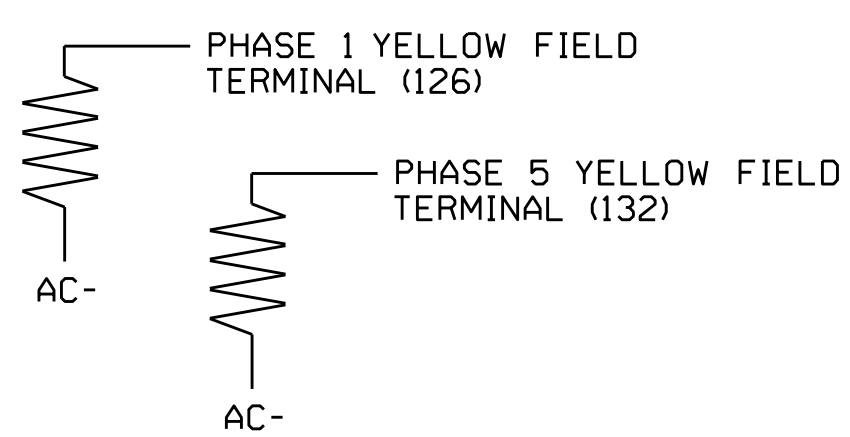
(wire signal heads as shown)



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



PHASE SEQUENCE PROGRAMMING DETAIL

(program controller as shown below)

FROM OASIS LOCAL CONTROLLER MAIN MENU
 SELECT: 4 PHASE SEQUENCE

PHASE SEQUENCE: PAGE 1		NEXT: PAGES	
RNG:LEAD	BARRIER 1	X-LAG:LEAD	BARRIER 2
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0

Electrical Detail - Temporary Design 3 (TMP Phase III)
 Sheet 1 of 5

Prepared In the Offices of:
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 NORTH CAROLINA PROFESSIONAL ENGINEER
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Division 7 Guilford County Jamestown

PLAN DATE: February 2018 REVIEWED BY:
 PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by:
 Keith M. Minas 2/8/2018
 DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 KEITH M. MINAS
 036880

SIG. INVENTORY NO. 07-1191T3

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