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**PROJECI** 

# 7649. 3

Index of Plans SIG. 2.0 - 5.4 SIG. 12.0 - 16.3 SR 1002 (AVIATION PARKWAY) @ NATIONAL **SIG.** 17.0 - 19.3 05–1399 SIG. 20.0 - 20.1 SIG. M1 - M8 METAL POLE STANDARD DRAWINGS SIGNALS COMMUNICATION PLANS **LEGEND** (XX-XXXX) - SIGNAL INVENTORY NUMBER

#### INTELLIGENT TRANSPORTATION AND SIGNALS UNIT Contacts:

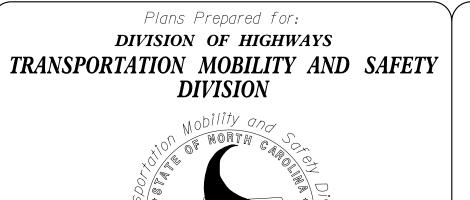
Rob Ziemba, PE

Todd Joyce, PE

I. Neil Avery

Heidi Berggren, EI

Signal Communications Project Design Engineer



# Stantec

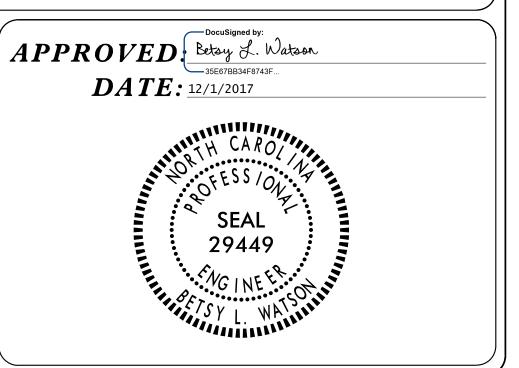
Stantec Consulting Services Inc. 801 Jones Franklin Rd-Suite 300 Raleigh, NC 27606

Tel. 919.851.6866 Fax. 919.851.7024 www.stantec.com License No. F-0672

Betsy L. Watson, PE

Regina Muncey, PE

Larry Overn, PE



Signals Engineer, Central Region Signal Equipment Design Review Engineer

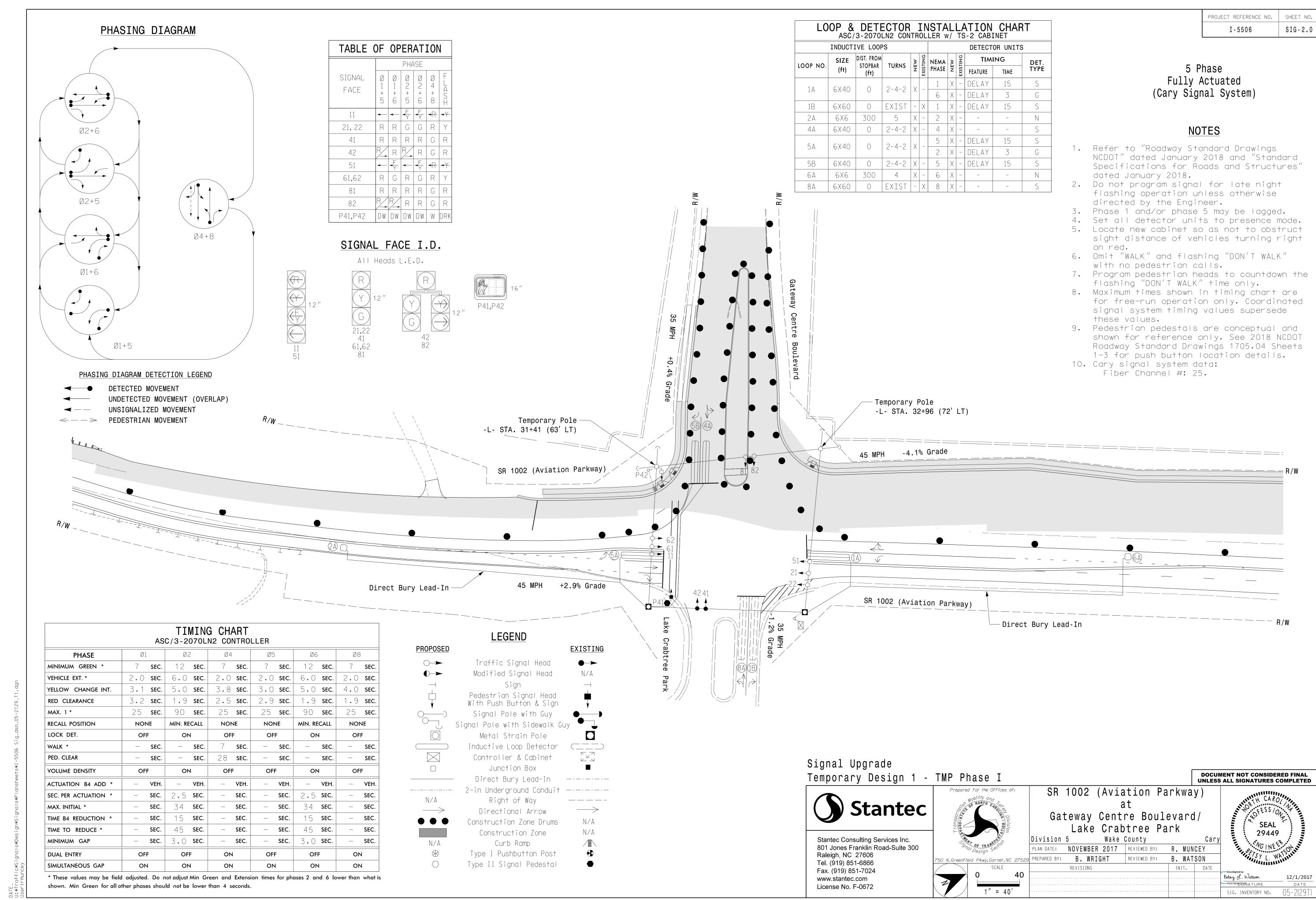
Signal Communications Project Engineer

750 N. Greenfield Parkway, Garner, NC 27529

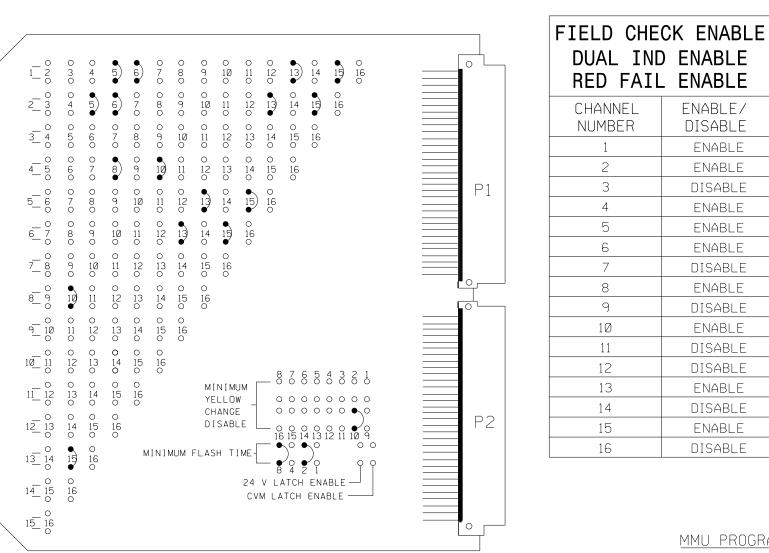
Senior Transportation Engineer

Transportation Engineer

Transportation Engineer



(program card and tables as shown)



MMU PROGRAMMING CARD

OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	LLOW ARROW								
CONFIG MODE	В								
ENABLE CHANN	NEL PAIR, FYA								
CH 1-13	ON								
CH 3-14	OFF								
CH 5-15	ON								
CH 7-16	OFF								
RED/YEL INF	PUT ENABLE								
CH 1	ON								
CH 3	OFF								
CH 5	ON								
CH 7	OFF								
FLASH RATE FAULT	ON								
FYA TRAP DETECT	ON								

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

ENABLE

ENABLE

DISABLE

ENABLE

ENABLE ENABLE

DISABLE

ENABLE

DISABLE

ENABLE

DISABLE DISABLE

ENABLE

DISABLE

ENABLE

DISABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,7,9,11,12,14&16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. Program phases 4 and 8 for dual entry.
- 11. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-2.1

				S	IGN	AL	HEA	AD F	100	K-U	P C	HAF	RT					
PHASE		1	2	3	4		5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11	82	21,22	NU	41,42	<b>★</b> 51	42	61,62	NU	81,82	NU	P41, P42	NU	NU	11	NU	<b>★</b> 51	NU
RED		*	2R		4R		*	6R		8R								
YELLOW			2Y		4 Y			6Y		8Y								
GREEN			2G		4G			6G		8G								
RED ARROW															13R		15R	
YELLOW ARROW		1 Y					5Y								13Y		15Y	
FLASHING YELLOW ARROW															13G		15G	
GREEN ARROW	1G	1G				5G	5G											
₩												1ØR						
×												1ØG						
₩.	Used																	

- \* Denotes install load resistor. See Load Resistor Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1		CH1			
		L3	L1	L7	L5	L11	L9	S	L13	S	S	S
		Ø 1	Ø 1	Ø 4	Ø2	Ø6	Ø5		Ø5	L		
RACK #1		1B	1A	4 A	2A	6A	5A	T	5B	T	T	T
** <u>I</u>	BIU	CH2	CH2	CH2	CH2	CH2	CH2	E	CH2	E	E	E
		L4	L2	L8	L6	L12	L10	M P	L14	M P	M	МР
		NOT USED	Ø6	NOT USED	NOT USED	NOT USED	Ø2	T Y	NOT USED	T Y	T	
			1 🖰				5A					
								I			l	

RACK		S L O T	сн1 L17 Ø8 8А	S L O T	S L O T	SLOT	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	
#2	BIU	E M P T Y	CH2 L18 NOT USED	E M P T Y									

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE IN THE CHART BELOW SHOWN IN THE CHART BELOW

	IN THE C	LHARI BELUW	1		SHUWN	IN THE	CHARI B	ELUW
	LOOP NO.	LOOP PANEL TERMINALS			ROLLER	FUNCTION	ΤΙ	MING
ADD JUMPERS FROM:		L1A,L1B		DETE	CTOR NO.	1 011011011	FEATURE	TIME(SEC)
L1A TO L2A, AND	1 A				1	Ø 1	DELAY	15
L1B TO L2B	4.5	L2A,L2B		*	2	Ø 6	DELAY	3
	1 B	L3A,L3B			3	Ø 1	DELAY	15
	NU	L4A,L4B				У 1	DELAT	1.3
	2 A	L5A,L5B			4			
	NU	L6A,L6B		**	5	Ø 2		
	4 A	L7A,L7B			6			
					7	Ø 4		
	NU	L8A,L8B			8			
ADD JUMPERS FROM: L9A TO L1OA, AND	5 A	L9A,L9B			9	d =	DELAY	1.5
L9B TO L10B	AC	L10A,L10B			-	Ø 5	DELAY	15
	6A	L11A,L11B		*		Ø 2	DELAY	3
	NU	L12A,L12B		**	11	Ø 6		
					12			
	5B	L13A,L13B			13	Ø 5	DELAY	15
	NU	L14A,L14B				ρ 3	DEEM	13
	NU	L15A,L15B			14			
	NU	L16A,L16B			15	*		
					1 C			1

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE IN THE CHART RELOW SHOWN IN THE CHART RELOW

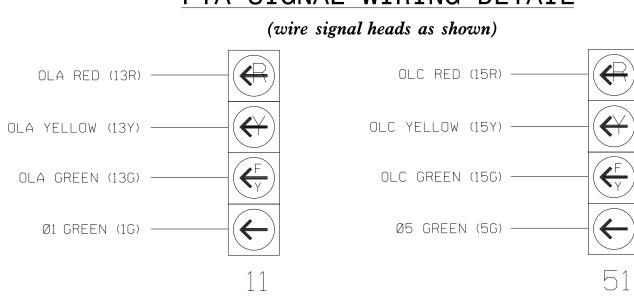
LOOP NO.	LOOP PANEL TERMINALS	CONTROLLER	FUNCTION	TI	MING
8 A	L17A,L17B	DETECTOR NO.	I UNCITUM	FEATURE	TIME(SEC)
		. 17	Ø 8		
NU	L18A,L18B	. 18			
NU	L19A,L19B	19			
NU	L20A,L20B	20			
NU	L21A,L21B				
NU	L22A,L22B	· 21			
NU	L23A,L23B	. 22	,		
NU	L24A,L24B	· 23			
NU	L25A,L25B	24	·		
	L26A,L26B	. 25			
NU		. 26			
NU	L27A,L27B	. 27			
NU	L28A,L28B	28			
NU	L29A,L29B				
NU	L30A,L30B	29			
NU	L31A,L31B	. 30			
NU	L32A,L32B	· 31			
		. 32			

#### **EQUIPMENT INFORMATION**

CONTROLLER.....2070LN2 CABINET ......TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....1,2,4,5,6,8,10,13,15 OLA....\* OLB.....NOT USED OLC....\* OLD.....NOT USED

\* See overlap programming detail on sheet 2

# FYA SIGNAL WIRING DETAIL



#### LOAD SWITCH ASSIGNMENT DETAIL

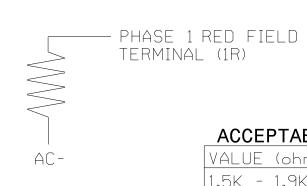
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	Ø 1
2	Ø 2
3	_
4	Ø 4
5	Ø 5
6	Ø 6
7	-
8	Ø 8
9	_
10	Ø4 PED
11	_
12	_
13	OLA
14	_
15	OLC
16	_
	VITCH CHANNE

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES VALUE (ohms) | WATTAGE

1.5K - 1.9K | 25W (mın) 2.0K - 3.0K | 10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 2 Temporary Design 1 - TMP Phase I



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SR 1002 (Aviation Parkway) Gateway Centre Boulevard/

Lake Crabtree Park Division 5 Wake County Cary

SIG. INVENTORY NO.

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY INIT. DATE REVISIONS

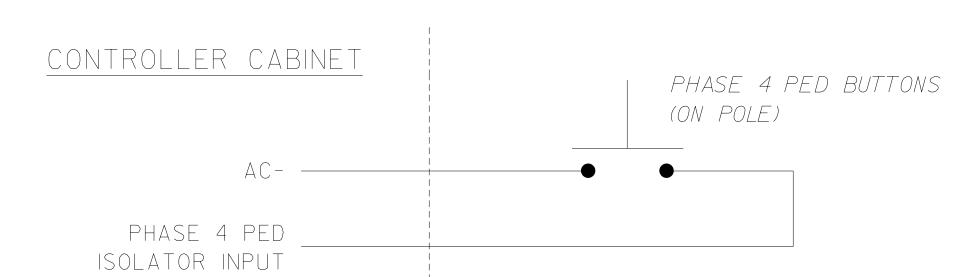
12/5/2017

\* Detector Type - G \*\* Detector Type - N

PROJECT REFERENCE NO. I-5506 SIG-2.2

#### PEDESTRIAN PUSH BUTTON WIRING DETAIL

(wire push buttons as shown)



#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

#### Electrical Detail - Sheet 2 of 2 Temporary Design 1 - TMP Phase I



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SR 1002 (Aviation Parkway) Division 5 Wake County

Gateway Centre Boulevard/ Lake Crabtree Park

PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 Toggle Twice Select TMG VEH OVLP [C] and 'PPLT FYA' TMG VEH OVLP...[C] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0 END PROGRAMMING

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

OVERLAP A

TMG VEH OVLP...[A] TYPE: .... PPLT FYA

PROTECTED LEFT TURN.... PHASE 1

OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH13 ISOLATE

OVERLAP C

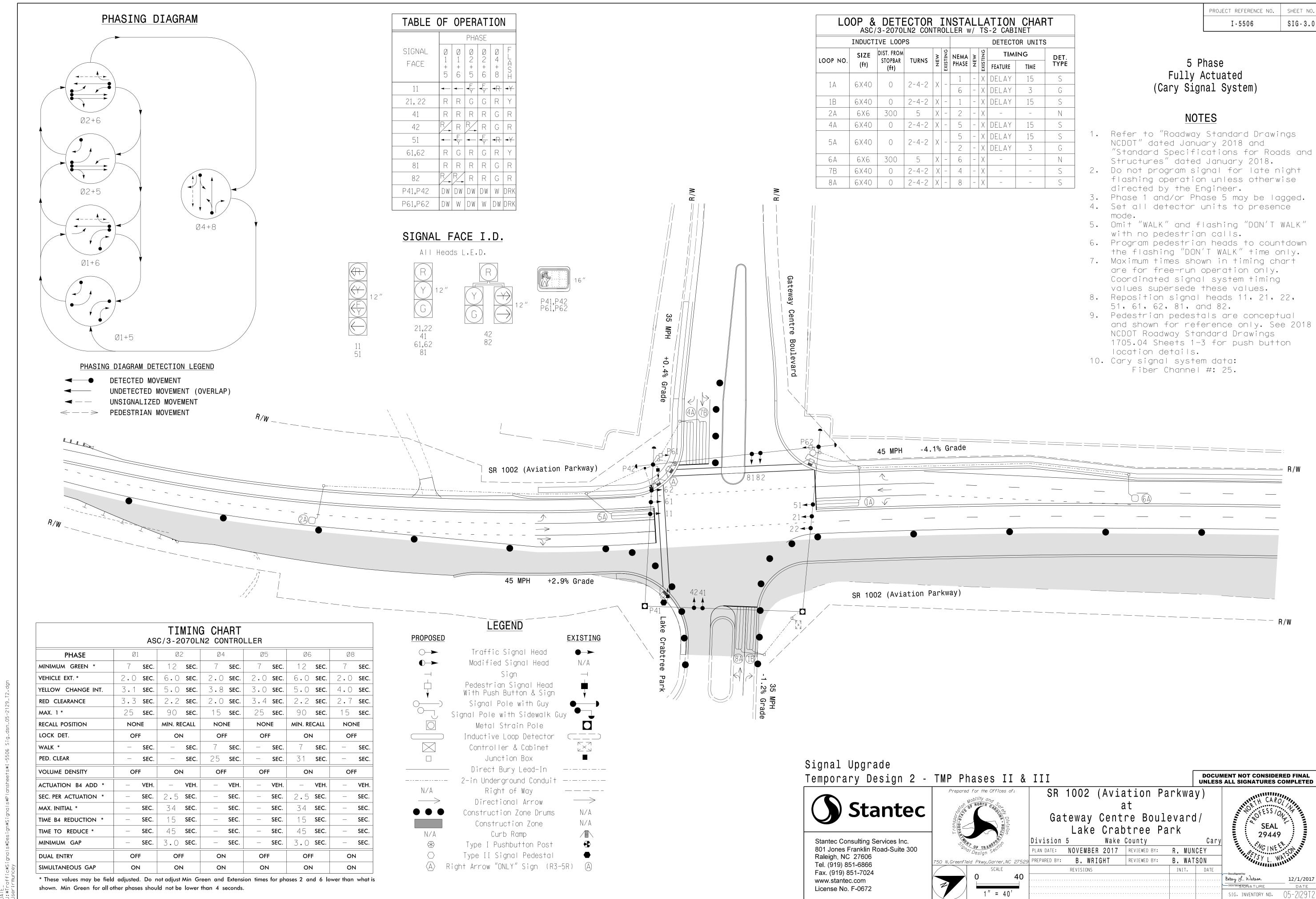
Select TMG VEH OVLP [A] and 'PPLT FYA'

1. From Main Menu select 2. CONTROLLER

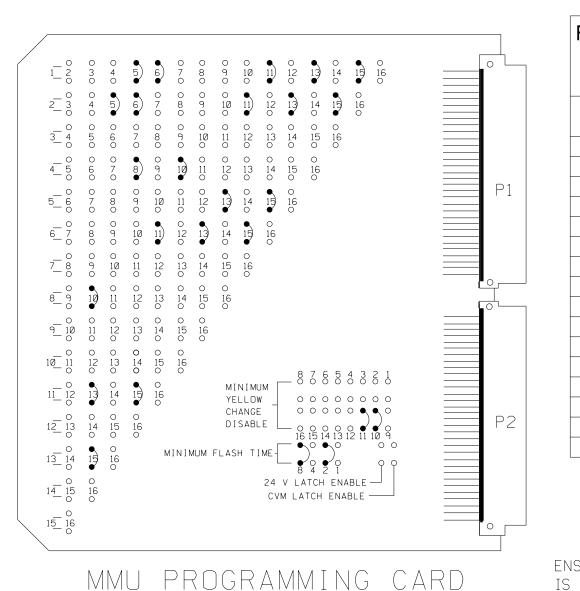
Tel. (919) 851-6866 Fax. (919) 851-7024

50 N.Greenfield Pkwy,Garner,NC 27529

SIG. INVENTORY NO.



(program card and tables as shown)



FIELD CHE DUAL IND RED FAIL	ENABLE						
CHANNEL NUMBER	ENABLE/ DISABLE						
1	ENABLE						
2	ENABLE						
3	DISABLE						
4	ENABLE						
5	ENABLE						
6	ENABLE						
7	DISABLE						
8	ENABLE						
9	DISABLE						
10	ENABLE						
11	ENABLE						
12	DISABLE						
13	ENABLE						
14	DISABLE						
15	ENABLE						
16	DISABLE						

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	ELLOW ARROW						
CONFIG MODE	В						
ENABLE CHANN	NEL PAIR, FYA						
CH 1-13	ON						
CH 3-14	OFF						
CH 5-15	ON						
CH 7-16	OFF						
RED/YEL INF	PUT ENABLE						
CH 1	ON						
CH 3	OFF						
CH 5	ON						
CH 7	OFF						
FLASH RATE FAULT	ON						
FYA TRAP DETECT	ON						

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,7,9,12,14&16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in Phase 2 Green and 6 Walk.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. Program phases 4 and 8 for dual entry.

CONTROLLER.....2070LN2

CABINET .....TS-2

CABINET MOUNT.....BASE

LOADBAY POSITIONS.....16

OLA.....\*

OLC....\*

OLB....NOT USED

OLD.....NOT USED

11. The cabinet and controller are a part of the Cary Signal System.

**EQUIPMENT INFORMATION** 

SOFTWARE .....ECONOLITE ASC/3-2070

LOAD SWITCHES USED.....1,2,4,5,6,8,10,11,13,15

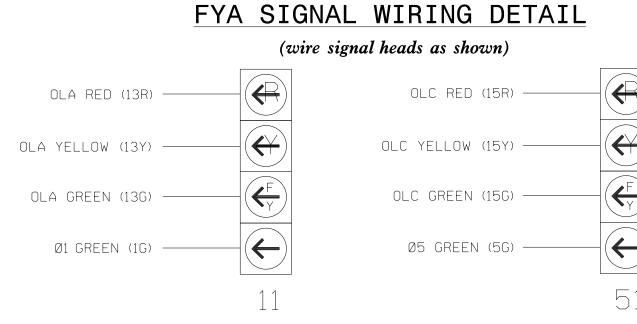
PHASES USED......1,2,4,4PED,5,6,6PED,8

\* See overlap programming detail on sheet 2

PROJECT REFERENCE NO. I-5506 SIG-3.1

				S	IGN	AL	HEA	AD H	100	K - UI	P C	HAF	RT					
PHASE		1	2	3	4		5		7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	<b>★</b>	82	21,22	NU	41,42	<b>*</b>	42	61,62	NU	81,82	NU	P41, P42	P61, P62	NU	11	NU	<b>★</b> 51	NU
RED		*	2R		4R		*	6R		8R								
YELLOW			2Y		4 Y			6Y		8Y								
GREEN			2G		4G			6G		8G								
RED ARROW															13R		15R	
YELLOW ARROW		1Y					5Y								13Y		15Y	
FLASHING YELLOW ARROW															13G		15G	
GREEN ARROW	1G	1G				5G	5G											
₩												1ØR	11R					
×												1ØG	11G					
NU = Not	Used																	

- \* Denotes install load resistor. See Load Resistor Installation Detail.
- ★ See pictorial of head wiring detail this sheet.



## DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1	CH1				
		L3	L1	L7	L5	L11	L9	L15	S	S	S	S
		Ø 1	Ø 1	Ø5	Ø2	ø6	Ø5	Ø 4	L	L		L
RACK #1		1B	1A	4 A	2A	6A	5A	7B	T	T	T	T
#1	BIU	CH2	CH2	CH2	CH2	CH2	CH2	CH2	E	E	E	E
		L4	L2	L8	L6	L12	L1Ø	L16	M	Мр	M	M
		NOT USED	Ø6	NOT USED	NOT USED	NOT USED	Ø2	NOT USED	T Y		T Y	T
			1 A				5A		·	·	·	, '
				I								1

RACK		SLOT	сні L17 Ø8 8А	S L O T	S L O T	SLOT	S L O T	S L O T	S L O T	SLOT	S L O T	S L O T
#2	BIU	E M P T Y	CH2 L18 NOT USED	E M P T Y	E M P T							

ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS IN THE CHART BELOW SHOWN IN THE CHART BELOW

	IN THE C	CHART BELOW	SHOWN	IN THE	CHART E	BELOW		
	LOOP NO.	LOOP PANEL TERMINALS	CONTROLLER	FUNCTION	TIMING			
ADD JUMPERS FROM:		L1A,L1B	DETECTOR NO.		FEATURE	TIME(SEC)		
L1A TO L2A, AND	1 A	L2A,L2B	1	Ø 1	DELAY	15		
L1B TO L2B			* 2	Ø6	DELAY	3		
	1B	L3A,L3B	3	Ø 1	DELAY	15		
	NU	L4A,L4B	4	γ '	DELAT	13		
	2 A	L5A,L5B		-/ -				
	NU	L6A,L6B	<del>**</del> 5	Ø 2				
	4 A	L7A,L7B	. 6					
	NU	L8A,L8B	· 7	Ø 5	DELAY	15		
ADD HIMDEDS EDOM.	NU		. 8					
ADD JUMPERS FROM: L9A TO L10A, AND	5 A	L9A,L9B	9	Ø 5	DELAY	15		
L9B TO L10B	071	L10A,L10B		· ·		3		
	6A	L11A,L11B	* 10	Ø 2	DELAY	3		
	NU	L12A,L12B	** 11	Ø6				
	NU	L13A,L13B	12					
			13					
	NU	L14A,L14B	1.4					
	7B	L15A,L15B	15	Ø 4				
	NU	L16A,L16B		μ <del>γ</del> 4				
			16					

LOOP NO.	LOOP PANEL TERMINALS	CONTROLLER	FUNCTION	ΤI	MING
Ο Λ		DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)
8 A	L17A,L17B	. 17	Ø 8		
NU	L18A,L18B	18			
NU	L19A,L19B	19			
NU	L20A,L20B				
NU	L21A,L21B	20			
NU	L22A,L22B	• 21			
NU	L23A,L23B	· 22			
NU	L24A,L24B	. 23			
		- 24			
NU	L25A,L25B	25			
NU	L26A,L26B	26			
NU	L27A,L27B				
NU	L28A,L28B	27			
NU	L29A,L29B	28			
NU	L30A,L30B	29			
NU	L31A,L31B	. 30			
		. 31			
NU	L32A,L32B	. 32			

NOTE: BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

#### LOAD SWITCH ASSIGNMENT DETAIL

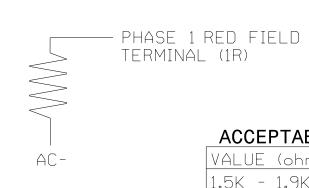
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	Ø 1
2	Ø 2
3	_
4	Ø 4
5	Ø 5
6	Ø 6
7	-
8	Ø 8
9	-
10	Ø4 PED
11	Ø6 PED
12	_
13	OLA
14	-
15	OLC
16	-
	./

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K | 25W (min)

2.0K - 3.0K | 10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129T2 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 2 Temporary Design 2 - TMP Phase II



License No. F-0672

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**UNLESS ALL SIGNATURES COMPLETED** SR 1002 (Aviation Parkway) Gateway Centre Boulevard/ Lake Crabtree Park

Division 5 Wake County Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS

**DOCUMENT NOT CONSIDERED FINAL** 

12/5/2017

\* Detector Type - G \*\* Detector Type - N

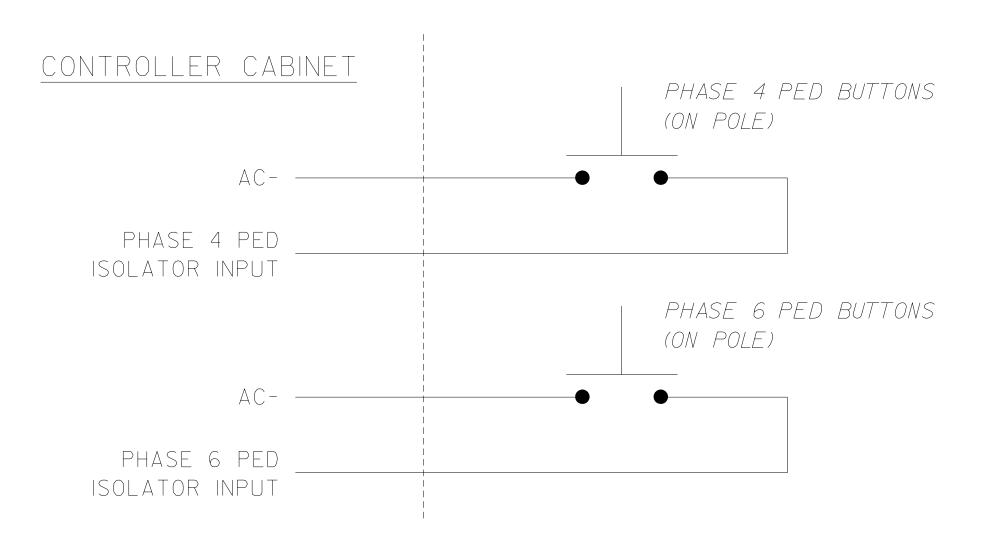
INIT. DATE

SIG. INVENTORY NO.

SIG-3.2 I-5506

#### PEDESTRIAN PUSH BUTTON WIRING DETAIL

(wire push buttons as shown)



#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129T2 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

## Electrical Detail - Sheet 2 of 2 Temporary Design 2 - TMP Phase II



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DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) Gateway Centre Boulevard/ Lake Crabtree Park

Division 5 Wake County PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

SIG. INVENTORY NO.

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .....PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O

ACTION PLAN SF BIT DISABLE..... O

Toggle Twice

OVERLAP C

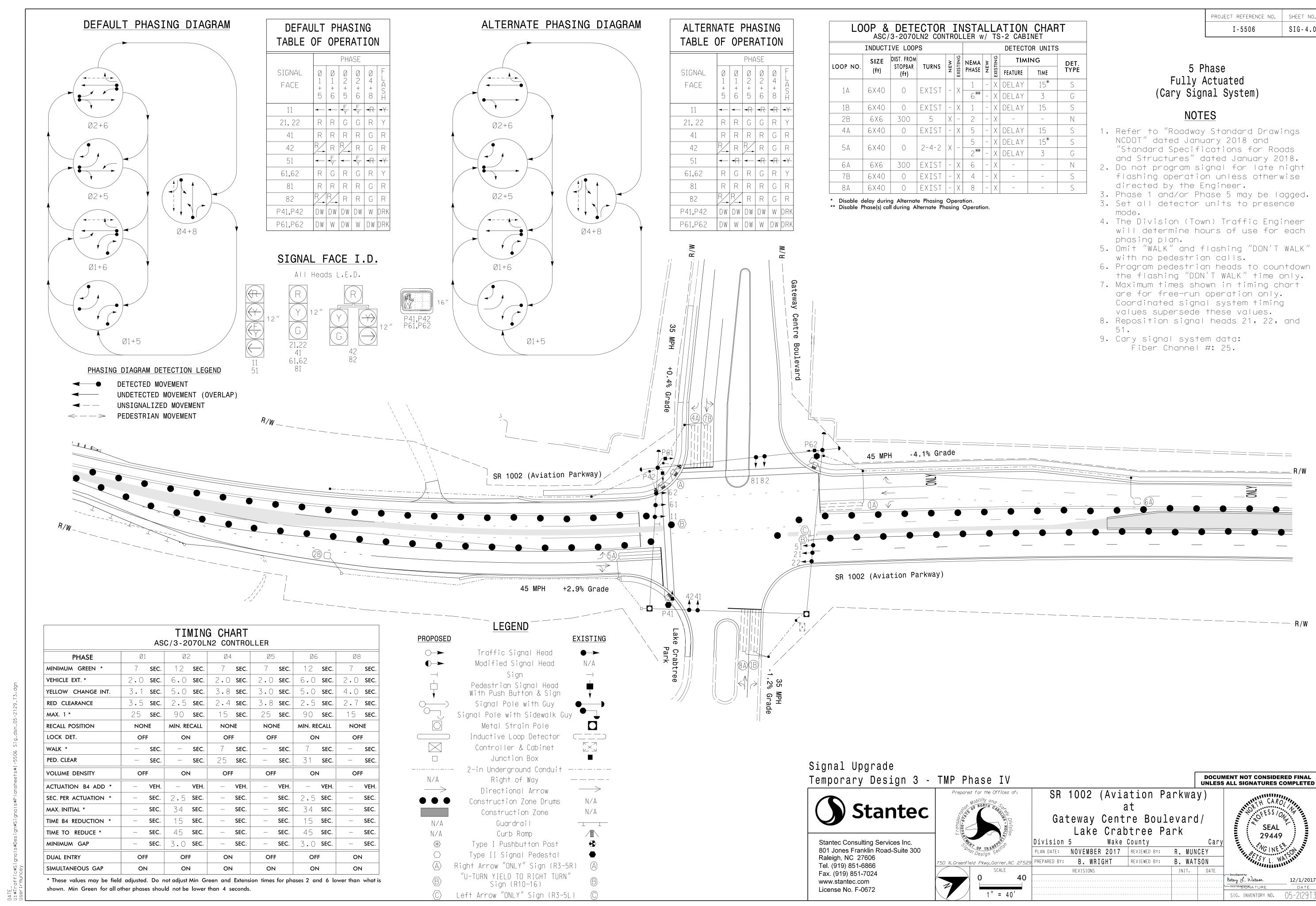
Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6

DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

FLASHING ARROW OUTPUT....CH15 ISOLATE

END PROGRAMMING



(program card and tables as shown)

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW.

L9

5A

L1Ø

M

USED

PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

L11 |

6A

L6 L12

Ø2 NOT

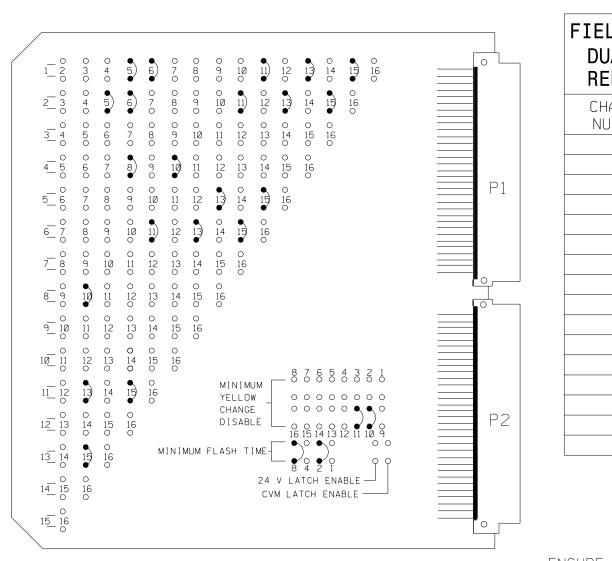
L5 |

Ø1 | Ø5 | NOT | Ø6 | Ø5 |

2B

M

CH2 CH2 CH2 CH2



MMU PROGRAMMING CARD

1 🖰

4 A

NOT

FIELD CHE DUAL IND RED FAIL	) ENABLE
CHANNEL NUMBER	ENABLE/ DISABLE
1	ENABLE
2	ENABLE
3	DISABLE
4	ENABLE
5	ENABLE
6	ENABLE
7	DISABLE
8	ENABLE
9	DISABLE
10	ENABLE
11	ENABLE
12	DISABLE
13	ENABLE
14	DISABLE

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	LLOW ARROW							
CONFIG MODE	В							
ENABLE CHANN	NEL PAIR, FYA							
CH 1-13	ON							
CH 3-14	OFF							
CH 5-15	ON							
CH 7-16	OFF							
RED/YEL INF	PUT ENABLE							
CH 1	ON							
CH 3	OFF							
CH 5	ON							
CH 7	OFF							
FLASH RATE FAULT	ON							
FYA TRAP DETECT	ON							

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

ENABLE

DISABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,7,9,12,14&16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in Phase 2 Green and 6 Walk.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. Program phases 4 and 8 for dual entry.
- 11. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. SIG-4.1 I-5506

				S	IGN	AL	HEA	4D F	100	K-U	P C	HAF	RT					
PHASE			2	3	4	Ç	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11	82	21,22	NU	41,42	<b>★</b> 51	42	61,62	NU	81,82	NU	P41, P42	P61, P62	NU	11	NU	<b>★</b> 51	NU
RED		*	2R		4R		*	6R		8R								
YELLOW			2Y		4 Y			6Y		8Y								
GREEN			2G		4G			6G		8G								
RED ARROW															13R		15R	
YELLOW ARROW		1Y					5Y								13Y		15Y	
FLASHING YELLOW ARROW															13G		15G	
GREEN ARROW	1G	1G				5G	5G											
₩												1ØR	11R					
Ķ												1ØG	11G					
NU = Not	Used																	

\* Denotes install load resistor. See Load Resistor Installation Detail.

OLA RED (13R) —

OLA YELLOW (13Y) —

OLA GREEN (13G) -

Ø1 GREEN (1G) —

★ See pictorial of head wiring detail this sheet.

#### **EQUIPMENT INFORMATION**

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....1,2,4,5,6,8,10,11,13,15 OLA....\* OLB.....NOT USED OLC....\*

\* See overlap programming detail on sheet 2

OLD.....NOT USED

## FYA SIGNAL WIRING DETAIL

(wire signal heads as shown) OLC RED (15R) -OLC YELLOW (15Y) — OLC GREEN (15G) -Ø5 GREEN (5G) -

#### LOAD SWITCH ASSIGNMENT DETAIL

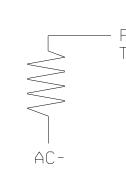
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	Ø 1
2	Ø 2
3	-
4	Ø 4
5	Ø 5
6	Ø 6
7	_
8	Ø 8
9	_
10	Ø4 PED
11	Ø6 PED
12	_
13	OLA
14	-
15	OLC
16	_
16	

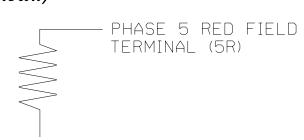
UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

## LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



PHASE 1 RED FIELD TERMINAL (1R)



ACCEPTABLE VALUES VALUE (ohms) | WATTAGE 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min)

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129T3 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 3 Temporary Design 3 - TMP Phase IV



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UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) Gateway Centre Boulevard/

Lake Crabtree Park Division 5 Wake County Cary

29449 12/5/2017

SIG. INVENTORY NO.

DATE

**DOCUMENT NOT CONSIDERED FINAL** 

PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY INIT. DATE REVISIONS

\* Detector Type - G

7B | L15A,L15B |

NU | L16A, L16B |

\*\* Detector Type - N

L9A TO L10A, AND L9B TO L10B

RACK

#1

RACK

# >

BIU

BIU

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE IN THE CHART BELOW SHOWN IN THE CHART BELOW LOOP NO. LOOP PANEL TERMINALS CONTROLLER DETECTOR NO. FUNCTION | L1A,L1B L1A TO L2A, AND L1B TO L2B 1 A 1 | Ø 1 | DELAY | 15 L2A,L2B \* 2 | Ø6 | DELAY | 3 1B L3A,L3B

CH2

USED

`|FEATURE|TIME(SEC) 3 | Ø1 | DELAY | 15 NU L4A,L4B NU L5A,L5B 2B L6A,L6B \*\* 6 | Ø 2 4A L7A,L7B | Ø 5 | DELAY | 15 NU L8A,L8B L9A,L9B 5 A | Ø 4 | DELAY | 15 \_\_\_\_\_\_ L10A,L10B Ø 2 DELAY 3 6A | L11A, L11B Ø 6 NU L12A,L12B NU L13A,L13B NU L14A,L14B 14

15 Ø 4

ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW IN THE CHART BELOW LOOP NO. LOOP PANEL TERMINALS 8A L17A,L17B

NU L31A,L31B

NU L32A,L32B

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

M

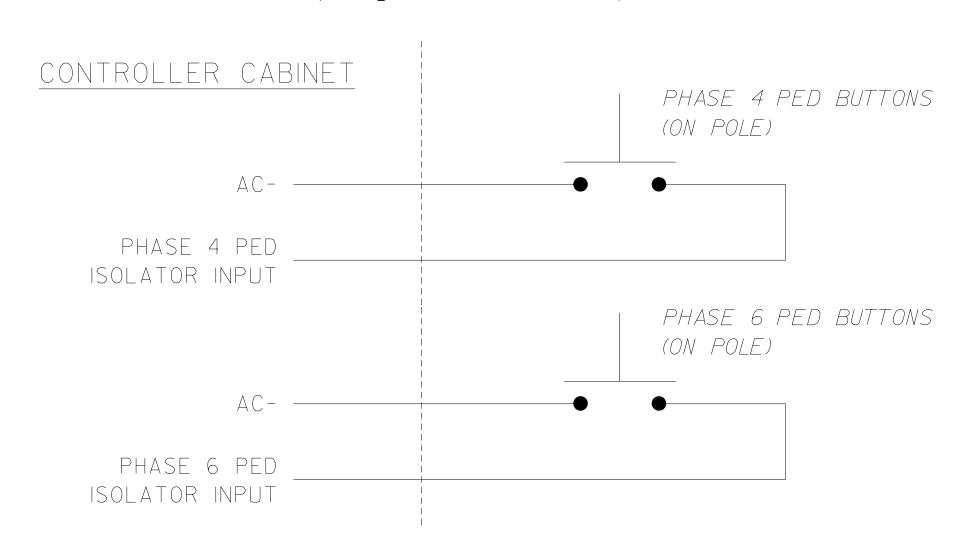
M

CONTROLLER DETECTOR NO. FUNCTION -FEATURE TIME(SEC) Ø 8 NU L18A,L18B NU L19A,L19B NU L20A,L20B 20 NU | L21A, L21B 21 NU | L22A, L22B NU L23A,L23B 23 NU L24A,L24B 24 NU L25A,L25B 25 NU | L26A, L26B NU L27A,L27B 27 NU | L28A, L28B NU | L29A, L29B 29 NU L30A,L30B

30

#### PEDESTRIAN PUSH BUTTON WIRING DETAIL

(wire push buttons as shown)



#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

#### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING <u>free run</u> — program changes (shown below) in a time based action plan, TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

TO RUN ALT, PHASING DURING <u>coordination</u> — select the time based action plan that is programmed to SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

PHASING	veh det plan	SF BITS ENABLED
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	1 2	NONE 1,5

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OR WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1 AND 5

Modifies overlap parent phases for heads 11 and 51 to run protected

turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call

on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

# ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN |

ACTION PL	AN.	[	99	]												
PATTERN				. 99		SYS	0 V	'ERR	IDE		. N	0				
TIMING PL	AN.			. 0		SEQ	UEN	ICE.			•	0				
VEH DETEC	TOR	PL.	AN.	. 2		DET	LC	G			NON	E				
FLASH			•			RED	RE	ST.			. N	0				
VEH DET D	IAG	PLI	Ν	. 0		PED	DE	T D	IAG	PL	Ν	0				
DIMMING EI	NABI	LE.	•	NO		PRI	OR I	ΤY	RET	JRN	. N	0				
PED PR RE	TURI	٧		NO		QUE	UE	DEL	ΑΥ.		. N	0				
PMT COND I	DEL,	ДΥ		NO												
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
PED RCL	•	6	•		•	•	•	•	•	•	•	•	•	•	•	
WALK 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
VEH RCL	6		•		•	•	•	•	•	•	•	•	•	•	•	
MAX RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
CS INH	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
OMIT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
SPC FCT	Χ	•	•	•	X	•	•	•	( 1	-8)						
AUX FCT	•	•	•	( 1	-3	)										
	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
LP 1-15	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 16-30	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 31-45	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 46-60	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 61-75	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 76-90	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 91-100	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

DESIGNED: NOV 2017 SEALED: 12-01-2017

REVISED: N/A

I-5506 SIG-4.2

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 1 /

Toggle Twice

#### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 5

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129T3



The PPLT FYA operation of Signal Head 11 (Overlap A) and Signal Head 51 (Overlap C) can be altered to fully protected operation.

Electrical Detail - Sheet 2 of 3 Temporary Design 3 - TMP Phase IV



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Lake Crabtree Park Wake County Division 5

PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

50 N.Greenfield Pkwy,Garner,NC 27529

SIG. INVENTORY NO.

PROJECT REFERENCE NO. SIG-4.3

I-5506

# ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERATE PHASING

#### LOOPS 1A, 5A (program controller as shown)

# 

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu selet 8. UTILITIES
- 2. From Utilities Submenu select | 1. COPY/CLEAR |
- 3. Copy from Detector Plan "1" to Detector Plan "2".

COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN.... > TIMING PLAN.... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - For Loop 1A, modify vehicle detectors.
  - Place cursor in VEH DETECTOR [ ] position and enter "1".
  - Set delay time to "0".

VEH DETECTOR [ 1] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR..... X ECPI LOG...... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O - ENSURE DELAY IS SET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO - Place cursor in VEH DETECTOR [ ] position and enter "2". - Set assigned phase to "0".

VEH DETECTOR [ 2] VEH DET PLAN [ 2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- For Loop 5A, modify vehicle detectors. - Place cursor in VEH DETECTOR [ ] position and enter "9".

- Set delay time to "0".

VEH DETECTOR [ 9] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR.... X ECPI LOG.... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

EXTEND TIME... O.O DELAY TIME... O.O + ENSURE DELAY IS SET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [ ] position and enter "10".
- Set assigned phase to "0".

VEH DETECTOR [10] VEH DET PLAN [2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE →
IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129T3 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 3 of 3 Temporary Design 3 - TMP Phase IV



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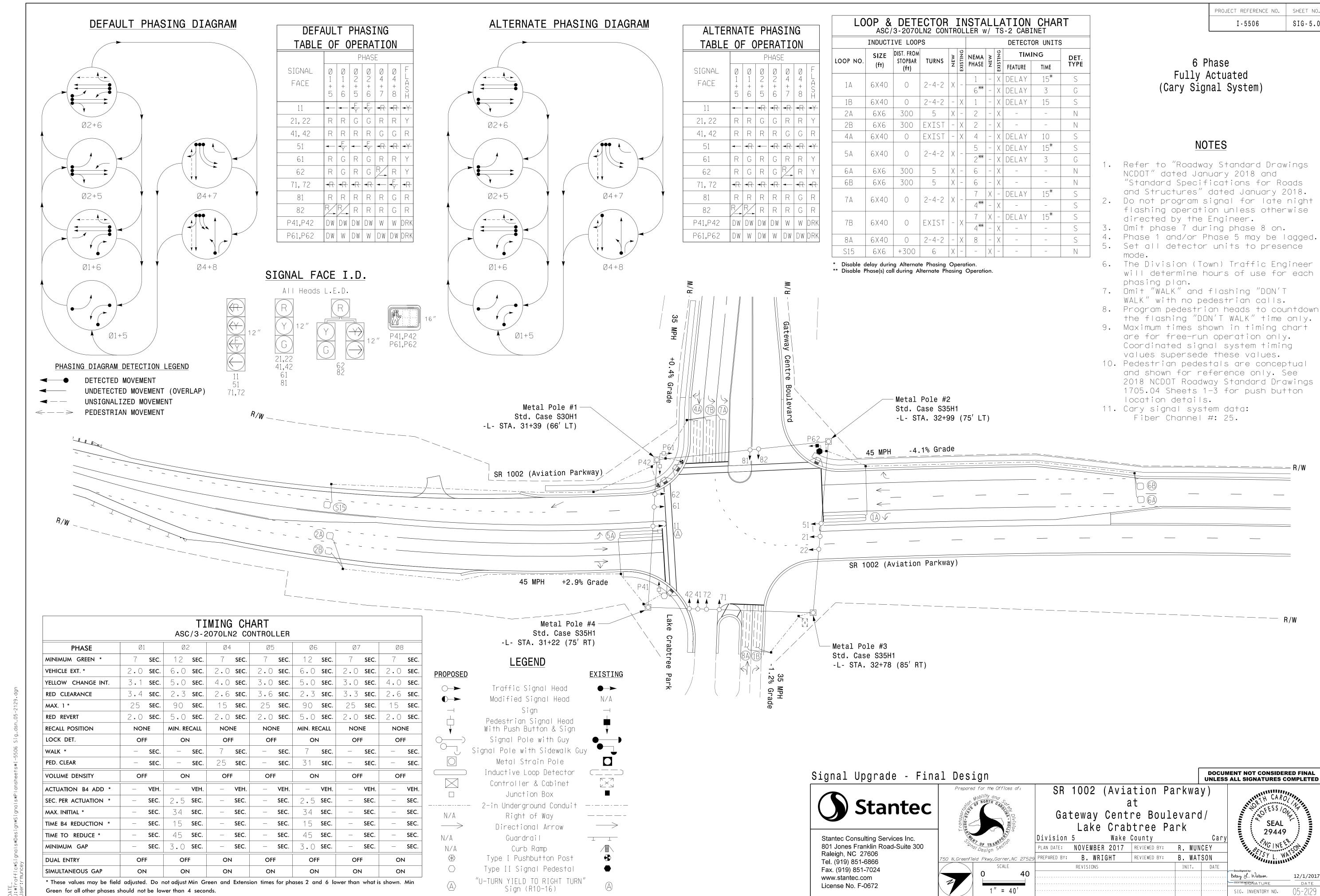
Wake County Division 5 Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN

PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

12/5/2017 sig. Inventory No. 05-2|29

29449

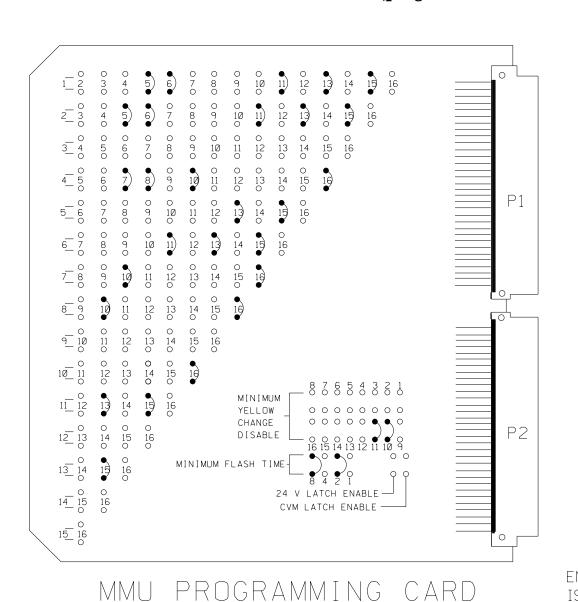
**DOCUMENT NOT CONSIDERED FINAL** 



SIG-5.0

the flashing "DON'T WALK" time only.

(program card and table



bles as shown	<i>i</i> )						
FIELD CHE DUAL IND RED FAIL	ENABLE						
CHANNEL NUMBER	ENABLE/ DISABLE						
1	ENABLE						
2	ENABLE						
3	DISABLE						
4	ENABLE						
5	DISABLE						
6	ENABLE						
7	ENABLE						
8	ENABLE						
9	DISABLE						
1Ø	ENABLE						
11	ENABLE						
12	DISABLE						
13	ENABLE						
1.4	nisari f						

UNIT O	PTIONS					
OPTION	SETTING					
RECURRENT PULSE	ON					
WALK DISABLE	OFF					
LOG CVM FAULTS	ON					
EXTERN WATCHDOG	OFF					
24V-2=12VDC	OFF					
PGM CARD MEMORY	ON					
LEDguard	ON					
FORCE TYPE 16	OFF					
TYPE12-SDLC	OFF					
VM 3x/Day Latch	ON					
·						

FLASHING YE	LLOW ARROW						
CONFIG MODE	В						
ENABLE CHANN	NEL PAIR, FYA						
CH 1-13	ON						
CH 3-14	OFF						
CH 5-15	ON						
CH 7-16	ON						
RED/YEL INF	PUT ENABLE						
CH 1	ON						
CH 3	OFF						
CH 5	ON						
CH 7	ON						
FLASH RATE FAULT	ON						
FYA TRAP DETECT	ON						

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

ENABLE

ENABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,9,12,&14 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 Green and 6 Walk.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. Program phases 4 and 8 for dual entry.
- 11. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-5.1

				S	IGN	AL	HEA	AD H	100	K-U	P C	HAF	RT					
PHASE	-	1	2	3	4	5	6	-/	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	<b>★</b>	82	21,22	NU	41,42	<b>★</b> 51	61,62	<b>★</b> 71,72	62	81,82	NU	P41, P42	P61, P62	NU	11	NU	<b>★</b> 51	<b>★</b> 71,72
RED		*	2R		4R	*	6R		*	8R								
YELLOW			2Y		4 Y	*	6Y			8Y								
GREEN			2G		4 G		6G			8G								
RED ARROW															13R		15R	16R
YELLOW ARROW		1 Y							7Y						13Y		15Y	16Y
FLASHING YELLOW ARROW															13G		15G	16G
GREEN ARROW	1G	1G				5G		7G	7G									
₩												1ØR	11R					
Ķ												1ØG	11G					
NU = Not	Used																	

- \* Denotes install load resistor. See Load Resistor
- Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1	CH1	CH1			
		L3	∟1	L7	L5	L11	L9	L15	L13	S	S	S
		Ø 1	Ø 1	Ø4	Ø2	Ø6	ø5	Ø7	Ø7			
RACK #1	BIU	1B	1 🖰	4A	2A	6A	5A	7B	7A	T	T	T
'' Ι	DIU	CH2	CH2	CH2	CH2	CH2	CH2	CH2	CH2	E	E	E
		L4	L2	L8	L6	L12	L10	L16	L14	M	M	M
		NOT USED	Ø6	NOT USED	Ø2	Ø6	Ø2	Ø 4	Ø4	T	T	T
			1A		2B	6B	5A	7B	7A	1		

CH1 CH1		
L19 L17 S S S S S S	SS	
RACK   SYS   Ø8   L   L   L   L   L   L   L   L   L		
SI5   8A   '   '   '   '   '   '   '	T	
#2 BIU CH2 CH2 E E E E E	EEE	
L20 L18 M M M M M M M M M M M M M M M M M M M	M M	
NOT NOT T T T T T T T T Y	T T Y	

ON LOOP PANEL AS SHOWN — ACCORDING TO THE SCHEDULE — ON LOOP PANEL AS SHOWN — ACCORDING TO THE SCHEDULE IN THE CHART BELOW SHOWN IN THE CHART BELOW

	IN THE (	CHART BELOW	l		SHOWN	IN THE	CHART B	ELOW
	LOOP NO.	LOOP PANEL TERMINALS			ROLLER	FUNCTION	TI	MING
ADD JUMPERS FROM:		L1A,L1B		DETE	CTOR NO.		FEATURE	TIME(SEC)
L1A TO L2A, AND	1 A	L2A,L2B			1	Ø 1	DELAY	15
L1B TO L2B				*	2	Ø6	DELAY	3
	1B	L3A,L3B			3	Ø 1	DELAY	15
	NU	L4A,L4B			_	Ψ 1	DELAT	13
	2 A	L5A,L5B			4			
	2B	L6A,L6B		**	5	Ø 2		
				**	6	Ø 2		
	4 A	L7A,L7B		<u> </u>	7	Ø 4	DELAY	10
	NU	L8A,L8B				<i>y</i> 4	DELAT	10
ADD JUMPERS FROM:		L9A,L9B		-	8	·		
L9A TO L10A, AND	5 A	L1OA,L1OB			9	Ø 5	DELAY	15
L9B TO L10B	C A			*	10	Ø 2	DELAY	3
	6A	L11A,L11B		**	1 1	Ø6		
	6B	L12A,L12B				· ·		
ADD JUMPERS FROM:		L13A,L13B		**		Ø6		
L13A TO L14A, AND	7 A	L14A,L14B			13	Ø 7	DELAY	15
L13B TO L14B					14	Ø 4		
ADD JUMPERS FROM:	7B	L15A,L15B			15	Ø 7	DELAY	15
L15B TO L16B		L16A,L16B					DELAT	13
	1				16	Ø 4		

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS IN THE CHART BELOW SHOWN IN THE CHART BELOW

LOOP NO.	LOOP PANEL TERMINALS	CONTROLLER	FUNCTION	TIMING				
8 A	L17A,L17B	DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)			
NU	L18A,L18B	· 17	Ø 8					
S15	L19A,L19B	. 18						
		<del>**</del> 19	SYSTEM					
NU	L20A, L20B	. 20						
NU	L21A,L21B	. 21						
NU	L22A,L22B	. 22						
NU	L23A,L23B	. 23						
NU	L24A,L24B	24						
NU	L25A,L25B	25						
NU	L26A,L26B	26						
NU	L27A,L27B	. 27						
NU	L28A,L28B	28						
NU	L29A,L29B		'					
NU	L30A,L30B	. 29	,					
NU	L31A,L31B	30	,					
NU	L32A,L32B	• 31						
		. 32						

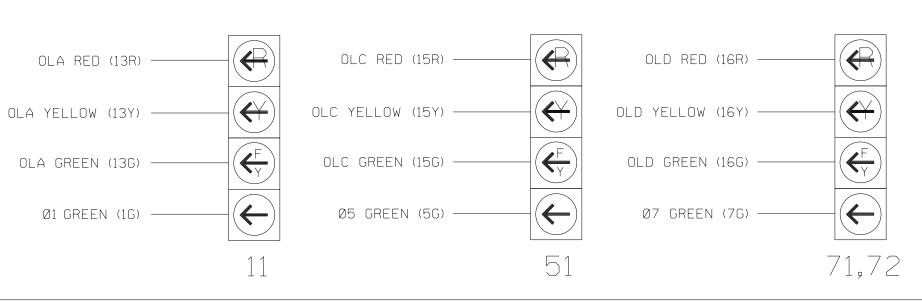
NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

#### **EQUIPMENT INFORMATION**

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....1,2,4,5,6,7,8,10,11,13,15,16 OLA.....\* OLB.....NOT USED OLC....\* OLD.....\*

# FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



#### LOAD SWITCH ASSIGNMENT DETAIL

\* See overlap programming detail on sheet 2

(program controller according to schedule in chart below)

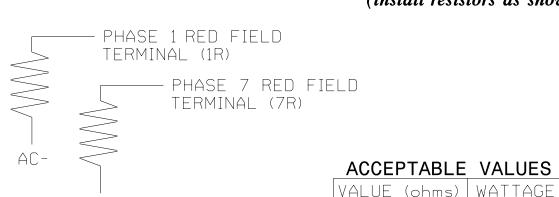
I DAD SWITCH

NUMBER	FUNCTION
1	Ø 1
2	Ø 2
3	_
4	Ø 4
5	Ø 5
6	Ø 6
7	Ø 7
8	Ø 8
9	-
10	Ø4 PED
11	Ø6 PED
12	-
13	OLA
14	-
15	OLC
16	OLD
	TITCH CHANNE

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



PHASE 5 RED FIELD TERMINAL (5R) - PHASE 5 YELLOW FIELD TERMINAL (5Y)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

|1.5K - 1.9K | 25W (min) 2.ØK - 3.ØK | 1ØW (min)

Electrical Detail - Sheet 1 of 4 Final Design



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# **UNLESS ALL SIGNATURES COMPLETED** SR 1002 (Aviation Parkway) Gateway Centre Boulevard/

Lake Crabtree Park Division 5 Wake County Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN

SIG. INVENTORY NO.

**DOCUMENT NOT CONSIDERED FINAL** 

PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

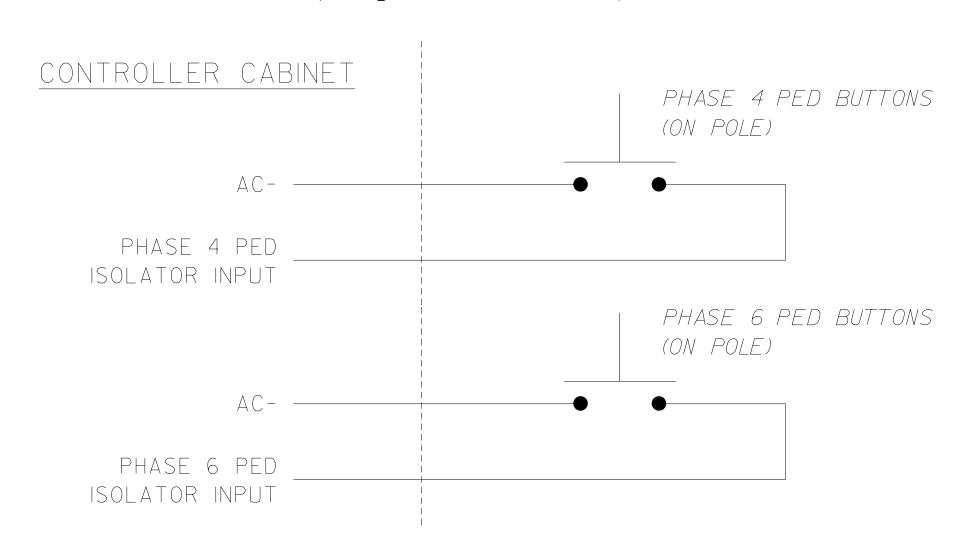
12/5/2017

05-212

\* Detector Type - G \*\* Detector Type - N

#### PEDESTRIAN PUSH BUTTON WIRING DETAIL

(wire push buttons as shown)



#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

#### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING <u>free run</u> — program changes (shown below) in a time based action plan, TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, 5, AND 7.

TO RUN ALT, PHASING DURING <u>coordination</u> — select the time based action plan that is programmed to SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, 5, AND 7.

Į į	PHASING	veh det plan	<u>sf bits enabled</u>
	ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	NONE
	ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1,5,7

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OR WHAT TAKES PLACE WHEN SF BITS 1, 5, AND 7 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1, 5, AND 7 Modifies overlap parent phases for heads 11, 51, 71, and 72 to run protected turns only.

VEH DET PLAN 2:

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Disables phase 4 call on loops 7A and 7B and reduces delay time for phase 7 call on loops 7Å and 7B to 0 seconds.

# ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN

ACTION PL	AN	. [ 99	) ]												
PATTERN			. 99		SYS	OV	ERR	IDE		. N	10				
TIMING PL	ΔΝ		. 0		SEQ	UEN	ICE.			•	0				
VEH DETECT	TOR	PLAN.	. 2		DET	LC	IG			NON	ΙE				
FLASH				-	RED	RE	ST.			. N	10				
VEH DET D	IAG	PLN	. 0		PED	DE	T D	IAG	PL	Ν	0				
DIMMING EN	NABL	Ε	NO		PRI	OR I	ΤY	RET	URN	. N	10				
PED PR RE	TURN		NO		QUE	UE	DEL	AY.		. N	10				
PMT COND [	DELA	Υ	NO												
PHASE	1	2 3	4	5	6	7	8	9	0	1	2	3	4	5	(
PED RCL	•		•	•	•	•	•	•	•	•	•		•	•	
WALK 2	•		•	•	•	•	•		•	•	•		•	•	
VEX 2	•			•	•	•	•		•	•	•		•	•	
VEH RCL	•		•	•	•	•	•	•	•	•	•	•	•	•	
MAX RCL	•		•	•	•	•	•		•	•	•		•	•	
MAX 2	•		•	•	•	•	•	•	•	•	•	•	•	•	
PHASE	1	2 3	4	5	6	7	8	9	0	1	2	3	4	5	
MAX 3	•		•	•	•	•	•	•	•	•	•	•	•	•	
CS INH	•		•	•	•	•	•	•	•	•	•	•	•	•	
OMIT	•		•	•	•	•	•			•	•	•	•	•	
SPC FCT	X		•	Χ	•	Χ	•	( 1	-8)						
AUX FCT	•		( 1	-3 2	)										
	1	2 3	4	5	6	7	8	9	0	1	2	3	4	5	
LP 1-15	•		•	•	•	•	•		•	•	•		•	•	
LP 16-30	•		•	•	•	•	•	•	•	•	•	•	•	•	
LP 31-45	•		•	•	•	•	•	•	•	•	•	•	•	•	
LP 46-60	•		•	•	•	•	•	•	•	•	•	•	•	•	
LP 61-75	•	6 6	•	•	•	•	•	•	•	•	•	•	•	•	
LP 76-90	•		•	•	•	•	•	•	•	•	•	•	•	•	
LP 91-100	•		•	•	•	•	•	•	•	•	•	•	•	•	

I-5506 SIG-5.2

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

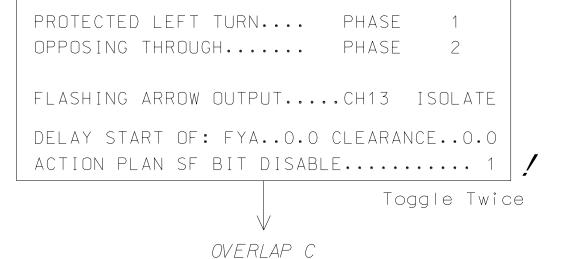
1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

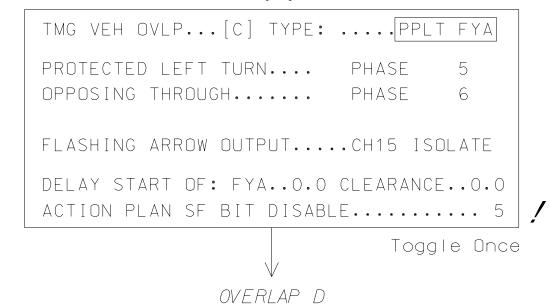
#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

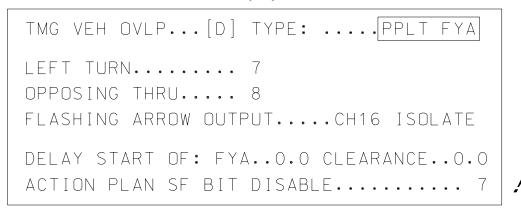
TMG VEH OVLP...[A] TYPE: .... PPLT FYA



Select TMG VEH OVLP [C] and 'PPLT FYA'



Select TMG VEH OVLP [D] and 'PPLT FYA'



END PROGRAMMING



The PPLT FYA operation of Signal Head 11 (Overlap A), Signal Head 51 (Overlap C) and Signal Heads 71 and 72 (Overlap D) can be altered to fully protected operation.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

REVISIONS

Electrical Detail - Sheet 2 of 4 Final Design



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#### **UNLESS ALL SIGNATURES COMPLETED** SR 1002 (Aviation Parkway) Gateway Centre Boulevard/ Lake Crabtree Park

Wake County Division 5 Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY

DOCUMENT NOT CONSIDERED FINAL

INIT. DATE SIG. INVENTORY NO.

PROJECT REFERENCE NO. I-5506 SIG-5.3

# ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERATE PHASING

LOOPS 1A, 5A, 7A, AND 7B

(program controller as shown)

# 

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu selet 8. UTILITIES
- 2. From UTILITIES Submenu select | 1. COPY/CLEAR |
- 3. Copy from Detector Plan "1" to Detector Plan "2".

COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN.... > TIMING PLAN.... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - For Loop 1A, modify vehicle detectors.
  - Place cursor in VEH DETECTOR [ ] position and enter "1".
  - Set delay time to "0".

← NOTICE VEH DET PLAN 2 VEH DETECTOR [ 1] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR.... X ECPI LOG.... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O FISSET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY, NO

- Place cursor in VEH DETECTOR [ ] position and enter "2". - Set assigned phase to "0".

VEH DETECTOR [ 2] VEH DET PLAN [ 2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO 'O' EXTEND TIME... O.O DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- For Loop 5A, modify vehicle detectors.

- Place cursor in VEH DETECTOR [ ] position and enter "9". - Set delay time to "0".

> VEH DETECTOR [ 9] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

EXTEND TIME... O.O DELAY TIME... O.O + ENSURE DELAY TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [ ] position and enter "10". - Set assigned phase to "0".

VEH DETECTOR [10] VEH DET PLAN [2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE -EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- For Loop 7A, modify vehicle detectors.

- Place cursor in VEH DETECTOR [ ] position and enter "13".

- Set delay time to "0".

VEH DETECTOR [13] VEH DET PLAN [2] TYPE: S-STANDARD TS2 DETECTOR.... X ECPI LOG.... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O ← ENSURE DELAY USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [ ] position and enter "14". - Set assigned phase to "0".

MOTICE VEH DET PLAN 2 VEH DETECTOR [14] VEH DET PLAN [2] TYPE: S-STANDARD TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE -EXTEND TIME... O.O DELAY TIME... O.O USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- For Loop 7B, modify vehicle detectors. - Place cursor in VEH DETECTOR [ ] position and enter "15".
- Set delay time to "0".

PMT QUEUE DELAY. NO

VEH DETECTOR [15] VEH DET PLAN [2] TYPE: S-STANDARD TS2 DETECTOR..... X ECPI LOG..... NO DFT PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... 0.0 DELAY TIME... 0.0 \ ENSURE DELAY TO '0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC .

- Place cursor in VEH DETECTOR [ ] position and enter "16". - Set assigned phase to "0".

MOTICE VEH DET PLAN 2 VEH DETECTOR [16] VEH DET PLAN [2] TYPE: S-STANDARD TS2 DFTFCTOR.... X FCPI LOG..... NO DFT PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE EXTEND TIME... O.O DELAY TIME... O.O USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-2129 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 3 of 4 Final Design

IS SET TO 'O



← NOTICE VEH DET PLAN 2

← NOTICE VEH DET PLAN 2

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UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) Gateway Centre Boulevard/

Lake Crabtree Park Wake County Division 5 Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY

REVISIONS INIT. DATE

29449 12/5/2017

SIG. INVENTORY NO.

**DOCUMENT NOT CONSIDERED FINAL** 

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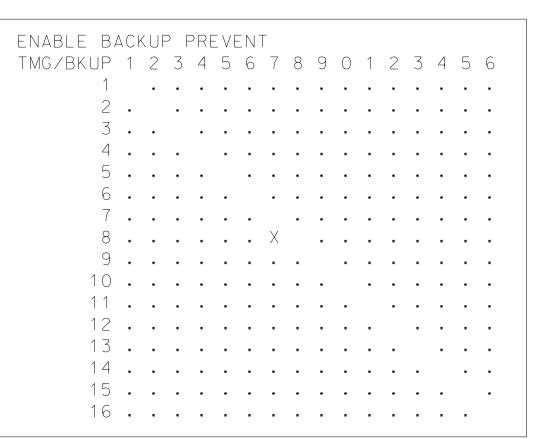
PROJECT REFERENCE NO. SHEET NO. SIG-5.4

# ECONOLITE ASC/3-2070 BACKUP PROTECTION ENABLE PROGRAMMING

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 1. CONTROLLER SEQ
- 3. From CONTROLLER SEQUENCE Submenu select | 3. BACKUP PREVENT PHASES

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move cursor to the appropriate field and press 'YES/NO' on the controller keypad to toggle field value between 'X', 'B', 'C' and 'OFF'.



END PROGRAMMING

#### NOT

1. 'B' without a 'C' programmed for the 'TIMING' (row) phase inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active, or next, until the controller goes through Red Revert and Red Clear. Make sure the proper Red Revert and Red Clear times shown on the Signal Design plan are programmed in the controller phase timing.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-2129
DESIGNED: NOV 2017
SEALED: 12-01-2017
REVISED: N/A

Electrical Detail - Sheet 4 of 4 Final Design



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SR 1002 (Aviation Parkway) at Gateway Centre Boulevard/ Lake Crabtree Park

Lake Crabtree Park

Division 5 Wake County

PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN

PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY

REVISIONS INIT. DATE

50 N.Greenfield Pkwy, Garner, NC 27529

Wake County

VEMBER 2017 REVIEWED BY: L. OVERN

G. SPELL REVIEWED BY: R. MUNCEY

VISIONS

INIT. DATE

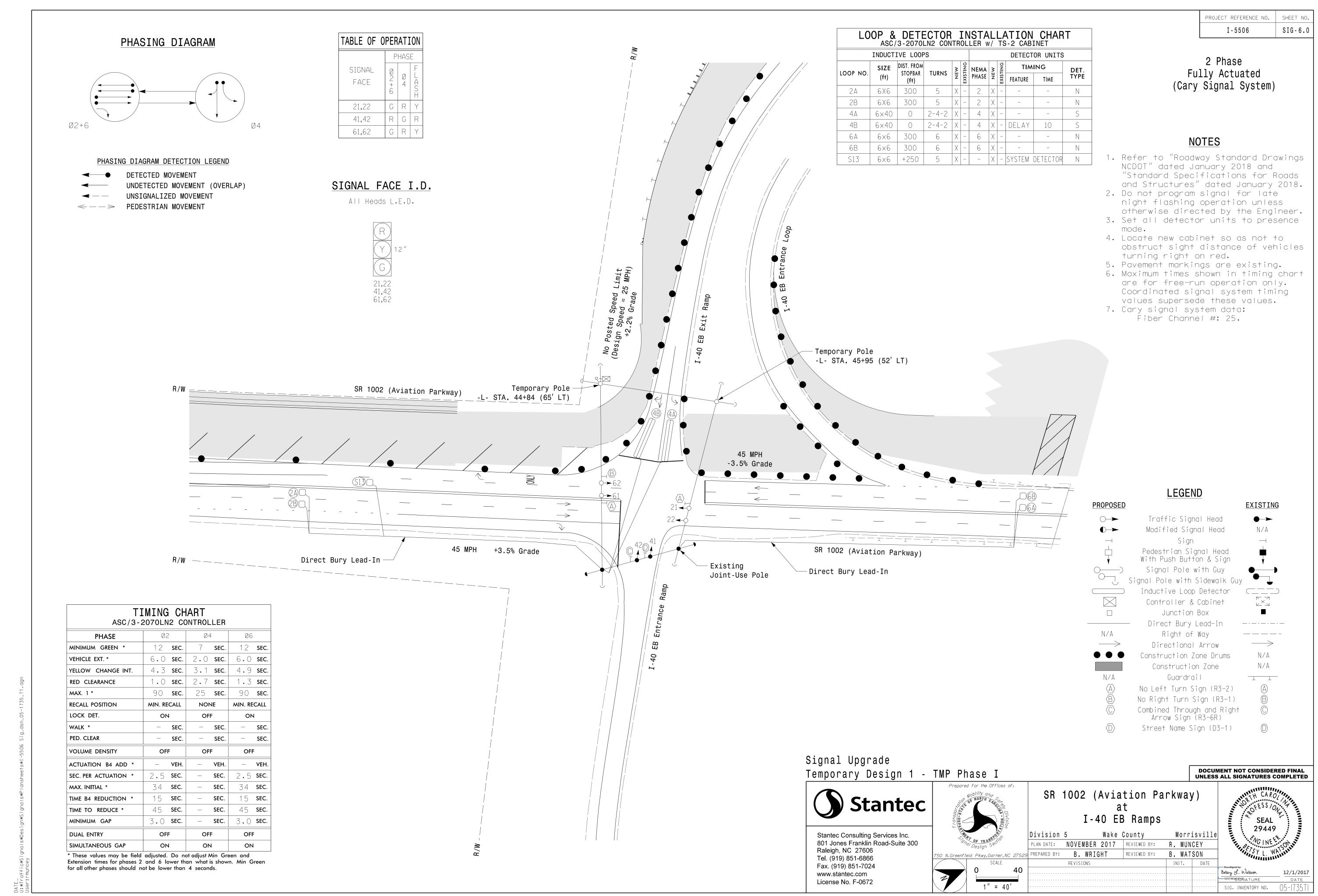
Docusigned by: Bray J. Watson 12/5/2017

SIGNATURE DATE

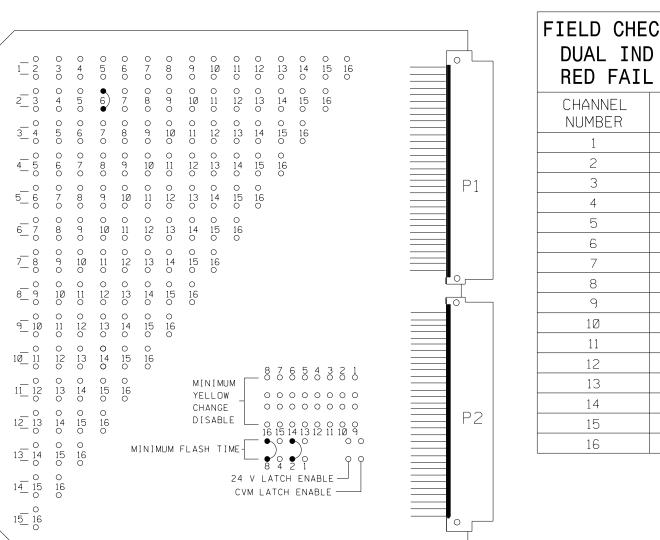
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO.

u:\*Traffic\*Signals\*Design\*Signals\*Electrical Details\*I-5506 sm\_ele. User:rmuncey



(program card and tables as shown)



MMU PROGRAMMING CARD

FIELD CHE DUAL IND RED FAIL	) ENABLE						
CHANNEL NUMBER	ENABLE/ DISABLE						
1	DISABLE						
2	ENABLE						
3	DISABLE						
4	ENABLE						
5	DISABLE						
6	ENABLE						
7	DISABLE						
8	DISABLE						
9	DISABLE						
10	DISABLE						
11	DISABLE						
12	DISABLE						
13	DISABLE						
14	DISABLE						
15	DISABLE						

DISABLE

UNIT O	PTIONS					
OPTION	SETTING					
RECURRENT PULSE	ON					
WALK DISABLE	OFF					
LOG CVM FAULTS	ON					
EXTERN WATCHDOG	OFF					
24V-2=12VDC	OFF					
PGM CARD MEMORY	ON					
LEDguard	ON					
FORCE TYPE 16	OFF					
TYPE12-SDLC	OFF					
VM 3x/Day Latch	ON					

FLASHING YE	ELLOW ARROW					
CONFIG MODE	В					
ENABLE CHANN	NEL PAIR, FYA					
CH 1-13	OFF					
CH 3-14	OFF					
CH 5-15	OFF					
CH 7-16	OFF					
RED/YEL INF	PUT ENABLE					
CH 1	OFF					
CH 3	OFF					
CH 5	OFF					
CH 7	OFF					
FLASH RATE FAULT	OFF					
FYA TRAP DETECT	OFF					

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs
  - 1,3,5,7,8,9,10,11,12,13,14,15 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-6.1

	SIGNAL HEAD HOOK-UP CHART															
PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	41,42	NU	61,62	NU	NU	NU	NU	NU	NU	NU	NU	NU	NU
RED		2R		4R		6R										
YELLOW		2Y		4 Y		6Y										
GREEN		2G		4G		6G										
RED ARROW																
YELLOW ARROW																
FLASHING YELLOW ARROW																
GREEN ARROW																
*																
*																

NU = NOT USED

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1		CH1							
		L3	L1	S	L5	S	S	S	S	S	S	S
		Ø 4	Ø2		Ø6							
RACK #1	$\Box$ T L L	4 🛆	2A	T	6A	T	Ť	Ť	T	T	T	T
# I	BIU	CH2	CH2	E	CH2	E	E	E	E	E	E	E
		L4	L2	M	L6	M P	M P	M P	M	M	M P	M
		Ø 4	Ø2	, T	Ø6	<del>'</del> T	T	T	T	<u> </u>	T	+
		4B	2B	Y	6B	Y	Y	Y	Y	Y	Y	Y

RACK		S L O T	CH1 L17 SYS DET S13	SLOT	S L O T	S L O T	S L O T	S L O T	SLOT	S L O T	SLOT	S L O F	
#2	RIU	E M P T Y	CH2 L18 NOT USED	E M P T Y									

V	LOOP PA	ANEL AS SHOWN
]	IN THE C	CHART BELOW
	LOOP NO.	LOOP PANEL TERMINALS
	2 A	L1A,L1B
	2B	L2A,L2B
	4 A	L3A,L3B
	4B	L4A,L4B
	6 A	L5A,L5B
	6B	L6A,L6B
	NU	L7A,L7B
	NU	L8A,L8B
	NU	L9A,L9B
	NU	L10A,L10B
	NU	L11A,L11B
	NU	L12A,L12B
	NU	L13A,L13B
	NU	L14A,L14B
	NU	L15A,L15B
	NU	L16A,L16B

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

CONTROLLER CONTROLLER DETECTOR NO. FUNCTION FEATURE TIME(SEC) Ø 2 Ø 4 | Ø4 | DELAY | 10 Ø 6 <del>\*\*</del> 6 Ø 6 10 11 12 13 14 15

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW

LOOP NO. LOOP PANEL TERMINALS S13 L17A,L17B NU L18A,L18B NU L19A,L19B NU L20A,L20B NU L21A,L21B NU L22A,L22B NU L23A,L23B NU L24A,L24B NU L25A,L25B NU L26A,L26B NU L27A,L27B NU L28A,L28B NU L29A,L29B NU L30A,L30B NU L31A,L31B NU L32A,L32B PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

CONTROLLER	FUNCTION	TIMING						
DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)					
<del>**</del> 17	SYSTEM							
· 18								
. 19								
. 20								
· 21								
· 22								
· 23								
. 24								
. 25								
. 26								
. 27								
. 28								
. 29								
. 30								
· 31								
· 32								

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

## EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....2,4,6 PHASES USED.....2,4,6 OLA.....NOT USED OLB.....NOT USED OLC.....NOT USED OLD.....NOT USED

#### LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION					
1	-					
2	Ø 2					
3	-					
4	Ø 4					
5	-					
6	Ø 6					
7	-					
8	-					
9	-					
10	-					
11	_					
12	_					
13	-					
14	-					
15	_					
16	-					

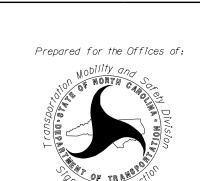
UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail Temporary Design 1 - TMP Phase I



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50 N.Greenfield Pkwy,Garner,NC 27529

# SR 1002 (Aviation Parkway) I-40 EB Ramps

Division 5 Wake County Morrisville NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

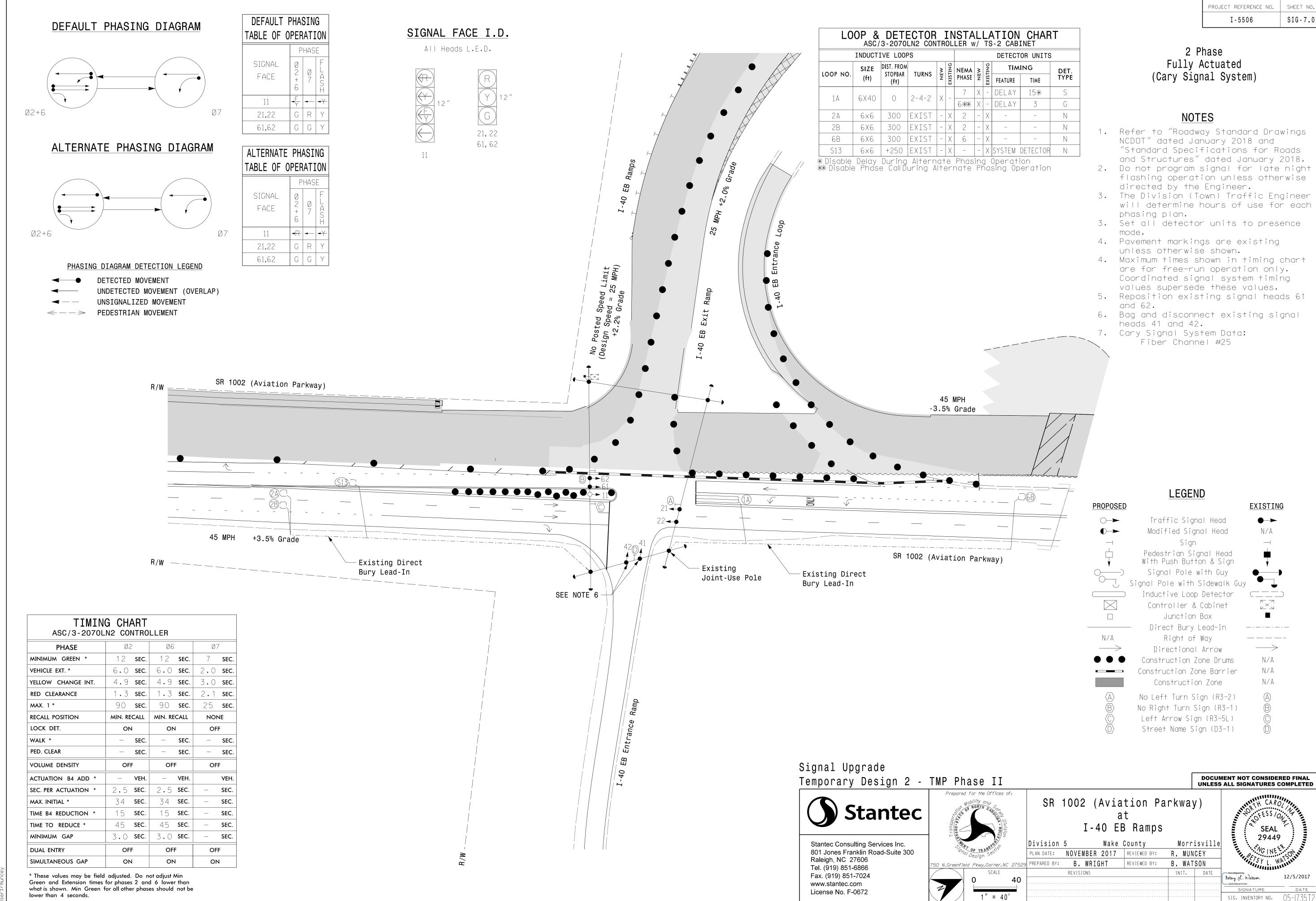
29449

**DOCUMENT NOT CONSIDERED FINAL** 

UNLESS ALL SIGNATURES COMPLETED

12/5/2017 SIG. INVENTORY NO.

\*\* Detector Type - N



/AIE\_ |:\*Traffic\*Signals\*Design\*Sign |ser:rm:nne.

(program card and tables as shown)

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW.

PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

NOT

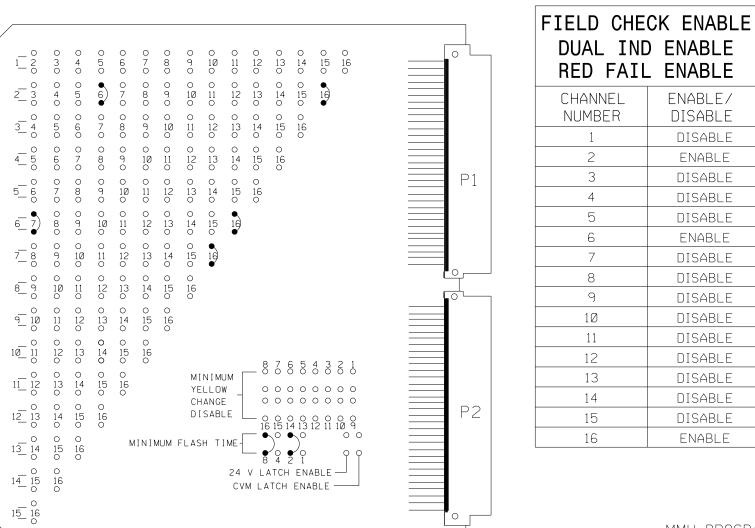
L6

Ø6

6B

E M

M



Ø2

24

CH2

2B

S13

CH2

L18

USED

14

15

\* Detector Type - G

\*\* Detector Type - N

CH2

Ø6

1A

MMU PROGRAMMING CARD

UNIT O	PTIONS					
OPTION	SETTING					
RECURRENT PULSE	ON					
WALK DISABLE	OFF					
LOG CVM FAULTS	ON					
EXTERN WATCHDOG	OFF					
24V-2=12VDC	OFF					
PGM CARD MEMORY	ON					
LEDguard	ON					
FORCE TYPE 16	OFF					
TYPE12-SDLC	OFF					
VM 3x/Day Latch	ON					

FLASHING YE	LLOW ARROW					
CONFIG MODE	В					
ENABLE CHANN	NEL PAIR, FYA					
CH 1-13	OFF					
CH 3-14	OFF					
CH 5-15	OFF					
CH 7-16	ON					
RED/YEL INF	PUT ENABLE					
CH 1	OFF					
CH 3	OFF					
CH 5	OFF					
CH 7	ON					
FLASH RATE FAULT	ON					
FYA TRAP DETECT	ON					

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 1,3,4,5,8,9,10,11,12,13,14,& 15 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-7.1

SIGNAL HEAD HOOK-UP CHART															
1	2	3	4	5	OLE	OLG	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
NU	21,22	NU	NU	NU	61,62	11	NU	NU	NU	NU	NU	NU	NU	NU	11
	2R				6R	*									
	2Y				6Y	*									
	2G				6G										
															16R
															16Y
															16G
						7G									
	NU	NU 21,22 2R 2Y	1 2 3  NU 21,22 NU  2R  2Y  2G  4  4  5  6  7  7  7  7  7  7  7  7  7  7  7  7	1 2 3 4  NU 21,22 NU NU  2R  2Y  2G  4  4  4  4  4  4  4  4  4  4  4  4  4	1 2 3 4 5  NU 21,22 NU NU NU  2R 2Y	1 2 3 4 5 OLE  NU 21,22 NU NU NU 61,62  2R 6R  2Y 6Y  2G 6G  1 1 7 6G  1 1 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	1 2 3 4 5 OLE OLG  NU 21,22 NU NU NU 61,62 11  2R 6R **  2Y 6Y **  2G 6G 6G  1 76  1 76	1 2 3 4 5 OLE OLG 8  NU 21,22 NU NU NU 61,62 11 NU  2R 6R *  2Y 6Y 6G 6G  2G 7G 7G  1 7G	1 2 3 4 5 OLE OLG 8 PED  NU 21,22 NU NU NU 61,62 11 NU NU  2R 6R * NU NU 6Y * NU NU 6Y NU 6Y NU NU NU NU 6Y NU	1       2       3       4       5       OLE       OLG       8 $\frac{2}{PED}$ $\frac{4}{PED}$ NU       21,22       NU       NU       NU       61,62 $11^{\frac{1}{2}}$ NU       NU       NU         2R        6R       **             2Y        6G              2G         6G	1       2       3       4       5       OLE       OLG       8 $\frac{2}{PED}$ $\frac{4}{PED}$ $\frac{6}{PED}$ NU       21,22       NU       NU       NU       61,62 $11^{\frac{1}{2}}$ NU       NU       NU       NU         2R         6R       **             2Y         6G       **              2G         6G	1 2 3 4 5 OLE OLG 8 PED	1       2       3       4       5       OLE       OLG       8       PED       4 PED       PED       PED       OLA         NU       21,22       NU       NU       NU       61,62       11*       NU       NU <td>1 2 3 4 5 OLE OLG 8 PED PED PED PED OLA OLB  NU 21,22 NU NU NU NU 61,62 11 NU NU</td> <td>1         2         3         4         5         OLE         OLG         8         <math>\frac{2}{PED}</math> <math>\frac{4}{PED}</math> <math>\frac{8}{PED}</math>         OLA         OLA         OLB         OLC           NU         21,22         NU         NU         NU         61,62         <math>11^{\frac{1}{2}}</math>         NU         <t< td=""></t<></td>	1 2 3 4 5 OLE OLG 8 PED PED PED PED OLA OLB  NU 21,22 NU NU NU NU 61,62 11 NU	1         2         3         4         5         OLE         OLG         8 $\frac{2}{PED}$ $\frac{4}{PED}$ $\frac{8}{PED}$ OLA         OLA         OLB         OLC           NU         21,22         NU         NU         NU         61,62 $11^{\frac{1}{2}}$ NU         NU <t< td=""></t<>

- \* Denotes install load resistor. See Load Resistor
- Installation Detail on this sheet. ★ See pictorial of head wiring detail this sheet.

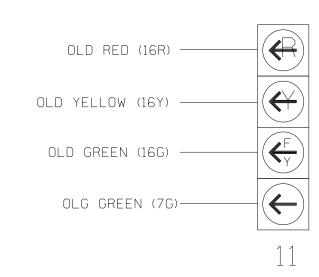
#### EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....2,6,7,16 OLA.....NOT USED OLB....NOT USED OLC.....NOT USED OLD.....\*

\* See overlap programming detail on sheet 2

#### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



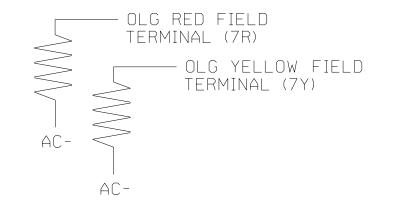
#### ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

#### LD SWITCH ASSIGN PHASE DIMMING ---FLASH---/OVLP TYPE R Y G D PWR AUT TGR 1 V ... + A R X V ... + A R X V ... + A R . . . . + A . . . . . – A P . . . - A . . 0 ... + A R X 14 2 0 ... + A R X 15 3 O . . . - A R . 16 4 0 ... - A Y .

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (mın)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T2 DESIGNED: NOV 2017 SEALED: 12-05-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 3 Temporary Design 2 - TMP Phase II

> SR 1002 (Aviation Parkway) Prepared for the Offices of: at

I-40 EB Ramps Wake County

29449 12/5/2017

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

Morrisville INIT. DATE SIG. INVENTORY NO.

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW LOOP NO. LOOP PANEL TERMINALS 2A L1A,L1B 2B L2A,L2B NU L3A,L3B NU L4A,L4B NU L5A,L5B 6B L6A,L6B L7A TO L8A, AND L7B TO L8B NU L9A,L9B NU L10A,L10B NU L11A,L11B NU L12A,L12B NU L13A,L13B NU L14A,L14B NU L15A,L15B NU L16A,L16B

RACK

#1

RACK

# 2

BIU

BIU

L7A,L7B

L8A,L8B

#### PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE

ACCORD	ING TO	THE SCH	EDULE				
SHOWN	IN THE	CHART E	BELOW				
CONTROLLER	FUNCTION	TIMING					
DETECTOR NO	·	FEATURE	TIME(SEC)				
** 1	Ø 2						
** 2	Ø 2						
· 3							
. 4							
· 5							
<del>**</del> 6	Ø 6						
· 7	Ø 7	DELAY	15				
* 8	Ø 6	DELAY	3				
. 9							
. 10							
· 11							
. 12							
. 13							

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN CONTROLLER DETECTOR NO. FUNCTION FEATURE TIME(SEC)

E M P

UIN	LUUF F	ANEL AS SHUWN
	IN THE (	CHART BELOW
	LOOP NO.	LOOP PANEL TERMINALS
	S13	L17A,L17B
	NU	L18A,L18B
	NU	L19A,L19B
	NU	L20A,L20B
	NU	L21A,L21B
	NU	L22A,L22B
	NU	L23A,L23B
	NU	L24A,L24B
	NU	L25A,L25B
	NU	L26A,L26B
	NU	L27A,L27B
	NU	L28A,L28B
	NU	L29A,L29B
	NU	L30A,L30B
	NILL	Ι 31Δ.Ι 31Β

NU L32A,L32B

18 19 20 21 22 23 24 25 26 27 28 29 30 31

PROGRAM CONTROLLER DETECTORS

ACCORDING TO THE SCHEDULE

SHOWN IN THE CHART BELOW

SYSTEM

<del>\*\*</del> 17

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

#### (program controller as shown)

To assign load switches 6 and 7 as OLE and OLG, program LD SWITCH 7 as OVLP '7' TYPE '0' and LD SWITCH 6 as OVLP '5' TYPE '0' as shown below.

OLE.....6+7 OLG.....\*

1. From Main Menu select | 1. CONFIGURATION |

NOTICE LD SWITCHES 6 & 7 🖚 ASSIGNED TO TYPE 'O'

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Division 5 PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS

# ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN

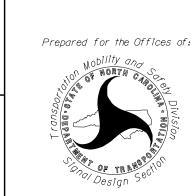
ACTION PLAN...[ 99] PATTERN......99 SYS OVERRIDE.... NO TIMING PLAN..... O SEQUENCE.... O VEH DETECTOR PLAN.. 2 DET LOG.....NONE FLASH..... -- RED REST..... NO VEH DET DIAG PLN... O PED DET DIAG PLN... DIMMING ENABLE.. NO PRIORITY RETURN. NO PED PR RETURN.. NO QUEUE DELAY.... NO PMT COND DELAY NO PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 SPC FCT . . . . . X . (1-8) AUX FCT . . (1-3) 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T2 DESIGNED: NOV 2017 SEALED: 12-05-2017 REVISED: N/A

Electrical Detail - Sheet 2 of 3 Temporary Design 2 - TMP Phase II



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# SR 1002 (Aviation Parkway) I-40 EB Ramps

Division 5 Wake County PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

(program controller as shown) 1. From Main Menu select 2. CONTROLLER 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS 3. Toggle Three Times. OVERLAP D Select TMG VEH OVLP [D] and 'PPLT FYA' TMG VEH OVLP...[D] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... OVERLAP G OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH16 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 7 Toggle Once OVERLAP E Select TMG VEH OVLP [E] and 'NORMAL' TMG VEH OVLP...[E] TYPE: ......NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 Toggle Twice MOVE CURSOR OVER [F]
TOGGLE TO TMG VEH OVLP [G] TMG VEH OVLP...[G] TYPE: ......NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0 END PROGRAMMING The PPLT FYA operation of Signal Head 11 (Overlap D) can be altered to fully protected operation. ALTERNATE PHASING ACTIVATION DETAIL TO RUN ALT, PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN, SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 7. TO RUN ALT, PHASING DURING <u>COORDINATION</u> - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 7. PHASING VEH DET PLAN SF BITS ENABLED ACTIONS REQUIRED TO RUN DEFAULT PHASING NONE ACTIONS REQUIRED TO RUN ALTERNATE PHASING ALTERNATE PHASING CHANGE SUMMARY THE FOLLOWING IS A SUMMARY OR WHAT TAKES PLACE WHEN SF BIT 7 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BIT 7

VEH DET PLAN 2:

Modifies overlap parent phases for head 11 to run protected turns only.

Disables phase 6 call on loop 1A and

reduces delay time for phase 7 call

on loop 1A to 0 seconds.

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

801 Jones Franklin Road-Suite 300

50 N.Greenfield Pkwy,Garner,NC 27529

SIG. INVENTORY NO. 05-1735

I-5506 SIG-7.3

# ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERATE PHASING L00P 1A

(program controller as shown)

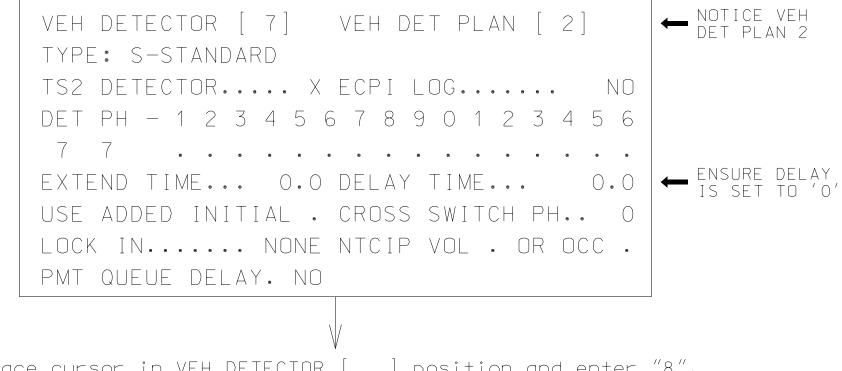
# 

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu selet 8. UTILITIES
- 2. From UTILITIES Submenu select 1. COPY/CLEAR
- 3. Copy from Detector Plan "1" to Detector Plan "2".

```
COPY / CLEAR UTILITY
                        TO
      FROM
PHASE TIMING.... > PHASE TIMING....
TIMING PLAN..... > TIMING PLAN.....
PH DET OPT PLAN. . > PH DET OPT PLAN. .
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
  TOGGLE TO SELECT A "FROM" AND A "TO"
           THEN PRESS ENTER
```

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP |
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - For Loop 1A, modify vehicle detectors.
  - Place cursor in VEH DETECTOR [ ] position and enter "7".
  - Set delay time to "0".



- Place cursor in VEH DETECTOR [ ] position and enter "8". - Set assigned phase to "0".

VEH DETECTOR [ 8] VEH DET PLAN [ 2] TYPE: G-GREEN TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY, NO

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T2 DESIGNED: NOV 2017 SEALED: 12-05-2017 REVISED: N/A

Electrical Detail - Sheet 3 of 3 Temporary Design 2 - TMP Phase II



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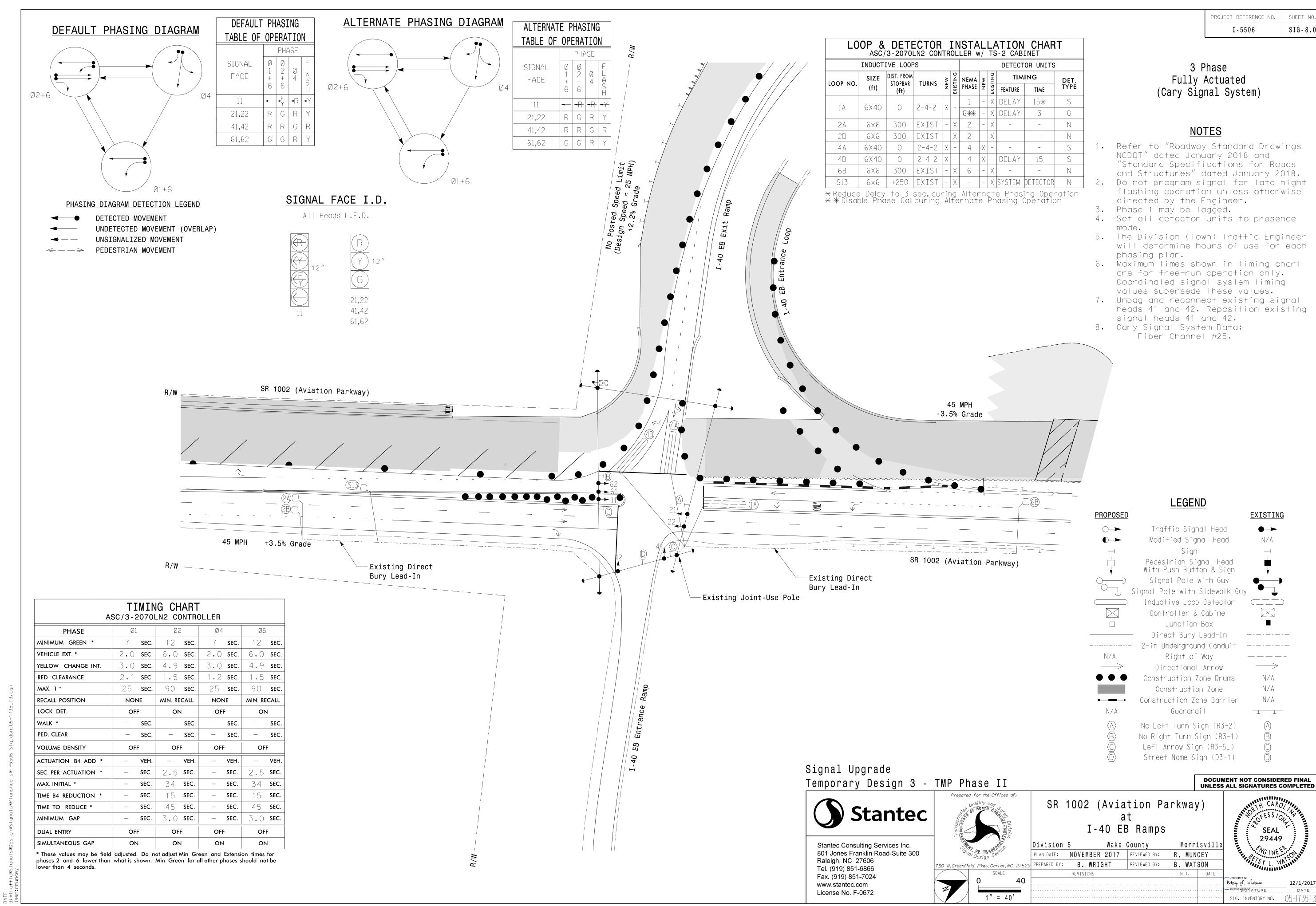
# SR 1002 (Aviation Parkway) I-40 EB Ramps

Wake County Division 5 PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN

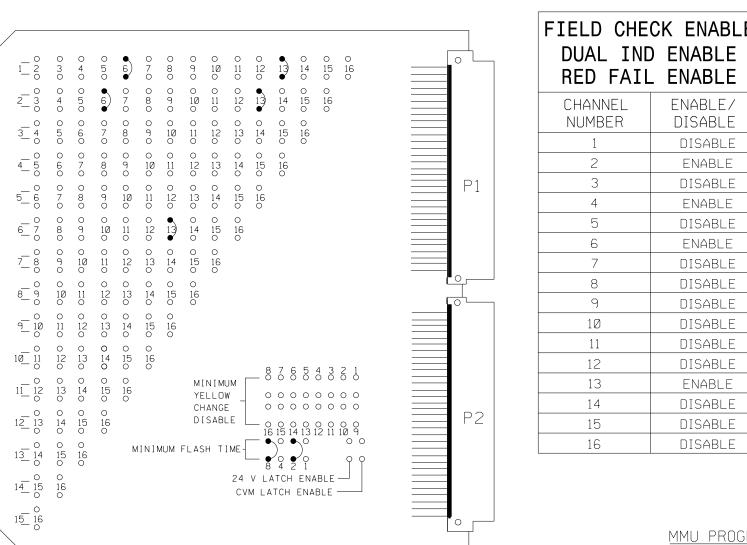
PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 29449 12/5/2017

SIG. INVENTORY NO. 05-1735



(program card and tables as show



MMU PROGRAMMING CARD

J	ONTI		
I.		OPTION	SETTING
w	_ a)	RECURRENT PULSI	E ON
ωι	ι)	WALK DISABLE	OFF
		LOG CVM FAULTS	6 ON
ΗE	CK ENABLE	EXTERN WATCHDOO	OFF
·ИГ	) ENABLE	24V-2=12VDC	OFF
	ENABLE	PGM CARD MEMOR	Y ON
/ T L		LEDguard	ON
	ENABLE/	FORCE TYPE 16	6 OFF
	DISABLE	TYPE12-SDLC	OFF
	DISABLE	VM 3x/Day Latel	n ON
	ENABLE		
	DISABLE		
	ENABLE	FLASHING Y	ELLOW ARRO
	DISABLE	CONFIG MODE	В

PROGRAM CONTROLLER DETECTORS

ACCORDING TO THE SCHEDULE

SHOWN IN THE CHART BELOW

SYSTEM

DETECTOR NO.

23

24

25

26 27

28 29

30

31

<del>\*\*</del> 17

TIMING

FUNCTION FEATURE TIME(SEC)

UNIT OPTIONS

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,5,7,8,9,10,11,12,14,15 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-8.1

#### SIGNAL HEAD HOOK-UP CHART 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 2 | 4 | 6 | 8 | OLA OLB OLC OLD 11 ★ 21,22 NU 41,42 NU 61,62 NU NU NU NU NU NU NU 4R \* 2R 6R RED 2 Y 4 Y 6Y YELLOW 2G 4 G GREEN RED 13R ARROW YELLOW 13Y ARROW FLASHING 13G YELLOW ARROW GREEN ARROW

NU = Not Used

\* Denotes install load resistor. See Load Resistor

Installation Detail.

★ See pictorial of head wiring detail this sheet.

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1								
		L3	L1	L7	L5	S	S	S	S	S	S	S	
		Ø 4	Ø2	Ø 1	NOT USED	L	L		L		L		
RACK #1	DIII	4 \right	2A	1 A	USED	T	T	T	T		T		
'' I	BIU	CH2	CH2	CH2	CH2	E	E	E	E	E	E	E	
		L4	L2	L8	L6	M	M	M	M	M	M	M	
		Ø 4	Ø2	Ø6	Ø6	<u> </u>	T	<u>'</u> T	T	<u> </u>	T	T	
		4B	2B	1A	6B	Y	Y	Y	Y	Y	Y	Y	

RACK		S _ O _	CH1 L17 SYS DET S13	S L O T	SLOT	S L O T	SLOT	S L O T	SLOT	SLOF	SLOT	SLOF	
#2	BIU	E M P T Y	CH2 L18 NOT USED	E M P T Y	E M P T	E M P T Y							

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN

011	2001 17	
]	IN THE C	CHART BELOW
	LOOP NO.	LOOP PANEL TERMINALS
	2 A	L1A,L1B
	2B	L2A,L2B
	4 A	L3A,L3B
	4B	L4A,L4B
	NU	L5A,L5B
	6B	L6A,L6B
JUMPERS FROM: 7A TO L8A, AND	1 A	L7A,L7B
7B TO L8B	I A	L8A,L8B
	NU	L9A,L9B
	NU	L10A,L10B
	NU	L11A,L11B
	NU	L12A,L12B
	NU	L13A,L13B
	NU	L14A,L14B
	NU	L15A,L15B
	NU	L16A,L16B

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE

ACCURD	ING TU T	HE SCHI	EDULE
SHOWN	IN THE	CHART B	ELOW
CONTROLLER	FUNCTION	ΤΙ	MING
DETECTOR NO.	ONCTION	FEATURE	TIME(SEC)
<del>**</del> 1	Ø 2		
<del>**</del> 2	Ø 2		
. 3	Ø 4		
4	Ø 4	DELAY	15
5			
<del>**</del> 6	Ø6		
. 7	Ø 1	DELAY	15
* 8	Ø6	DELAY	3
. 9			
10			
· 11			
12			
13			
4.4			

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN

UIV	LUUP PA	ANEL AS SHUWN
	IN THE (	CHART BELOW
	LOOP NO.	LOOP PANEL TERMINALS
	S13	L17A,L17B
	NU	L18A,L18B
	NU	L19A,L19B
	NU	L20A,L20B
	NU	L21A,L21B
	NU	L22A,L22B
	NU	L23A,L23B
	NU	L24A,L24B
	NU	L25A,L25B
	NU	L26A,L26B
	NU	L27A,L27B
	NU	L28A,L28B
	NU	L29A,L29B
	NU	L30A,L30B
	NU	L31A,L31B
	NU	L32A,L32B

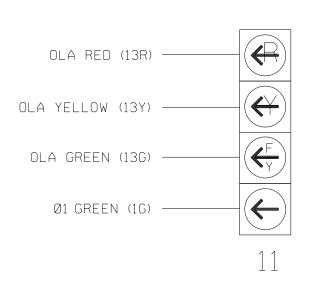
#### EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....1,2,4,6,13 PHASES USED......1,2,4,6 OLA.....\* OLB.....NOT USED OLC.....NOT USED OLD.....NOT USED

\* See overlap programming detail on sheet 2

#### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



#### LOAD SWITCH ASSIGNMENT DETAIL

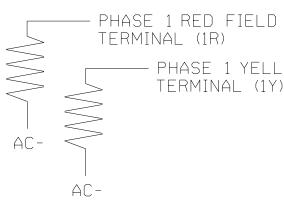
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	Ø 1
2	Ø 2
3	_
4	Ø 4
5	_
6	Ø 6
7	_
8	_
9	_
10	_
11	_
12	_
13	ØOLA
14	_
15	_
16	_

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



PHASE 1 YELLOW FIELD TERMINAL (1Y)

#### ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (mın)
2.ØK - 3.ØK	10W (mın)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T3 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 3 Temporary Design 3 - TMP Phase II



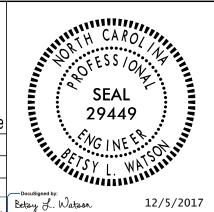
Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606



# UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway)

I-40 EB Ramps

Division 5 Wake County Morrisville NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE



**DOCUMENT NOT CONSIDERED FINAL** 

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS. Tel. (919) 851-6866 Fax. (919) 851-7024 50 N.Greenfield Pkwy,Garner,NC 27529 www.stantec.com \* Detector Type - G License No. F-0672 \*\* Detector Type - N SIG. INVENTORY NO.

I-5506 SIG-8.2

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 1

END PROGRAMMING

The PPLT FYA operation of Signal Head 11 (Overlap A) can be altered to fully protected operation.

#### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING <u>free run</u> — program changes (shown below) in a time based action plan, schedule a day plan that includes the action plan programmed TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

TO RUN ALT, PHASING DURING <u>COORDINATION</u> - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

PHASING	veh det plan	SF BITS ENABLED
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	1 2	NONE 1

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OR WHAT TAKES PLACE WHEN SF BIT 1 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE

PHASING":

SF BIT 1 Modifies overlap parent phases for heads 11 to run protected turns only.

VEH DET PLAN 2:

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.

# ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select 5. TIME BASE

2. From TIME BASE Submenu select 2. ACTION PLAN

ACTION PLAN.   99   97   98   98   98   98   98   98										,							
TIMING PLAN	ACTION PLA	4N.	[	99	]												
NEH DETECTOR   PLAN   2	PATTERN				.99		SYS	0 V	'ERR	IDE		. \	0				
FLASH	TIMING PLA	ΔN.			. 0		SEQ	UEN	ICE.			•	0				
VEH DET DIAG PLN         O         PED DET DIAG PLN         O         PED DET DIAG PLN         O           DIMMING ENABLE         NO         PRIORITY RETURN.         NO         PRIORITY RETURN.         NO           PED PR RETURN.         NO         QUEUE DELAY         NO         PRIORITY RETURN.         NO           PHASE         1         2         3         4         5         6         7         8         9         0         1         2         3         4         5         6           PED RCL   .	VEH DETEC	TOR	PL	AN.	. 2		DET	LC	)G			NON	Ε				
PIMMING ENABLE   NO	FLASH			•			RED	RE	ST.			. \	0				
PED PR RETURN 5	VEH DET D	IAG	PL	Ν	. 0		PED	DE	T D	IAG	PL	Ν	0				
PMT COND DEL XY	DIMMING EN	NAB	LE.	•	NO		PRI	OR I	ΤY	RET	URN	. \	0				
PHASE         1         2         3         4         5         6         7         8         9         0         1         2         3         4         5         6         7         8         9         0         1         2         3         4         5         6           PED RCL         1 <td< td=""><td>PED PR RE</td><td>TUR</td><td>Ν</td><td></td><td>NO</td><td></td><td>QUE</td><td>UE</td><td>DEL</td><td>AY.</td><td></td><td>. \</td><td>0</td><td></td><td></td><td></td><td></td></td<>	PED PR RE	TUR	Ν		NO		QUE	UE	DEL	AY.		. \	0				
PED RCL WALK 2	PMT COND [	DEL	ДΥ		NO												
WALK 2	PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
VEX 2         1.	PED RCL	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
VEH RCL	WALK 2	9		•	6	•	•	•	6	9	•	•	•	•	•	•	•
MAX RCL	VEX 2	•	•	•		•	•	•	6	•	•	•	•	٠	•	٠	•
MAX 2         1         2         3         4         5         6         7         8         9         0         1         2         3         4         5         6         7         8         9         0         1         2         3         4         5         6           MAX 3         1         2         3         4         5         6         7         8         9         0         1         2         3         4         5         6           CS 1NH         1         1         2	VEH RCL	9		•	6	•	•	•		9	•	•	6	•	•	•	•
PHASE         1         2         3         4         5         6         7         8         9         0         1         2         3         4         5         6           MAX 3	MAX RCL	9		•		•	9	•		9		•	6	•	6	•	•
MAX 3	MAX 2	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
CS INH OMIT OMIT OMIT OMIT OMIT OMIT OMIT OMIT	PHASE	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6
OMIT	MAX 3	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
SPC FCT       X       . </td <td>CS INH</td> <td>s</td> <td>•</td> <td>•</td> <td>6</td> <td>•</td> <td>•</td> <td>•</td> <td>6</td> <td>6</td> <td>9</td> <td>•</td> <td>•</td> <td>•</td> <td>•</td> <td>•</td> <td>•</td>	CS INH	s	•	•	6	•	•	•	6	6	9	•	•	•	•	•	•
AUX FCT       .       .       .       (1-3)         LP 1-15       .	OMIT	•	•	•	6	•	•	•	6	•	•	•	•	٠	•	•	•
LP 1-15       2       3       4       5       6       7       8       9       0       1       2       3       4       5         LP 1-15       2	SPC FCT	X		•	6	•	•	9	6	( 1	-8)						
LP 1-15	AUX FCT	s		•	( 1		)										
LP 16-30		1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	
LP 31-45		9	•	•	6	•	٠	9	6	•	•	•	•	٠	•	•	
LP 46-60		•	•	•	•	•	٠	•	٠	•	•	•	•	•	٠	٠	
LP 61-75		•	•	•	6	•	•	•		•	•	•	•	•	•	•	
LP 76-90			•	•	•	•	•	•	•			•	•	•	•	•	
		•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	
LP 91-100 · · · · · · · · · · · · · · · · · ·		•	•	•	•	•	•	•	•		•	•	•	•	٠	•	
	LP 91-100	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•	

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1735T3 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 2 of 3 Temporary Design 3 - TMP Phase II



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



# DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 EB Ramps

Division 5 Wake County PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY

REVISIONS INIT. DATE

SIG. INVENTORY NO. 05-1735

I-5506 SIG-8.3

# ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERATE PHASING LOOPS 1A

(program controller as shown)

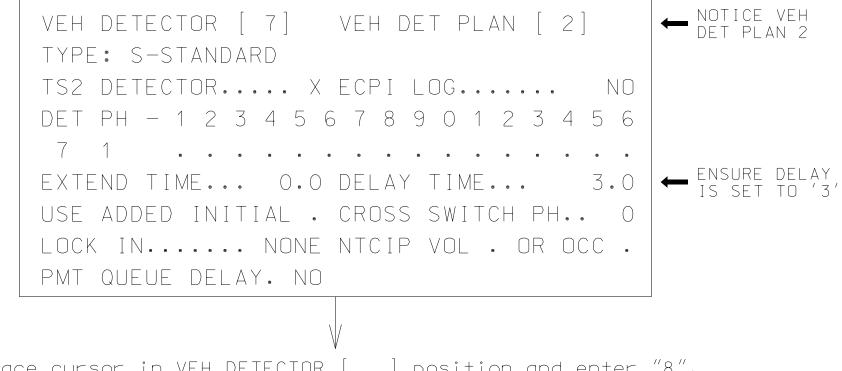
# 

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu selet 8. UTILITIES
- 2. From UTILITIES Submenu select 1. COPY/CLEAR
- 3. Copy from Detector Plan "1" to Detector Plan "2".

COPY / CLEAR UTILITY TO FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN..... > TIMING PLAN..... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP |
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - For Loop 1A, modify vehicle detectors.
  - Place cursor in VEH DETECTOR [ ] position and enter "7".
  - Set delay time to "3".



- Place cursor in VEH DETECTOR [ ] position and enter "8". - Set assigned phase to "0".

VEH DETECTOR [ 8] VEH DET PLAN [ 2] TYPE: G-GREEN EXTENSION DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY, NO

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T3 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 3 of 3 Temporary Design 3 - TMP Phase II



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



# DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 EB Ramps

Wake County Division 5 PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS

INIT. DATE 12/5/2017 SIG. INVENTORY NO. 05-1735

29449

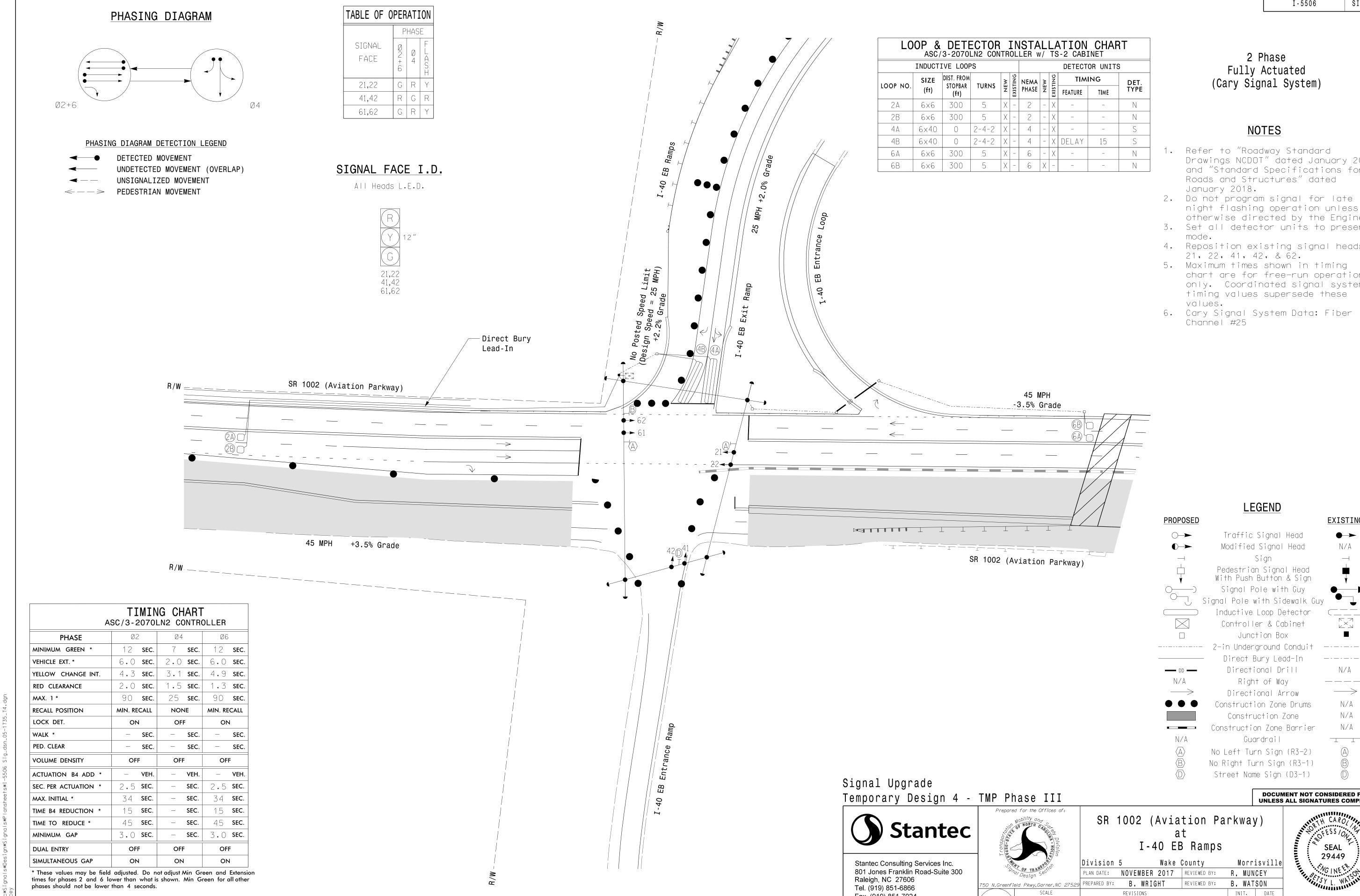
50 N.Greenfield Pkwy,Garner,NC 27529

PROJECT REFERENCE NO. I-5506 SIG-9.0 2 Phase Fully Actuated (Cary Signal System) NOTES 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018. 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer. 3. Set all detector units to presence mode. 4. Reposition existing signal heads 21, 22, 41, 42, & 62. 5. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values. 6. Cary Signal System Data: Fiber Channel #25 LEGEND <u>PROPOSED</u> **EXISTING** Traffic Signal Head **●**→ Modified Signal Head N/A Sign Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Inductive Loop Detector Controller & Cabinet Junction Box ---- 2-in Underground Conduit Direct Bury Lead-In Directional Drill N/A Right of Way N/A \_\_\_\_\_  $\longrightarrow$ Directional Arrow Construction Zone Drums N/A N/A Construction Zone Construction Zone Barrier N/A Guardrail  $\overline{\phantom{a}}$  $\triangle$ No Left Turn Sign (R3-2) No Right Turn Sign (R3-1)  $^{\odot}$ Street Name Sign (D3-1) DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED 29449 Wake County Morrisville R. MUNCEY INIT. DATE

Betsy L. Watson

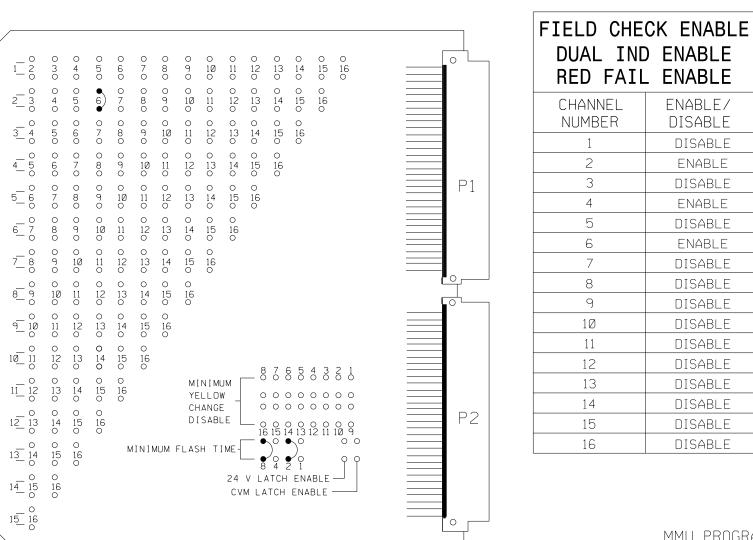
SIG. INVENTORY NO. 05-1735T

12/1/2017



Fax. (919) 851-7024

www.stantec.com License No. F-0672 (program card and tables as shown)



MMU PROGRAMMING CARD

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	ELLOW ARROW			
CONFIG MODE	В			
ENABLE CHANN	NEL PAIR, FYA			
CH 1-13	OFF			
CH 3-14	OFF			
CH 5-15	OFF			
CH 7-16	OFF			
RED/YEL INF	PUT ENABLE			
CH 1	OFF			
CH 3	OFF			
CH 5	OFF			
CH 7	OFF			
FLASH RATE FAULT	OFF			
FYA TRAP DETECT	OFF			
CH 1 CH 3 CH 5 CH 7 LASH RATE FAULT	OFF OFF OFF			

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DISABLE ENABLE

DISABLE

ENABLE

DISABLE

ENABLE

DISABLE DISABLE

DISABLE

DISABLE

DISABLE

DISABLE

DISABLE

DISABLE

DISABLE

DISABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs
  - 1,3,5,7,8,9,10,11,12,13,14,15 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-9.1

SIGNAL HEAD HOOK-UP CHART																
PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	41,42	NU	61,62	NU	NU	NU	NU	NU	NU	NU	NU	NU	NU
RED		2R		4R		6R										
YELLOW		2Y		4 Y		6Y										
GREEN		2G		4G		6G										
RED ARROW																
YELLOW ARROW																
FLASHING YELLOW ARROW																
GREEN ARROW																
₩																
Ķ																

NU = NOT USED

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1		CH1							
		L3	L1	S	L5	S	S	S	S	S.	S	S
		Ø4	Ø2		Ø6							
RACK #1		4A	2A	T	6A	T	T	Т	T	T	Т	T
# <u>1</u>	BIU	CH2	CH2	E	CH2	E	E	E	E	E	E	E
		L4	L2	M P	L6	M P	M P	M P	M	M P	M P	M
		Ø 4	Ø2	Ţ	Ø6	Ţ	<u> </u>	<u> </u>	<u> </u>	<u></u>	Ţ	
		4B	2B	Y	6B	Y	Y	Y	Y	Y	Y	Y

RACK		S L O T	S L O T	SLOT	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	
#2	BIU	E M P T Y											

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN

IN THE C	CHART BELOW
LOOP NO.	LOOP PANEL TERMINALS
2 A	L1A,L1B
2B	L2A,L2B
4 A	L3A,L3B
4B	L4A,L4B
64	L5A,L5B
6B	L6A,L6B
NU	L7A,L7B
NU	L8A,L8B
NU	L9A,L9B
NU	L10A,L10B
NU	L11A,L11B
NU	L12A,L12B
NU	L13A,L13B
NU	L14A,L14B
NU	L15A,L15B
NU	L16A,L16B

PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS ACCORDING TO THE SCHEDULE

SHOWN	IN THE	CHART B	SELOW
ONTROLLER	FUNCTION	TI	MING
ETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)
<del>**</del> 1	Ø 2		
<del>**</del> 2	Ø 2		
. 3	Ø 4		
4	Ø 4	DELAY	15
<del>**</del> 5	Ø 6		
<del>**</del> 6	Ø 6		
· 7			
- 8	٠		
. 9			
10			
· 11			
12			
13			
14			
. 15			

ON LOOP PANEL AS SHOWN

LOOP NO.	LOOP PANEL TERMINALS
NU	L17A,L17B
NU	L18A,L18B
NU	L19A,L19B
NU	L20A,L20B
NU	L21A,L21B
NU	L22A,L22B
NU	L23A,L23B
NU	L24A,L24B
NU	L25A,L25B
NU	L26A,L26B
NU	L27A,L27B
NU	L28A,L28B
NU	L29A,L29B
NU	L30A,L30B
NU	L31A,L31B
NU	L32A,L32B

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

CONTROLLER	CHNCTION.	TIMING					
DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)				
· 17							
1.8							
19							
20							
· 21							
. 22							
23							
24							
25	٠						
26	٠						
27							
28							
29							
. 30							
· 31							
. 32	٠						

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

# EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2

SOFTWARE .....ECONOLITE ASC/3-2070

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....2,4,6 PHASES USED.....2,4,6 OLA.....NOT USED OLB.....NOT USED OLC.....NOT USED OLD.....NOT USED

#### LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	_
2	Ø 2
3	_
4	Ø 4
5	_
6	Ø 6
7	_
8	_
9	_
10	_
11	_
12	_
13	_
14	_
15	_
16	-
1055 1 0 1 5	

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T4

DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail Temporary Design 4 - TMP Phase III



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



50 N.Greenfield Pkwy,Garner,NC 27529

# SR 1002 (Aviation Parkway) I-40 EB Ramps

Wake County L. OVERN

Division 5 Morrisville NOVEMBER 2017 REVIEWED BY: PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE



**DOCUMENT NOT CONSIDERED FINAL** 

UNLESS ALL SIGNATURES COMPLETED

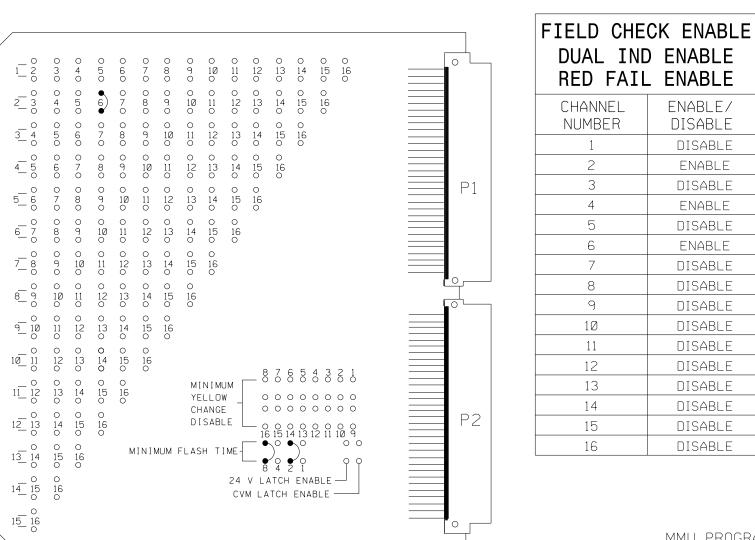
SIG. INVENTORY NO.

\*\* Detector Type - N

PROJECT REFERENCE NO. I-5506 SIG-10.0 TABLE OF OPERATION PHASING DIAGRAM PHASE LOOP & DETECTOR INSTALLATION CHART ASC/3-2070LN2 CONTROLLER W/ TS-2 CABINET SIGNAL 2 Phase FACE INDUCTIVE LOOPS DETECTOR UNITS Fully Actuated SIZE DIST. FROM (Cary Signal System) Ž NEMA | ≥ | Ž DET. 21,22 Z FEATURE TIME 41,42 61,62 6×6 NOTES 6×40 4 |-|X|DELAY| 15 PHASING DIAGRAM DETECTION LEGEND 1. Refer to "Roadway Standard Drawings DETECTED MOVEMENT NCDOT" dated January 2018 and SIGNAL FACE I.D. "Standard Specifications for Roads UNDETECTED MOVEMENT (OVERLAP) and Structures" dated January 2018. UNSIGNALIZED MOVEMENT All Heads L.E.D. 2. Do not program signal for late PEDESTRIAN MOVEMENT night flashing operation unless otherwise directed by the Engineer. 3. Set all detector units to presence 4. Reposition existing signal heads 61, & 62. 5. Maximum times shown in timing chart are for free-run operation only. 21,22 41,42 61,62 Coordinated signal system timing values supersede these values. 6. Cary Signal System Data: Fiber Channel #25 SR 1002 (Aviation Parkway) 45 MPH -3.5% Grade LEGEND <u>EXISTING</u> PROPOSED Traffic Signal Head **●**→ SR 1002 (Aviation Parkway) Temporary Pole -Modified Signal Head N/A -L- STA. 44+81 Sign (85' LT) Pedestrian Signal Head With Push Button & Sign Signal Pole with Guy Signal Pole with Sidewalk Guy Inductive Loop Detector TIMING CHART Controller & Cabinet ASC/3-2070LN2 CONTROLLER Junction Box PHASE Direct Bury Lead-In 7 **SEC**. 12 **SEC**. MINIMUM GREEN 12 **SEC**. 2-in Underground Conduit VEHICLE EXT. \* 2.0 **SEC**. 6.0 **SEC**. 6.0 **SEC**. Directional Drill 4.3 **SEC**. YELLOW CHANGE INT. 3.0 **SEC**. 4.9 **SEC**. N/A Right of Way 1.3 **SEC**. 2.3 **SEC**. 1.6 **SEC**. RED CLEARANCE Directional Arrow 90 **SEC**. 25 **SEC**. 90 **SEC** Construction Zone Drums N/A RECALL POSITION MIN. RECALL NONE MIN. RECALL Construction Zone N/A LOCK DET. ON OFF ON Construction Zone Barrier N/A — SEC. SEC N/A Guardrail ⟨A⟩ No U-Turn/No Left Turn Sign (R3-18) ⟨A⟩ PED. CLEAR — SEC. No Right Turn Sign (R3-1) OFF OFF OFF VOLUME DENSITY ACTUATION B4 ADD \* VEH. — VEH. Signal Upgrade 2.5 **SEC**. 2.5 **SEC**. SEC. PER ACTUATION \* DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Temporary Design 5 - TMP Phase IV 34 **SEC**. 34 **SEC**. MAX. INITIAL \* — SEC. 15 **SEC**. 15 **SEC**. TIME B4 REDUCTION ' SR 1002 (Aviation Parkway) TIME TO REDUCE \* 45 **SEC**. — SEC. 45 **SEC**. **Stantec** 3.0 **SEC**. — SEC. 3.0 **SEC**. MINIMUM GAP I-40 EB Ramps OFF DUAL ENTRY OFF OFF 29449 SIMULTANEOUS GAP ON Stantec Consulting Services Inc. Division 5 Wake County Morrisville 801 Jones Franklin Road-Suite 300 NOVEMBER 2017 REVIEWED BY: R. MUNCEY \* These values may be field adjusted. Do not adjust Min Green and Extension Raleigh, NC 27606 times for phases 2 and 6 lower than what is shown. Min Green for all other 9 PREPARED BY: B. WRIGHT REVIEWED BY: B. WATSON 50 N.Greenfield Pkwy,Garner,NC 275 phases should not be lower than 4 seconds. Tel. (919) 851-6866 REVISIONS INIT. DATE Fax. (919) 851-7024 Betsy L. Watson 12/1/2017 www.stantec.com License No. F-0672

sig. Inventory No. 05-1735T

(program card and tables as shown)



MMU PROGRAMMING CARD

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	ELLOW ARROW
CONFIG MODE	В
ENABLE CHANN	NEL PAIR, FYA
CH 1-13	OFF
CH 3-14	OFF
CH 5-15	OFF
CH 7-16	OFF
RED/YEL INF	PUT ENABLE
CH 1	OFF
CH 3	OFF
CH 5	OFF
CH 7	OFF
FLASH RATE FAULT	OFF
FYA TRAP DETECT	OFF

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs
  - 1,3,5,7,8,9,10,11,12,13,14,15 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-10.1

SIGNAL HEAD HOOK-UP CHART																
PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	41,42	NU	61,62	NU	NU	NU	NU	NU	NU	NU	NU	NU	NU
RED		2R		4R		6R										
YELLOW		2Y		4 Y		6Y										
GREEN		2G		4G		6G										
RED ARROW																
YELLOW ARROW																
FLASHING YELLOW ARROW																
GREEN ARROW																
*																
×																

NU = NOT USED

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1		CH1							
		L3	L1	S	L5	S	S	S	S	S	S	S
		Ø 4	Ø2		Ø6							
RACK #1		4 A	2A	Ť	6A	Ť	Ť	Ť	Ť	T	Ţ	T
# ]	BIU	CH2	CH2	E	CH2	E	E	E	E	E	E	E
		L4	L2	M P	L6	M P	M P	M P	M P	M	M P	M
		Ø 4	Ø2	ή	Ø6	<u>'</u>	T	<u> </u>	T	<u> </u>	T	T
		4B	2B	Y 	6B	Y	Y	Y	Y	Y	<u> </u>	Y

RACK		S L O T	SLOT	S L O T	S L O T	SLOT	S L O T	S L O T	SLOT	S L O T	SLOT	SLOT
#2	BIU	E M P T	E M P T Y	E M P T	E M P T Y							

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN

IN THE (	CHART BELOW
LOOP NO.	LOOP PANEL TERMINALS
2 A	L1A,L1B
2B	L2A,L2B
4 A	L3A,L3B
4B	L4A,L4B
6A	L5A,L5B
6B	L6A,L6B
NU	L7A,L7B
NU	L8A,L8B
NU	L9A,L9B
NU	L10A,L10B
NU	L11A,L11B
NU	L12A,L12B
NU	L13A,L13B
NU	L14A,L14B
NU	L15A,L15B
NU	L16A,L16B

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE

ACCURD	ING TO	INE SCHI	
SHOWN	IN THE	CHART B	ELOW
CONTROLLER	FUNCTION	TI	MING
DETECTOR NO.	TUNCTION	FEATURE	TIME(SEC)
<del>**</del> 1	Ø 2		
<del>**</del> 2	Ø 2		
. 3	Ø 4		
. 4	Ø 4	DELAY	15
<del>**</del> 5	Ø 6		
<del>**</del> 6	Ø 6		
· 7			
- 8			
. 9			
10			
· 11			
12			
13			
14			
. 15			

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN

IN THE (	CHART BELOW
LOOP NO.	LOOP PANEL TERMINALS
NU	L17A,L17B
NU	L18A,L18B
NU	L19A,L19B
NU	L20A,L20B
NU	L21A,L21B
NU	L22A,L22B
NU	L23A,L23B
NU	L24A,L24B
NU	L25A,L25B
NU	L26A,L26B
NU	L27A,L27B
NU	L28A,L28B
NU	L29A,L29B
NU	L30A,L30B
NU	L31A,L31B
NU	L32A,L32B

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

SHOWN	11N 111L	CHAIL	LLUW				
CONTROLLER	FUNCTION	TIMING					
DETECTOR NO.	LONCITON	FEATURE	TIME(SEC)				
. 17							
18							
19							
20							
· 21							
22							
23							
24							
25	٠						
26							
27	٠						
28							
29							
. 30							
· 31							
. 32	·	·					

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

\*\* Detector Type - N

16

# EQUIPMENT INFORMATION

CONTROLLER	2070LN2
CABINET	TS-2
SOFTWARE	ECONOLITE ASC/3-2070
CABINET MOUNT	BASE
LOADBAY POSITIONS	. 16
LOAD CWITCHES HISED	O 1 C

LOAD SWITCHES USED.....2,4,6 OLA....NOT USED OLB....NOT USED OLC.....NOT USED OLD.....NOT USED

#### LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	-
2	Ø 2
3	-
4	Ø 4
5	-
6	Ø 6
7	-
8	-
9	-
10	-
11	-
12	-
13	-
14	-
15	-
16	_

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735T5 DESIGNED: NOV 2017

SEALED: 12-01-2017 REVISED: N/A

Electrical Detail Temporary Design 5 - TMP Phase IV



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



50 N.Greenfield Pkwy,Garner,NC 27529

# SR 1002 (Aviation Parkway) I-40 EB Ramps

Wake County L. OVERN

Division 5 Morrisville NOVEMBER 2017 REVIEWED BY: PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

**DOCUMENT NOT CONSIDERED FINAL** 

UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO.

License No. F-0672

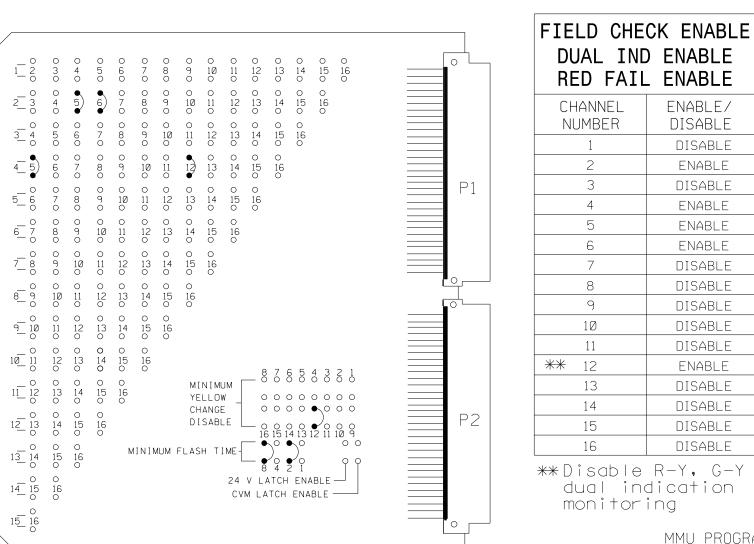
SIG-11.0

Betsy L. Watson 12/1/2017

SIG. INVENTORY NO.

lower than 4 seconds.

(program card and tables as shown)



MMU PROGRAMMING CARD

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	LLOW ARROW
CONFIG MODE	В
ENABLE CHANN	NEL PAIR, FYA
CH 1-13	OFF
CH 3-14	OFF
CH 5-15	OFF
CH 7-16	OFF
RED/YEL INF	PUT ENABLE
CH 1	OFF
CH 3	OFF
CH 5	OFF
CH 7	OFF
FLASH RATE FAULT	OFF
FYA TRAP DETECT	OFF

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DISABLE ENABLE

DISABLE

ENABLE

ENABLE

ENABLE DISABLE

DISABLE

DISABLE

DISABLE

DISABLE

ENABLE

DISABLE

DISABLE

DISABLE

DISABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 1,3,7,8,9,10,11,13,14,15 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-11.1

	SIGNAL HEAD HOOK-UP CHART															
PHASE	1	2	3	OLE	OLF	6	7	8	2 PED	4 PED	6 PED	3 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NC	41,42	51,52	61,62	NU	NU	NU	NU	NU	P31, P32	NU	NU	NU	NU
RED		2R				6R										
YELLOW		2Y				6Y										
GREEN		2G				6G										
RED ARROW				4R	5R											
YELLOW ARROW				4 Y	5Y											
FLASHING YELLOW ARROW																
GREEN ARROW				4G	5G											
*												12R				
Ķ												12G				

NU = NOT USED NC = NO CONNECTION

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1						
		L3	L1	L7	L5	L11	S	S	S	S	S	S
		Ø 4	Ø2	Ø6	Ø 4	Ø4	L	L	L			
RACK #1	DIII	4 A	2A	64	5A	5B	T	T	T	T	T	T
1	BIU	CH2	CH2	CH2	CH2	CH2	E	E	E	E	E	E
		L4	L2	L8	L6	L12	M P	M	M	M	M	M
		Ø 4	Ø2	Ø6	Ø5	Ø5	T	T	<u> </u>		<u> </u>	T
		4B	2B	6B	5A	5B	Y	Y	Y	Y	Y	Y

		CH1		CH1									
RACK		L19 SYS DET S13	S L O T	L23 SYS DET S15	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	S L O T	SLOT	
#2	BIU	CH2 L2Ø SYS DET S14	E M P T Y	CH2 L24 SYS DET S16	E M P T Y								

PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE IN THE CHART BELOW SHOWN IN THE CHART BELOW

WIR	E LOOPS	TO TERMINALS	F
ON	LOOP PA	ANEL AS SHOWN	
]	IN THE (	CHART BELOW	
	LOOP NO.	LOOP PANEL TERMINALS	COI
	2 A	L1A,L1B	
	2B	L2A,L2B	*
	4 A	L3A,L3B	*
	4B	L4A,L4B	•
	5 A	L5A,L5B	
	5B	L6A,L6B	•
	6 A	L7A,L7B	
	6B	L8A,L8B	*
	NU	L9A,L9B	*
	NU	L10A,L10B	
	NU	L11A,L11B	•
	NU	L12A,L12B	
	NU	L13A,L13B	
	NILI	L 1 4 A L 1 4 D	

NU L15A,L15B

NU L16A,L16B

	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,				•
l	SHOWN	IN THE	CHART B	SELOW	Ι
	CONTROLLER	FUNCTION	ΤI	MING	
	DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)	
	<del>**</del> 1	Ø 2			
	<del>**</del> 2	Ø 2			
	. 3	Ø 4			
	. 4	Ø 4			
	. 5	Ø 5	DELAY	15	
	. 6	Ø 5	DELAY	15	
	<del>**</del> 7	Ø 6			
	<del>**</del> 8	Ø 6			
	. 9				
	· 10				
	· 11				
	12				
	. 13			_	

	IN THE (	CHART BELOW			SHO
EC)	LOOP NO.	LOOP PANEL TERMINALS		CONT ETE(	ROLL CTOR
	NU	L17A,L17B	H		
	NU	L18A,L18B			17
	S13	L19A,L19B		-	18
	S14	L20A,L20B		**	19
	NU	L21A,L21B		**	20
	NU	L22A,L22B			21
	S15	L23A,L23B			22
				**	23
	S16	L24A,L24B		**	24
	NU	L25A,L25B			25
	NU	L26A,L26B			
	NU	L27A,L27B			26
	NU	L28A,L28B			27
	NU	L29A,L29B			28
	NU	L30A,L30B			29
	NU	L31A,L31B			30
					31
	NU	L32A,L32B			32

BELOW	SHUWN	IN THE	CHARI B	ELUW
PANEL IINALS	CONTROLLER	FUNCTION	ΤI	MING
,L17B	DETECTOR NO.	1 011011011	FEATURE	TIME(SEC)
	· 17	*		
,L18B	- 18			
,L19B	<del>**</del> 19	SYSTEM		
,L20B	<del>**</del> 20	SYSTEM		
,L21B		SISIEM		
,L22B	• 21	*		
,L23B	• 22	*		
,L24B	<del>**</del> 23	SYSTEM		
,L25B	<del>**</del> 24	SYSTEM		
	. 25			
,L26B	26			
,L27B	27			
,L28B		<u> </u>		
,L29B	28			
,L30B	29			
,L31B	- 30			
	. 31			
,L32B	32			

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

\*\* Detector Type - N

14

15

## EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....2,4,5,6,12 PHASES USED........2,3PED,4,5,6,\*3 OLA....NOT USED OLB....NOT USED OLC.....NOT USED OLD.....NOT USED OLE.....3+4 OLF.....4+5 OLG (DUMMY)....2+4+5+6 \* Used for timing purposes

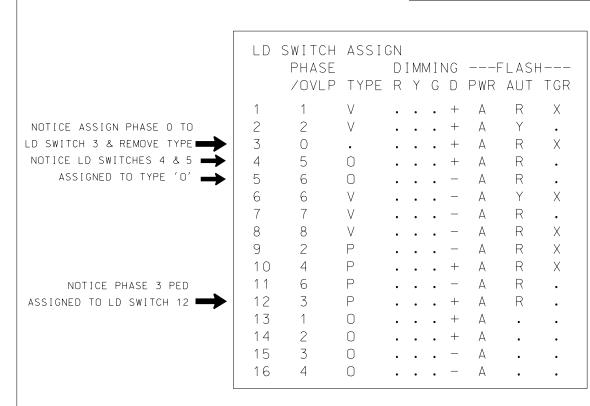
#### ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switches 4 and 5 as OLE and OLF, program LD SWITCH 4 as OVLP '5' TYPE '0' and LD SWITCH 5 as OVLP '6' TYPE '0' as shown below.

1. From Main Menu select | 1. CONFIGURATION |

2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



#### ECONOLITE ASC/3-2070 PED 3 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

1. From Main Menu select 6. DETECTORS

2. From DETECTOR Submenu select 3. PED DETECTOR INPUT ASSIGNMENT

PED DET PHASE ASSIGNMENT MODE: NTCIP PHASE 1 2 3 4 5 6 7 8 DETECTOR 0 2 8 4 0 6 0 0 PHASE 9 10 11 12 13 14 15 16 DETECTOR O O O O O O

1. See "ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL" for load switch assignment configuration.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 2 Final Design



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



50 N.Greenfield Pkwy,Garner,NC 27529

#### SR 1002 (Aviation Parkway) at I-40 EB Ramps

Division 5 Wake County Morrisville PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

29449 Betsy L. Watson SIG. INVENTORY NO.

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

#### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL (program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

Toggle Four Times

Toggle Once

OVERLAP E Select TMG VEH OVLP [E] and 'NORMAL TMG VEH OVLP...[E] TYPE: ......NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0

OVERLAP F

Select TMG VEH OVLP [F] and 'NORMAL' TMG VEH OVLP...[F] TYPE: .....NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0

Toggle Once OVERLAP G (DUMMY)

MOVE CURSOR OVER [F]
TOGGLE TO TMG VEH OVLP [G] TMG VEH OVLP...[G] TYPE: .....NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LAG GRN 0.0 YEL 0.0 RED 0.0

END PROGRAMMING

#### ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR "NO TURN ON RED" BLANK OUT SIGN

(program controller as shown)

The following logic processor configuration activates the blank out sign during normal operation.

Upon the red interval preceeding phase 3, the logic will activate the blank out sign.

1. From Main Menu select | 1. CONFIGURATION |

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

1 COPY FROM: 1 ACTIVE: M (T/F) VEH OVERLAP RED 7 IS ON AND LP COB CODE OFF 544 THEN SIG SET PH PED CLR 3 ON ELSE SIG SET PH PED CLR 3 OFF

LOGIC FOR ACTIVATING THE BLANK OUT SIGN DURING NORMAL OPERATION, THE AND CONDITION ENSURES SIGN IS OFF DUING CONTROLLER FLASH.

END PROGRAMMING

Notes:

- 1. COB 544 is a controller flash internal logic processor reference.
- 2. To ensure sign does not illuminate upon exit from cabinet or controller flash, enable overlap G for START UP and AUTOMATIC flash.

1. From Main Menu select | 1. CONFIGURATION |

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select | 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENT 1 BY POSITIONING

THE CURSOR OVER THE FIELD SHOWN BELOW AND USING

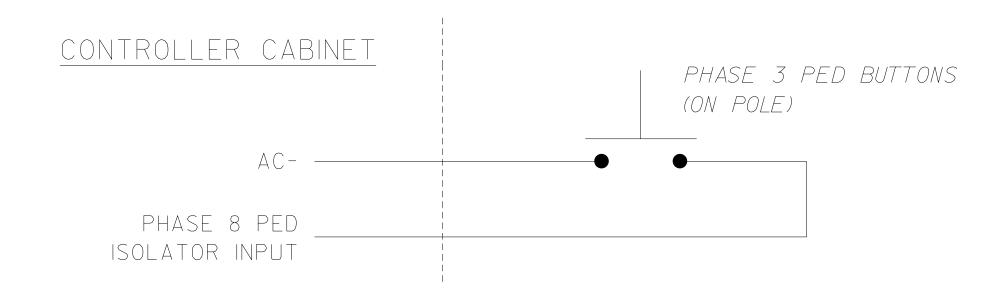
THE TOGGLE KEY TO ENABLE IT.

LOGIC STATEMENT CONTROL 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 

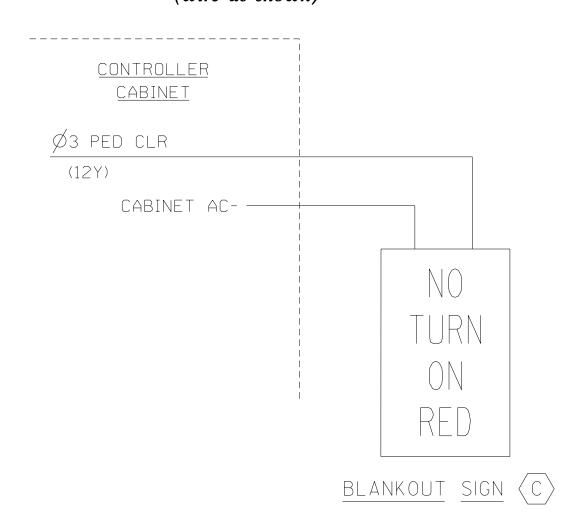
END PROGRAMMING

#### PEDESTRIAN PUSH BUTTON WIRING DETAIL

(wire push buttons as shown)



# BLANKOUT SIGN WIRING DETAIL (wire as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1735 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Electrical Detail - Sheet 2 of 2 Final Design



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50 N.Greenfield Pkwy,Garner,NC 2752

# SR 1002 (Aviation Parkway) I-40 EB Ramps

Wake County Morrisville Division 5 PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

SIG. INVENTORY NO.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

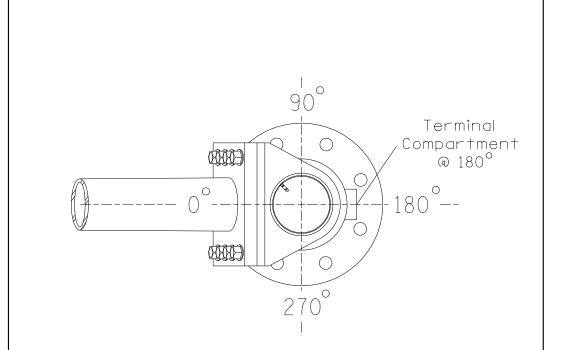
Elevation View

#### SPECIAL NOTE

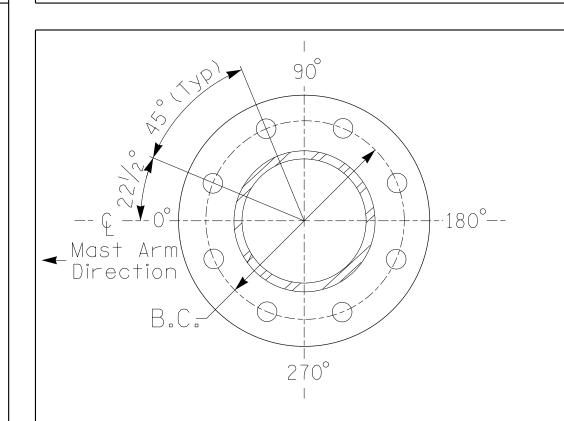
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

#### Elevation Data for Mast Arm Attachment (H1)

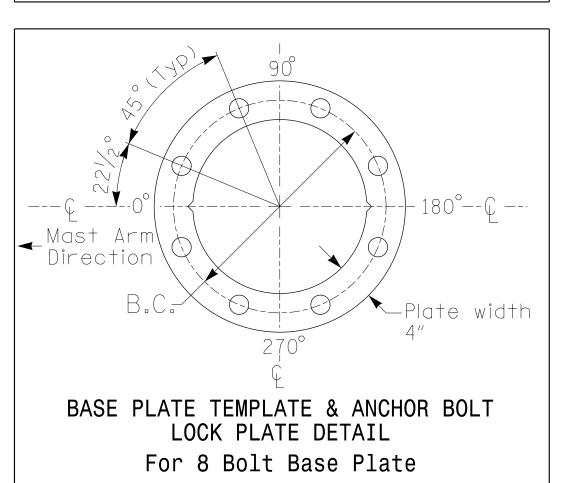
Elevation Differences for:	Pole 5	
Baseline reference point at £ Foundation @ ground level	344.97 ft.	
Elevation difference at High point of roadway surface	-1.03 ft.	
Elevation difference at Edge of travelway or face of curb	-1.43 ft.	



#### POLE RADIAL ORIENTATION



8 BOLT BASE PLATE DETAIL See Note 6



#### METAL POLE No. 5

I - 5506	SIG-11.3
PROJECT REFERENCE NO.	SHEET NO.

	MAST ARM LOADING SC	HEDU	LE	
loading Symbol	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5" W X 52.5"L	60 LBS
	PEDESTRIAN SIGNAL HEAD WITH MOUNTING HARDWARE	2.2 S.F.	18.5" W X 17.0" L	21 LBS
2	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0"L	14 LBS

#### NOTES

#### DESIGN REFERENCE MATERIAL

1. Design the traffic signalstructure and foundation in accordance with:

- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signalproject specialprovisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signalproject plans and specialprovisions.
- The NCDOT "MetalPole Standards" located at the following NCDOT website:
- https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

#### DESIGN REQUIREMENTS

- 2. Design the traffic signalstructure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that willbe applied at the time of the installation. The contractor should refer to the traffic signalplans for the actualloads that will be applied at the time of the installation.
- 3. Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions:
- a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views. d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed
- foundation ground leveland the high point of the roadway. 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or

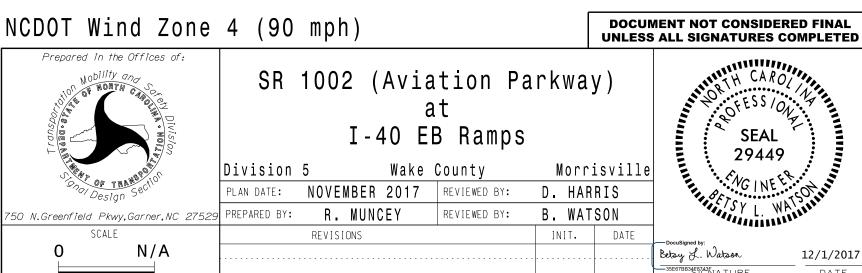
N/A

- H1 plus 1/2 of the totalheight of the mast arm attachment assembly plus 1 foot. 9. If pole location adjustments are required, the contractor must gain approval from the
- Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919)814-5000.
- 10.The contractor is responsible for verifying that the mast arm length shown willallow proper positioning of the signalheads over the roadway.
- 11. The contractor is responsible for providing soilpenetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



SIG. INVENTORY NO.

05-1739



-High Point of Roadway Surface-

See Note 7e

Base line reference elev. = 0.0'

Elevation View @ 0

G Foundation

METAL POLE No. 6

MAST ARM LOADING SCHEDULE LOADING DESCRIPTION SYMBOL RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE SIGN RIGID MOUNTED

#### **NOTES**

#### DESIGN REFERENCE MATERIAL

- 1. Design the traffic signal structure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signal project plans and special provisions.
- The NCDOT "Metal Pole Standards" located at the following NCDOT website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

#### DESIGN REQUIREMENTS

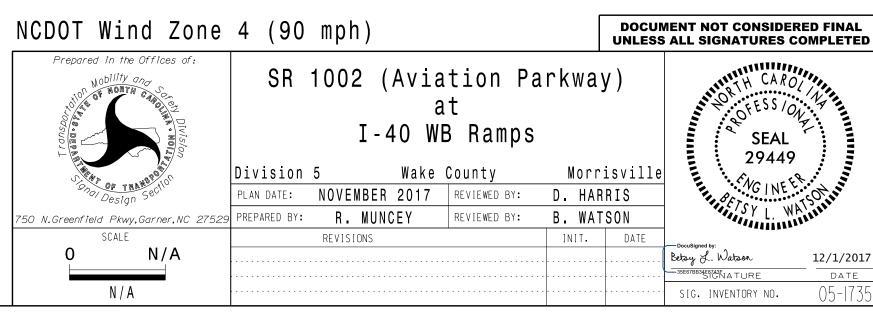
- 2. Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation. 3. Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate arm connection points.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment
- height as they are assumed to offset each other. b. Signal heads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
- H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- 9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10. The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- 11. The contractor is responsible for providing soil penetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.

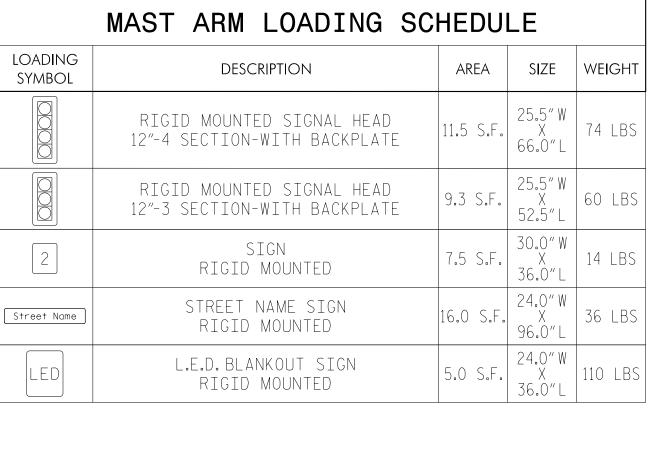


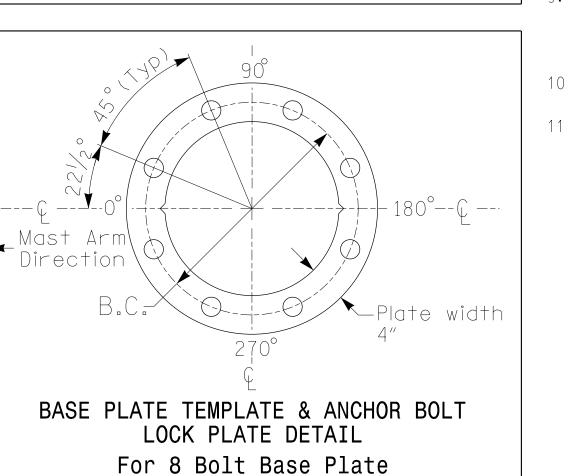
PROJECT REFERENCE NO.

I-5506

SIG-11.4







8 BOLT BASE PLATE DETAIL

See Note 6

SPECIAL NOTE

Elevation Data for Mast Arm

Attachment (H1)

345.12 ft. 345.12 ft.

+1.43 ft. | -0.34 ft

-0.80 ft.

Terminal

@ 180°

Compartmen

+1.89 ft.

ARM B

POLE RADIAL ORIENTATION

Baseline reference point at

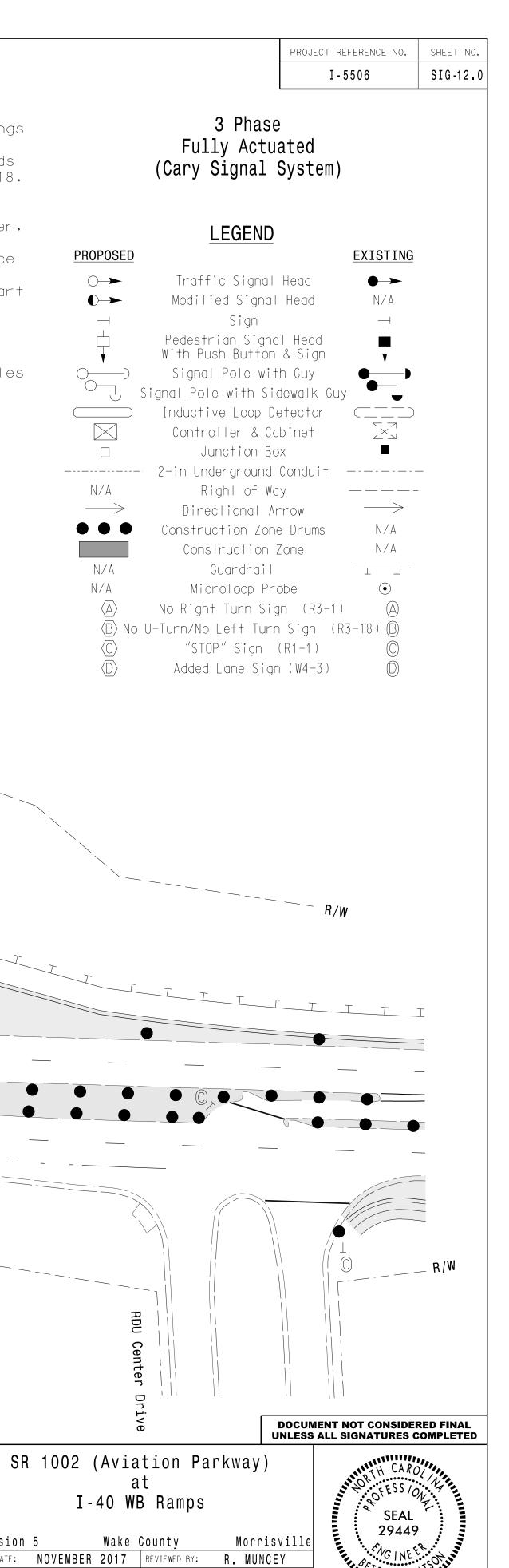
© Foundation @ ground level

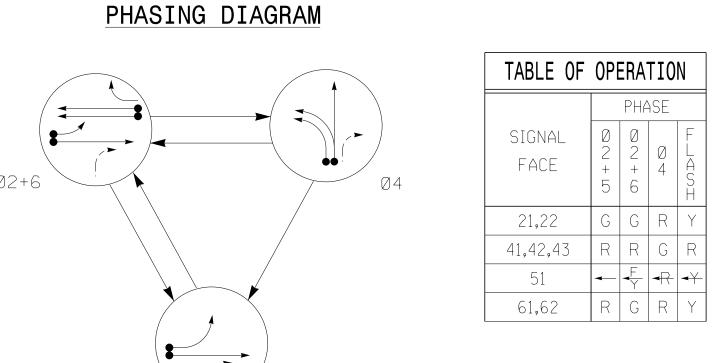
BETWEEN

ARMS

Elevation difference at High point of roadway surface

Elevation difference at





	DETECTOR UNITS										
LOOP NO	SIZE DIST. FROM ≥ Z		NEMA	EW	XISTING	TIM	ING	DET.			
LOOP NO.	(ft)	STOPBAR (ft)	TURNS	ZEX	EXISTIN	PHASE	ž	EXIST	FEATURE	TIME	TYPE
2A**	* *	300	EXIST	-	Χ	2	Χ	-	-	_	Ν
4 A	6×60	0	EXIST	-	Χ	4	Χ	_	-	-	S
4B	6×60	0	EXIST	-	Χ	4	Χ	-	-	-	S
Ε.Λ	6 - 6 0		LATOT		\ \/	5	Χ	-	DELAY	15	S
5 A	6×60	0	EXIST	-	X	2	Χ	-	DELAY	3	G
6 A	6×6	300	EXIST	-	Χ	6	Χ	-	-	-	Ν
6B	6×6	300	EXIST	-	Χ	6	Χ	-	-	-	Ν
S11	6X6	+530	EXIST	-	Χ	-	Χ	-	SYSTEM [	DETECTOR	Ν
S12	6X6	+530	EXIST	-	Χ	-	Χ	_	SYSTEM (	DETECTOR	Ν

-Temporary Pole

-L- STA. 55+32 (76' LT)



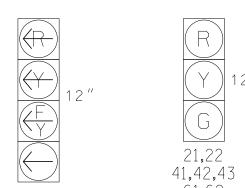
Temporary Pole —

-L- STA. 54+51 (73' LT)

+1.1% Grade

#### PHASING DIAGRAM DETECTION LEGEND

- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP) UNSIGNALIZED MOVEMENT
- $<\!\!--\!\!>$  PEDESTRIAN MOVEMENT



SIGNAL FACE I.D.

All Heads L.E.D.

12"	(Y) 12
F	G
	21,22
	41,42,43 61,62
51	01,02

SR 1002 (Aviation Parkway)

TIMING CHART ASC/3-2070LN2 CONTROLLER									
PHASE	02	02		1	Ø5		Ø6		
MINIMUM GREEN *	12	SEC.	7	SEC.	7	SEC.	12	SEC.	
VEHICLE EXT. *	6.0	SEC.	2.0	SEC.	2.0	SEC.	6.0	SEC.	
YELLOW CHANGE INT.	4.7	SEC.	3.1	SEC.	3.0	SEC.	4.7	SEC.	
RED CLEARANCE	1.4	SEC.	1.8	SEC.	2.8	SEC.	1.4	SEC.	
MAX. 1 *	90	SEC.	25	SEC.	30	SEC.	90	SEC.	
RECALL POSITION	MIN. RECALL		NONE		NONE		MIN. RECALL		
LOCK DET.	10	7	OF	F	OF	F	ON		
WALK *	_	SEC.	_	SEC.	_	SEC.	_	SEC.	
PED. CLEAR	_	SEC.	_	SEC.	_	SEC.	_	SEC.	
VOLUME DENSITY	10	1	OFF		OFF		ON		
ACTUATION B4 ADD *	_	VEH.	_	VEH.	_	VEH.	_	VEH.	
SEC. PER ACTUATION *	2.5	SEC.	_	SEC.	_	SEC.	1.5	SEC.	
MAX. INITIAL *	34	SEC.	_	SEC.	_	SEC.	34	SEC.	
TIME B4 REDUCTION *	15	SEC.	_	SEC.	_	SEC.	15	SEC.	
TIME TO REDUCE *	30	SEC.	_	SEC.	_	SEC.	30	SEC.	
MINIMUM GAP	3.0	SEC.	_	SEC.	_	SEC.	3.0	SEC.	
DUAL ENTRY	OF	F	OF	F	OFF	=	OFF	<u> </u>	
SIANUTANIEOUS GAD	0		O N		ON		ON		

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Temporary Design 1 - TMP Phase I

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Signal Upgrade

SR 1002 (Aviation Parkway)

Stantec

Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866

NOTES

1. Refer to "Roadway Standard Drawings

"Standard Specifications for Roads

and Structures" dated January 2018.

otherwise directed by the Engineer.

NCDOT" dated January 2018 and

night flashing operation unless

4. Set all detector units to presence

5. Maximum times shown in timing chart

values supersede these values.

6. Locate new cabinet so as not to

turning right on red.

Fiber Channel #: 25.

7. Cary signal system data:

are for free-run operation only.

Coordinated signal system timing

obstruct sight distance of vehicles

2. Do not program signal for late

3. Phase 5 may be lagged.

mode.

45 MPH \_-1.1% Grade

Division 5 PLAN DATE: NOVEMBER 2017 REVIEWED BY: PREPARED BY: B. WRIGHT REVIEWED BY: B. WATSON

REVISIONS INIT. DATE

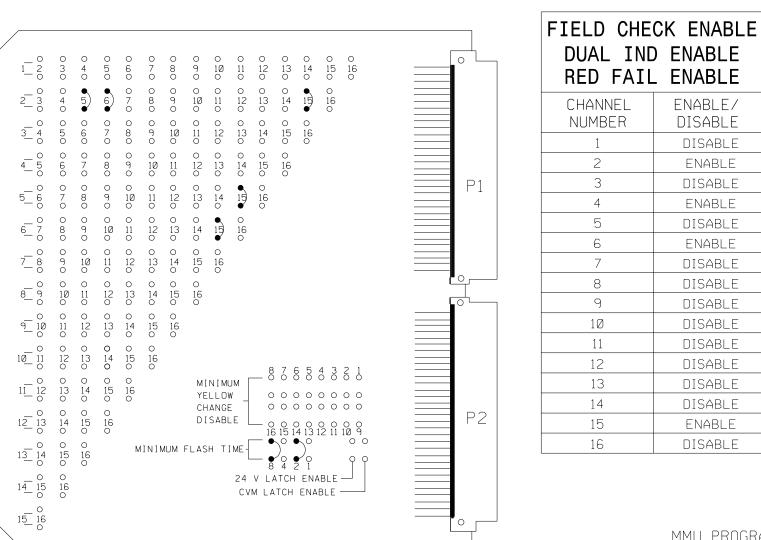
12/1/2017 SIG. INVENTORY NO.

(program card and tables as shown)

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW.

PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.



MMU PROGRAMMING CARD

L3

Ø2

2A

CH2

BIU

BIU

ON LOOP PANEL AS SHOWN

IN THE CHART BELOW

2A L3A,L3B

NU L4A,L4B

4A L5A,L5B

4B L6A,L6B

6A L7A,L7B

6B L8A,L8B

NU L9A,L9B

NU L10A,L10E

NU L11A,L11E

NU L12A,L12E

NU L13A,L13B

NU L14A,L14B

NU L15A,L15B

NU | L16A, L16B |

L1A,L1B

L2A,L2B

LOOP NO. LOOP PANEL TERMINALS

5 A

RACK

#1

RACK

# >

ADD JUMPERS FROM: L1A TO L2A, AND L1B TO L2B

L7

Ø5 Ø6 Ø4

6A

CH2

L8

Ø6

6B

4 🛆

L6

Ø 4

4B

M

SHOWN IN THE CHART BELOW

Ø 2

Ø 4

Ø 4

Ø 6

Ø 6

М

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS

ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN

FEATURE TIME(SEC)

Ø5 DELAY 15

Ø2 DELAY 3

L1

5A

CH2

L2

5A

CH2

S12

CONTROLLER

<del>\*\*</del> 8

10

1 1

12

13

14

15

DETECTOR NO.

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	ELLOW ARROW
CONFIG MODE	В
ENABLE CHANN	NEL PAIR, FYA
CH 1-13	OFF
CH 3-14	OFF
CH 5-15	ON
CH 7-16	OFF
RED/YEL INF	PUT ENABLE
CH 1	OFF
CH 3	OFF
CH 5	ON
CH 7	OFF
FLASH RATE FAULT	ON
FYA TRAP DETECT	ON

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

O T

М

IN THE CHART BELOW

LOOP NO. LOOP PANEL TERMINALS

S11 L17A,L17B

S12 L18A,L18B

NU L19A,L19B

NU L20A,L20B

NU | L21A, L21B

NU | L22A, L22B

NU L23A,L23B

NU L24A,L24B

NU L25A,L25B

NU L26A,L26B

NU L27A,L27B

NU | L28A, L28B

NU | L29A, L29B

NU L30A,L30B

NU L31A,L31B

NU L32A,L32B

M

CONTROLLER

<del>\*\*</del> 17

<del>\*\*</del> 18

20

21

22

23

24

25

26

27

28

29

30

ACCORDING TO THE SCHEDULE

SHOWN IN THE CHART BELOW

DETECTOR NO. FUNCTION FEATURE TIME(SEC)

SYSTEM

SYSTEM

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 1,3,7,8,9,10,11,12,13,14, & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-12.1

			S	SIGN	IAL	HE	AD	HOC	OK-U	JP	СНА	RT				
PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	41,42, 43	<b>★</b> 51	61,62	NU	NU	NU	NU	NU	NU	NU	NU	<b>★</b> 51	NU
RED		2R		4R	*	6R										
YELLOW		2Y		4 Y	*	6Y										
GREEN		2G		4G		6G										
RED ARROW															15R	
YELLOW ARROW															15Y	
FLASHING YELLOW ARROW															15G	
GREEN ARROW					5G											
₩																
Ķ																

- \* Denotes install load resistor. See Load Resistor
- Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2

SOFTWARE .....ECONOLITE ASC/3-2070

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16

LOAD SWITCHES USED.....2,4,5,6,15 PHASES USED.......2,4,5,6

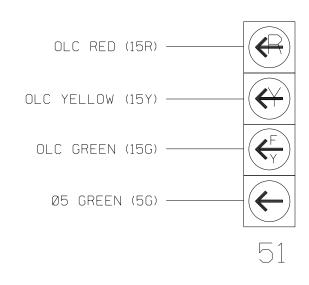
OLA....NOT USED OLB.....NOT USED

OLC.....\* OLD.....NOT USED

\* See overlap programming detail on sheet 2

# FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



#### LOAD SWITCH ASSIGNMENT DETAIL

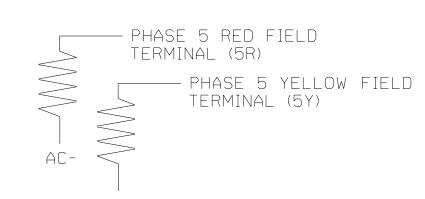
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION					
1	-					
2	Ø 2					
3	-					
4	Ø 4					
5	Ø 5					
6	Ø 6					
7	_					
8	_					
9	_					
10	-					
11	-					
12	-					
13	-					
14	-					
15	OLC					
16	-					

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-13Ø9T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 2 Temporary Design 1 - TMP Phase 1



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License No. F-0672



50 N.Greenfield Pkwy,Garner,NC 27529

# SR 1002 (Aviation Parkway) I-40 WB Ramps

Division 5 Wake County Morrisville NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL

REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

29449 12/5/2017 DATE

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

SIG. INVENTORY NO.

\*\* Detector Type - N

\* Detector Type - G

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

PROJECT REFERENCE NO. I-5506 SIG-12.2

### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |
- 3. Toggle Twice.

#### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .....PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... O

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-1309T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 2 of 2 Temporary Design 1 - TMP Phase I



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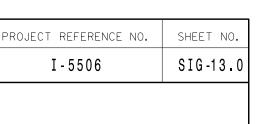
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 WB Ramps

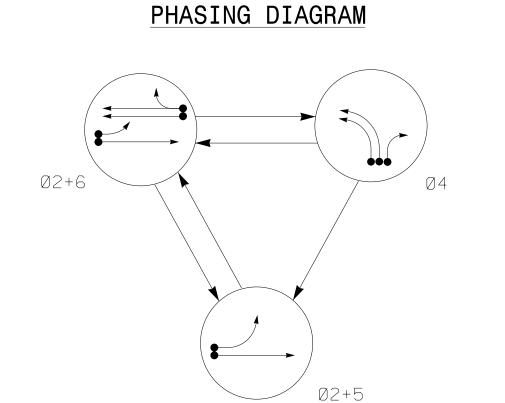
Division 5 Wake County

Morrisville REVISIONS INIT. DATE

SIG. INVENTORY NO.

PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY 750 N.Greenfield Pkwy,Garner,NC 27529





PHASING DIAGRAM DETECTION LEGEND

DETECTED MOVEMENT

 $<\!\!\!<\!\!\!--\!\!\!>$  PEDESTRIAN MOVEMENT

UNSIGNALIZED MOVEMENT

UNDETECTED MOVEMENT (OVERLAP)

TABLE OF	0PI	ERA <sup>®</sup>	TIO	N
		PHA	4SE	
SIGNAL FACE	Ø 2 + 5	Ø 2 + 6	Ø 4	FLASH
21,22	G	G	R	Y
41,42	<b>→</b> R	<del></del>	-	4
43,44	R	R	-	R
51	-	<b>-</b> F	<del></del>	<b>→</b> }
61,62	R	G	R	Y

SIGNAL FACE I.D.

All Heads L.E.D.

43,44

+1.1% Grade

				DETECT	OR UNITS	}						
1000 110	SIZE DIST. FROM ≥ SZ		NEMA	>	9 <u>N</u>	TIM	ING	DET.				
LOOP NO.	(ft)	STOPBAR (ft)	TURNS	NEW	EXISTIN	PHASE	NEW	EXISTING	FEATURE	TIME	TYPE	
2 A	6×6	300	5	X	_	2	-	Χ	-	-	N	
4 A	6×40	0	2-4-2	Χ	_	4	-	Χ	-	-	S	
4B	6×40	0	2-4-2	Χ	_	4	-	Χ	-	-	S	
4C	6×40	0	2-4-2	Х	_	4	Χ	-	DELAY	10	S	
ΕΛ	C v 10		0 4 0	2 4 2	X		5	-	Χ	DELAY	15	S
5A	6×40	0	2-4-2	X	-	2	-	Χ	DELAY	3	G	
6A	6×6	300	6	X	_	6	-	Χ	-	-	Ν	
6B	6×6	300	6	X	_	6	-	Χ	-	-	Ν	
S11	6X6	+530	EXIST	-	Χ	-	-	Χ	SYSTEM I	DETECTOR	Ν	
S12	6X6	+530	EXIST	-	Χ	-	-	X	SYSTEM I	DETECTOR	N	

### 3 Phase Fully Actuated (Cary Signal System)

#### LEGEND

<u>PROPOSED</u>		<b>EXISTING</b>
$\bigcirc$	Traffic Signal Head	<b></b>
$\bigcirc\!$	Modified Signal Head	N/A
$\overline{}$	Sign	
	Pedestrian Signal Head With Push Button & Sign	•
	Signal Pole with Guy	•
	Signal Pole with Sidewalk Guy	y
	Inductive Loop Detector	
N/A	Microloop Probe	•
	Controller & Cabinet	×
	Junction Box	
	2-in Underground Conduit	
	- Direct Bury Lead-In	
N/A	Right of Way	
$\longrightarrow$	Directional Arrow	$\longrightarrow$

# ● ● ● Construction Zone Drums N/A Construction Zone N/A Construction Zone Barrier N/A

	CONCIN GOTTON ZONO BUTTON	
N/A	Guardrail <u> </u>	
$\langle A \rangle$	No Right Turn Sign (R3-1)	$\bigcirc$
	No U-Turn/No Left Turn Sign (R3-18)	$\mathbb{B}$
$\langle \mathbb{C} \rangle$	"STOP" Sign (R1−1)	$\mathbb{C}$
$\langle \mathbb{D} \rangle$	Right Arrow "ONLY" Sign (R3-5R)	$\bigcirc$

SR 1002 (Aviation Parkway)
T MTL T T T T T T T T T T T T T T T T T

TIMING CHART ASC/3-2070LN2 CONTROLLER													
PHASE	02	2	Ø4	1	Ø5		Ø6						
MINIMUM GREEN *	12	SEC.	7	SEC.	7	SEC.	12	SEC.					
VEHICLE EXT. *	6.0	SEC.	2.0	SEC.	2.0	SEC.	6.0	SEC.					
YELLOW CHANGE INT.	4.7	SEC.	3.0	SEC.	3.0	SEC.	4.7	SEC.					
RED CLEARANCE	2.7	SEC.	2.6	SEC.	3.9	SEC.	2.7	SEC.					
MAX. 1 *	90	SEC.	25	SEC.	30	SEC.	90	SEC.					
RECALL POSITION	MIN. RE	CALL	NON	JE	NON	1E	MIN. RECALL						
LOCK DET.	10	7	OF	F	OF	F	ON						
WALK *	_	SEC.	_	SEC.	_	SEC.	_	SEC.					
PED. CLEAR	_	SEC.	_	SEC.	_	SEC.	ı	SEC.					
VOLUME DENSITY	10	7	OFF		OFF		ON						
ACTUATION B4 ADD *	_	VEH.	_	VEH.	_	VEH.	_	VEH.					
SEC. PER ACTUATION *	2.5	SEC.	_	SEC.	_	SEC.	1.5	SEC.					
MAX. INITIAL *	34	SEC.	_	SEC.	_	SEC.	34	SEC.					
TIME B4 REDUCTION *	15	SEC.	_	SEC.	_	SEC.	15	SEC.					
TIME TO REDUCE *	30	SEC.	_	SEC.	_	SEC.	30	SEC.					
MINIMUM GAP	3.0	SEC.	_	SEC.	_	SEC.	3.0	SEC.					
DUAL ENTRY	OF	F	OF	F	OFF	•	OFF						
SIMULTANEOUS GAP	10	1	40	1	ON		ON						

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

Direct Bury Lead-in

Signal Upgrade Temporary Design 2 - TMP Phase II

Stantec

45 MPH -1.1% Grade

SR 1002 (Aviation Parkway)

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License No. F-0672

Temporary Pole

-L- STA. 57+45 <u>(85' RT)</u>



Direct Bury Lead-in

**NOTES** 

1. Refer to "Roadway Standard Drawings

night flashing operation unless

4. Set all detector units to presence

5. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.

"Standard Specifications for Roads and Structures" dated January 2018.

otherwise directed by the Engineer.

NCDOT" dated January 2018 and

2. Do not program signal for late

3. Phase 5 may be lagged.

6. Cary signal system data:

Fiber Channel #: 25.

mode.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 WB Ramps

Division 5 Wake County Morrisville PLAN DATE: NOVEMBER 2017 REVIEWED BY: R. MUNCEY

PREPARED BY: B. WRIGHT REVIEWED BY: B. WATSON REVISIONS INIT. DATE 12/1/2017 sig. inventory No. 05-1309T

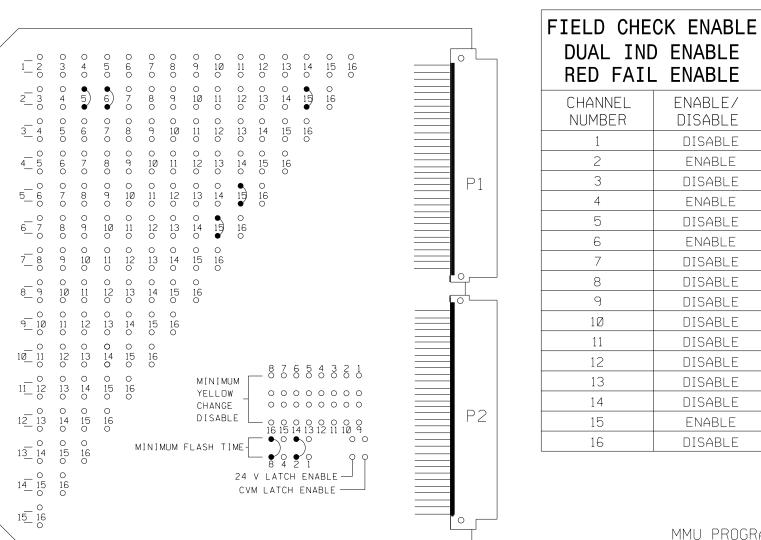
29449

(program card and tables as shown)

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW.

PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.



MMU PROGRAMMING CARD

L3

2A

CH2

BIU

BIU

RACK

#1

RACK

# >

L7

Ø5 Ø6 Ø4

6A

CH2

L8

Ø6

6B

4 🛆

L6

Ø4

4B

M

М

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS

ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN

5A

CH2

L2

5A

CH2

S12

<del>\*\*</del> 8

10

1 1

12

13

14

15

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	ELLOW ARROW								
CONFIG MODE	В								
ENABLE CHANN	NEL PAIR, FYA								
CH 1-13	OFF								
CH 3-14	OFF								
CH 5-15	ON								
CH 7-16	OFF								
RED/YEL INF	PUT ENABLE								
CH 1	OFF								
CH 3	OFF								
CH 5	ON								
CH 7	OFF								
LASH RATE FAULT	ON								
FYA TRAP DETECT	ON								

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

CH2

L14

O T

М

M

CONTROLLER

<del>\*\*</del> 17

<del>\*\*</del> 18

20

21

22

y USED

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 1,3,7,8,9,10,11,12,13,14, & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-13.1

				SIG	NAL	_ H	EAD	HC	OK-	-UP	CH.	ART	•				
PHASE	1	2	3	2	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	41,42	43,44	<b>★</b> 51	61,62	NU	NU	NU	NU	NU	NU	NU	NU	<b>★</b> 51	NU
RED		2R			4R	*	6R										
YELLOW		2Y				*	6Y										
GREEN		2G					6G										
RED ARROW				4R												15R	
YELLOW ARROW				4 Y	4 Y											15Y	
FLASHING YELLOW ARROW																15G	
GREEN ARROW				4G	4G	5G											
₩																	
Ķ																	
NU = Not L	Ised																

- \* Denotes install load resistor. See Load Resistor Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2

SOFTWARE .....ECONOLITE ASC/3-2070

\* See overlap programming detail on sheet 2

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16

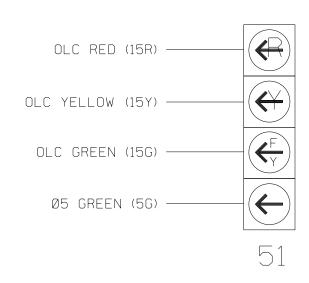
LOAD SWITCHES USED.....2,4,5,6,15 PHASES USED......2,4,5,6

OLA.....NOT USED OLB.....NOT USED OLC.....\*

OLD.....NOT USED

# FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### LOAD SWITCH ASSIGNMENT DETAIL

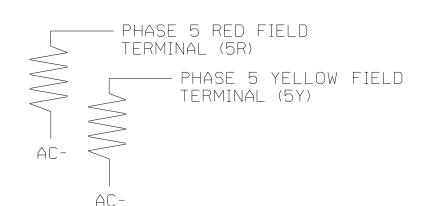
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	_
2	Ø 2
3	_
4	Ø 4
5	Ø 5
6	Ø 6
7	_
8	_
9	_
10	_
11	_
12	_
13	_
14	_
15	OLC
16	_

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-13Ø9T2 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 2 Temporary Design 2 - TMP Phase II



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50 N.Greenfield Pkwy,Garner,NC 27529

### **UNLESS ALL SIGNATURES COMPLETED** SR 1002 (Aviation Parkway) I-40 WB Ramps

Division 5 Wake County Morrisville NOVEMBER 2017 | REVIEWED BY: L. OVERN

29449 12/5/2017

DATE

**DOCUMENT NOT CONSIDERED FINAL** 

REVIEWED BY: R. MUNCEY PREPARED BY: G. SPELL INIT. DATE REVISIONS SIG. INVENTORY NO.

\*\* Detector Type - N

\* Detector Type - G

ADD JUMPERS FROM: L1A TO L2A, AND L1B TO L2B L1A,L1B 5 A L2A,L2B 2A L3A,L3B NU L4A,L4B 4A L5A,L5B 4B L6A,L6B 6A L7A,L7B

6B L8A,L8B NU L9A,L9B NU L10A,L10E NU L11A,L11E NU L12A,L12E 4C L13A,L13B

NU L14A,L14B

NU L15A,L15B

NU | L16A, L16B |

ON LOOP PANEL AS SHOWN

IN THE CHART BELOW

LOOP NO. LOOP PANEL TERMINALS

CONTROLLER DETECTOR NO. FEATURE TIME(SEC) Ø5 DELAY 15  $\emptyset$  2 | DELAY | 3 Ø 2 Ø 4 Ø 4

SHOWN IN THE CHART BELOW

Ø 6 Ø 6 Ø 4 | DELAY | 10

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

NU L19A,L19B NU L20A,L20B NU | L21A, L21B NU | L22A, L22B NU L23A,L23B NU L24A,L24B NU L25A,L25B NU L26A,L26B NU L27A,L27B NU L28A,L28B NU | L29A, L29B

IN THE CHART BELOW

LOOP NO. LOOP PANEL TERMINALS

S11 L17A,L17B

S12 L18A,L18B

23 24 25 26 27 28 29 30

NU L30A,L30B NU L31A,L31B NU L32A,L32B

ACCORDING TO THE SCHEDULE

SHOWN IN THE CHART BELOW

DETECTOR NO. FUNCTION FEATURE TIME(SEC)

SYSTEM

SYSTEM

PROJECT REFERENCE NO. SHEET NO. I-5506 SIG-13.2

### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |
- 3. Toggle Twice.

#### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .....PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH..... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O

ACTION PLAN SF BIT DISABLE..... O END PROGRAMMING

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-13Ø9T2 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 2 of 2 Temporary Design 2 - TMP Phase II



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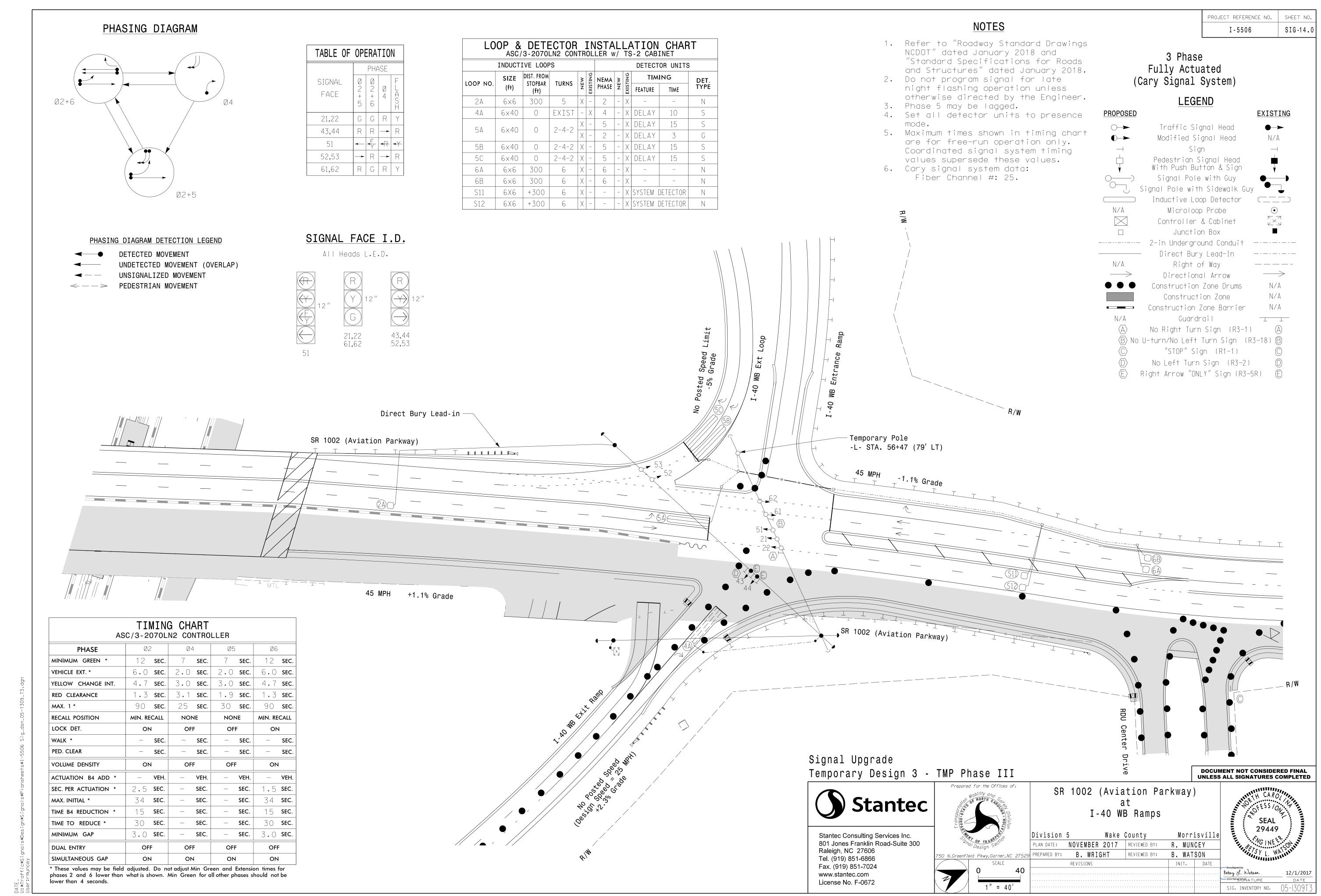
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 WB Ramps

Wake County Division 5 Morrisville PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN REVIEWED BY: R. MUNCEY PREPARED BY:

INIT. DATE REVISIONS

SIG. INVENTORY NO.

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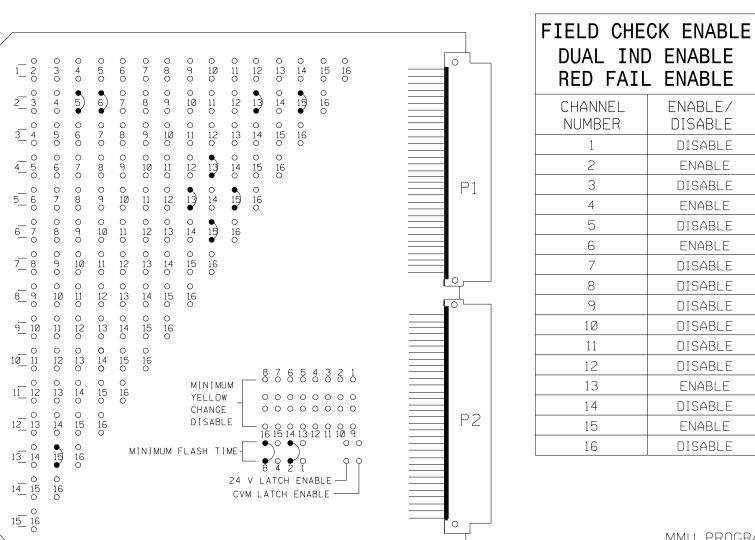


(program card and tables as shown)

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW.

PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.



MMU PROGRAMMING CARD

L3

2A

CH2

BIU

BIU

ON LOOP PANEL AS SHOWN

IN THE CHART BELOW

LOOP NO. LOOP PANEL TERMINALS

2A L3A,L3B

NU L4A,L4B

4A L5A,L5B

NU L6A,L6B

6A L7A,L7B

6B L8A,L8B

NU L9A,L9B

NU L10A,L10E

NU L11A,L11

NU L12A,L12E

5B L13A,L13B

5C L14A,L14B

NU L15A,L15B

\*\* Detector Type - N

5 A

L1A,L1B

L2A,L2B

RACK

#1

RACK

# >

ADD JUMPERS FROM: L1A TO L2A, AND L1B TO L2B

L7

Ø5 Ø6 Ø4

6A

CH2

6B

4 🛆

L6

NOT

M

SHOWN IN THE CHART BELOW

Ø 2

Ø 6

Ø 6

M

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS

ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN

FEATURE TIME(SEC)

Ø5 DELAY 15

Ø4 DELAY 10

Ø 5 | DELAY | 15

Ø5 DELAY 15

Ø2 DELAY 3

5A

CH2

L2

5A

CH2

S12

CONTROLLER

<del>\*\*</del> 8

10

1 1

12

13

14

DETECTOR NO.

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	ELLOW ARROW								
CONFIG MODE	В								
ENABLE CHANN	NEL PAIR, FYA								
CH 1-13	OFF								
CH 3-14	OFF								
CH 5-15	ON								
CH 7-16	OFF								
RED/YEL INF	PUT ENABLE								
CH 1	OFF								
CH 3	OFF								
CH 5	ON								
CH 7	OFF								
LASH RATE FAULT	ON								
FYA TRAP DETECT	ON								

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

5B

CH2

L14

Ø5

O T

M

IN THE CHART BELOW

LOOP NO. LOOP PANEL TERMINALS

S11 L17A,L17B

S12 L18A,L18B

NU L19A,L19B

NU L20A,L20E

NU L21A,L21B

NU | L22A, L22B

NU L23A,L23B

NU L24A,L24B

NU L25A,L25B

NU L26A,L26B

NU L27A,L27E

NU L28A,L28B

NU | L29A, L29B

NU L30A,L30B

NU L31A,L31B

M

CONTROLLER

<del>\*\*</del> 17

<del>\*\*</del> 18

20

21

22

23

24

25

26

27

28

29

30

ACCORDING TO THE SCHEDULE

SHOWN IN THE CHART BELOW

DETECTOR NO. FUNCTION FEATURE TIME(SEC)

SYSTEM

SYSTEM

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 1,3,7,8,9,10,11,12,14, & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-14.1

SIGNAL HEAD HOOK-UP CHART																
PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	43,44	<b>★</b> 51	61,62	NU	NU	NU	NU	NU	NU	52,53	NU	<b>★</b> 51	NU
RED		2R		4R	*	6R							13R			
YELLOW		2Y			*	6Y										
GREEN		2G				6G										
RED ARROW															15R	
YELLOW ARROW				4 Y									13Y		15Y	
FLASHING YELLOW ARROW															15G	
GREEN ARROW				4G	5G								13G			
₩																
×																
NU = Not L	Jsed					1	I	I	1	I.	I .	I	1	I	1	I

- \* Denotes install load resistor. See Load Resistor
- Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16

LOAD SWITCHES USED.....2,4,5,6,13,15 PHASES USED.......2,4,5,6 OLA.....4+5

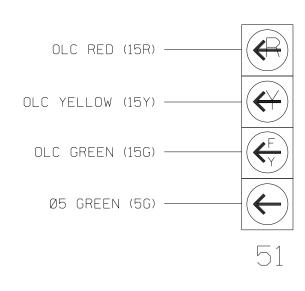
OLC.....\* OLD.....NOT USED

OLB....NOT USED

\* See overlap programming detail on sheet 2

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



#### LOAD SWITCH ASSIGNMENT DETAIL

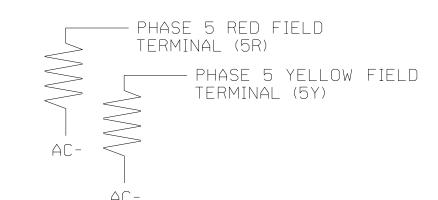
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	_
2	Ø 2
3	_
4	Ø 4
5	Ø 5
6	Ø 6
7	_
8	_
9	_
10	_
11	_
12	_
13	OLA
14	_
15	OLC
16	_

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-13Ø9T3 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

REVISIONS

Electrical Detail - Sheet 1 of 2 Temporary Design 3 - TMP Phase III



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### UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 WB Ramps

Division 5 Wake County Morrisville NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY

29449 INIT. DATE

**DOCUMENT NOT CONSIDERED FINAL** 

12/5/2017 DATE SIG. INVENTORY NO.

\* Detector Type - G

NU | L16A, L16B | NU L32A,L32B NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

PROJECT REFERENCE NO. SHEET NO. I-5506 SIG-14.2

### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

#### OVERLAP A

```
Select TMG VEH OVLP [A] and 'NORMAL'
TMG VEH OVLP...[A] TYPE: .....NORMAL
 PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
LAG GRN 0.0 YEL 0.0 RED 0.0
                       Toggle Twice
```

#### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .....PPLT FYA
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6
FLASHING ARROW OUTPUT....CH15 ISOLATE
DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE..... 0
           END PROGRAMMING
```

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1309T3 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 2 of 2 Temporary Design 3 - TMP Phase III



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



50 N.Greenfield Pkwy,Garner,NC 27529

SR 1002 (Aviation Parkway) I-40 WB Ramps

Wake County Division 5 PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY INIT. DATE REVISIONS

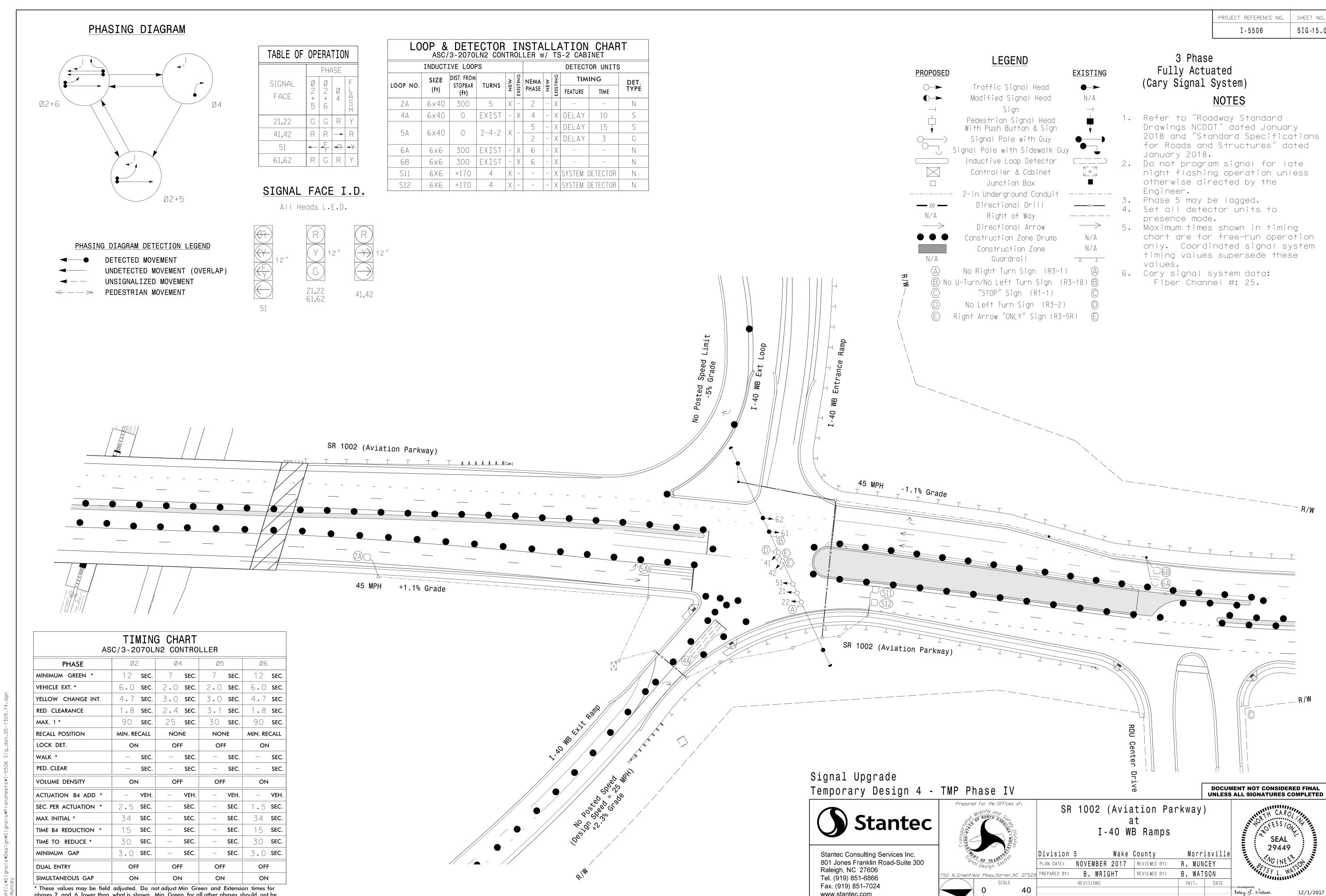
Morrisville

SIG. INVENTORY NO.

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phases 2 and 6 lower than what is shown. Min Green for all other phases should not be

lower than 4 seconds.



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(program card and tables as shown)

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW.

PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

L7

6A

CH2

L8

6B

4 A

L6

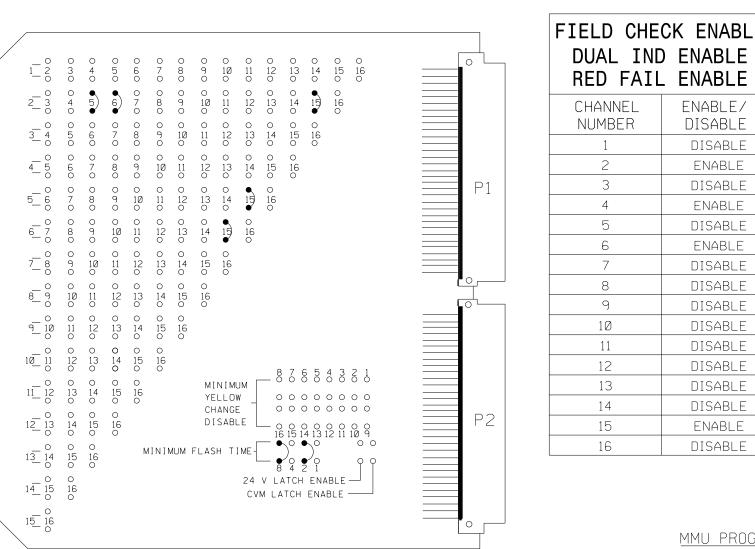
NOT

E M P

E M P

E M P

Ø5 Ø6 Ø4



MMU PROGRAMMING CARD

L3

2A

CH2

M P

BIU

BIU

5A

CH2

L2

5A

CH2

L18

RACK

#1

RACK

# >

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON
FLASHING YE	IIOW ARROW

FLASHING YE	ELLOW ARROW								
CONFIG MODE	В								
ENABLE CHANN	NEL PAIR, FYA								
CH 1-13	OFF								
CH 3-14	OFF								
CH 5-15	ON								
CH 7-16	OFF								
RED/YEL INF	PUT ENABLE								
CH 1	OFF								
CH 3	OFF								
CH 5	ON								
CH 7	OFF								
FLASH RATE FAULT	ON								
FYA TRAP DETECT	ON								

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

M P

E M P

E M P

ENABLE/

DISABLE

DISABLE

ENABLE

DISABLE

ENABLE

DISABLE

ENABLE

DISABLE

DISABLE

DISABLE

DISABLE

DISABLE DISABLE

DISABLE

DISABLE

ENABLE

DISABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 1,3,7,8,9,10,11,12,13,14, & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-15.1

	SIGNAL HEAD HOOK-UP CHART															
PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	41,42	<b>★</b> 51	61,62	NU	NU	NU	NU	NU	NU	NU	NU	<b>★</b> 51	NU
RED		2R		4R	*	6R										
YELLOW		2Y			*	6Y										
GREEN		2G				6G										
RED ARROW															15R	
YELLOW ARROW				4 Y											15Y	
FLASHING YELLOW ARROW															15G	
GREEN ARROW				4G	5G											
*																
Ķ																
NU = Not [	Ised	-				1	1	1	1	1	ı			1	1	

- \* Denotes install load resistor. See Load Resistor
- Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### EQUIPMENT INFORMATION

CONTROLLER.....2070LN2 CABINET .....TS-2

SOFTWARE .....ECONOLITE ASC/3-2070

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16

LOAD SWITCHES USED.....2,4,5,6,15 PHASES USED......2,4,5,6 OLA.....NOT USED

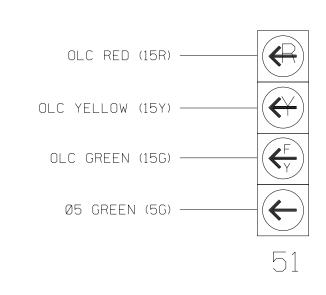
OLC....\* OLD.....NOT USED

OLB.....NOT USED

\* See overlap programming detail on sheet 2

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



#### LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION	
1	-	
2	Ø 2	
3	-	
4	Ø 4	
5	Ø 5	
6	Ø 6	
7	-	
8	-	
9	-	
10	-	
11	-	
12	-	
13	-	
14	-	
15	OLC	
16	-	
UNUSED LOAD SV	VITCH CHANNE	

SHALL BE DISABLED IN CONTROLLER PROGRAMMING

#### LOAD RESISTOR INSTALLATION DETAIL (install resistors as shown)

- PHASE 5 RED FIELD

TERMINAL (5Y)

TERMINAL (5R) — PHASE 5 YELLOW FIELD

ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (mın)

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-13Ø9T4 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 2 Temporary Design 4 - TMP Phase IV



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50 N.Greenfield Pkwy,Garner,NC 27529

#### SR 1002 (Aviation Parkway) at I-40 WB Ramps

Division 5 Wake County Morrisville NOVEMBER 2017 REVIEWED BY: L. OVERN REVIEWED BY: R. MUNCEY PREPARED BY: G. SPELL REVISIONS

29449

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

INIT. DATE 12/5/2017 DATE SIG. INVENTORY NO.

		TO TERMINALS				LER DET				ERMINALS				
		ANEL AS SHOWN Chart Below				THE SCHE CHART B		LUUP F [N THE		AS SHOWN BELOW		ING TO 1 IN THE		
	LOOP NO.	LOOP PANEL	CONTROL				MING	LOOP NO	LOOP	PANEL	CONTROLLER			MING
MPERS FROM:	LUUF NU.	TERMINALS L1A,L1B	DETECTO	1 - 11	NCTION		TIME(SEC)	S11	IERN	INALS	DETECTOR NO.	FUNCTION	FEATURE	1
L2A, AND	5 A	L2A,L2B	· 1	9	ø 5	DELAY	15	S12		,L17B ,L18B	<del>**</del> 17	SYSTEM		
TO L2B	2 A	L3A,L3B	* 2	/	Ø 2	DELAY	3	NU		,L19B	<del>**</del> 18	SYSTEM		
	NU	L4A,L4B	** 3	9	Ø 2			NU		,L20B	19			
	4 A	L5A,L5B	• 4		· -	DELAY	4.0	NU	L21A	,L21B	20			
	NU	L6A,L6B	· 5	· · · · · · · · · · · · · · · · · · ·	<b>5</b> 4	DELAY	10	NU	L22A	,L22B	21			
	6 A	L7A,L7B	** 7		ø 6			NU		,L23B	23			
	6B	L8A,L8B	** 8		ø 6			NU		,L24B	24			
	NU	L9A,L9B	. 9	'				NU		,L25B	25			
-	NU NU	L10A,L10B L11A,L11B	. 10					NU		,L26B ,L27B	- 26			
	NU	L12A,L12B	· 11					NU		,L27B	- 27			
	NU	L13A,L13B	12					NU		,L29B	28			
	NU	L14A,L14B	13					NU		,L30B	29	,		
	NU	L15A,L15B	• 14					NU	L31A	,L31B	30			
	NU	L16A,L16B	15					NU	L32A	,L32B	31			

\* Detector Type - G \*\* Detector Type - N

PROJECT REFERENCE NO. SHEET NO. SIG-15.2

### ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS
- 3. Toggle Twice.

#### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: ....PPLT FYA

PROTECTED LEFT TURN.... PHASE 5

OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT....CH15 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE.......

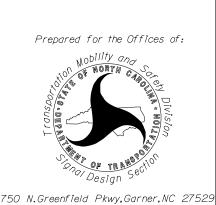
END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-1309T4
DESIGNED: NOV 2017
SEALED: 12-01-2017
REVISED: N/A

Electrical Detail - Sheet 2 of 2 Temporary Design 4 - TMP Phase IV



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SR 1002 (Aviation Parkway)
at
I-40 WB Ramps

I-40 WB Ramps

Division 5 Wake County Morrisville
PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN

PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN

PREPARED BY: REVIEWED BY: R. MUNCEY

REVISIONS INIT. DATE

REVIEWED BY:

REVIEWED BY:

R. MUNCEY

INIT. DATE

Docusigned by:

Betary L. Watson

12/5/2017

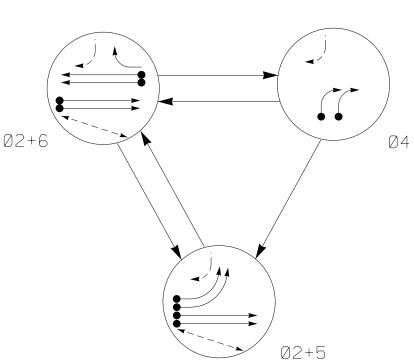
SIGNATURE

DATE

SIG. INVENTORY NO. ()5-|3()9

U:\*Traffic\*Signals\*Design\*Signals\*Electrical Details\*I-5506 sm\_ele\_05-1309\_T4-20f2





DETECTED MOVEMENT

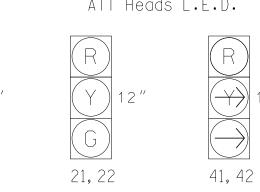
 $<\!\!--\!\!>$  PEDESTRIAN MOVEMENT

UNSIGNALIZED MOVEMENT

TABLE OF	OPE	ERA	TIO	N				
	PHASE							
SIGNAL FACE	®N+15	Ø2+6	Ø 4	FLASH				
21,22	G	G	R	Y				
41,42	R	R	-	R				
51,52	¥	<b>→</b> R	#	<del></del>				
61,62	R	G	R	Y				
P21,P22	W	W	DW	DRK				

# SIGNAL FACE I.D.

All Heads L.E.D.



L0	LOOP & DETECTOR INSTALLATION CHART ASC/3-2070LN2 CONTROLLER w/ TS-2 CABINET											
	DETECTOR UNITS											
	SIZE	DIST. FROM		≥	<u>N</u>	NEMA	>	S S	TIMING DET.			
LOOP NO.	(ft)	STOPBAR (ft)	TURNS	NEW	EXISTIN	PHASE		EXISTING	FEATURE	TIME	TYPE	
2 A	6×6	300	5	Χ	-	2	-	X	-	-	N	
2B	6×6	300	5	Х	-	2	Χ	-	-	-	N	
4 A	6×40	0	2-4-2	Х	-	4	-	Х	DELAY	15	S	
4B	6×40	0	2-4-2	X	-	4	_	Χ	DELAY	15	S	
5 A	6×40	0	2-4-2	X	-	5	_	Х	-	-	S	
5B	6×40	0	2-4-2	Χ	-	5	-	Χ	-	-	S	
6A	6×6	300	6	Χ	-	6	-	Х	-	-	Ν	
6B	6×6	300	6	Х	-	6	-	X	-	-	N	
S11	6×6	+160	4	Χ	-	-	-	Х	SYSTEM	DETECTOR	N	
S12	6×6	+160	4	X	-	-	-	Х	SYSTEM	DETECTOR	Ν	

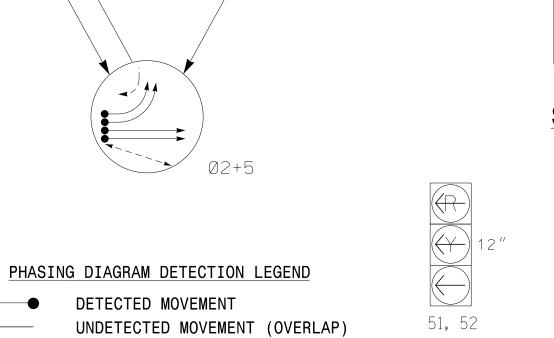
### 3 Phase Fully Actuated (Cary Signal System)

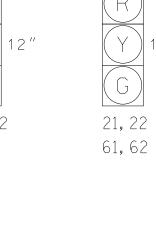
#### NOTES

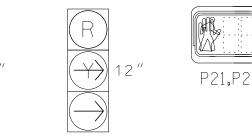
- 1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- 2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- 3. Phase 5 may be lagged.
- 4. Set all detector units to presence mode. 5. Maximum times shown in timing chart are for free-run operation only. Coordinated
- these values. 6. Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- 7. Program pedestrian heads to countdown the flashing "DON'T WALK" time only.

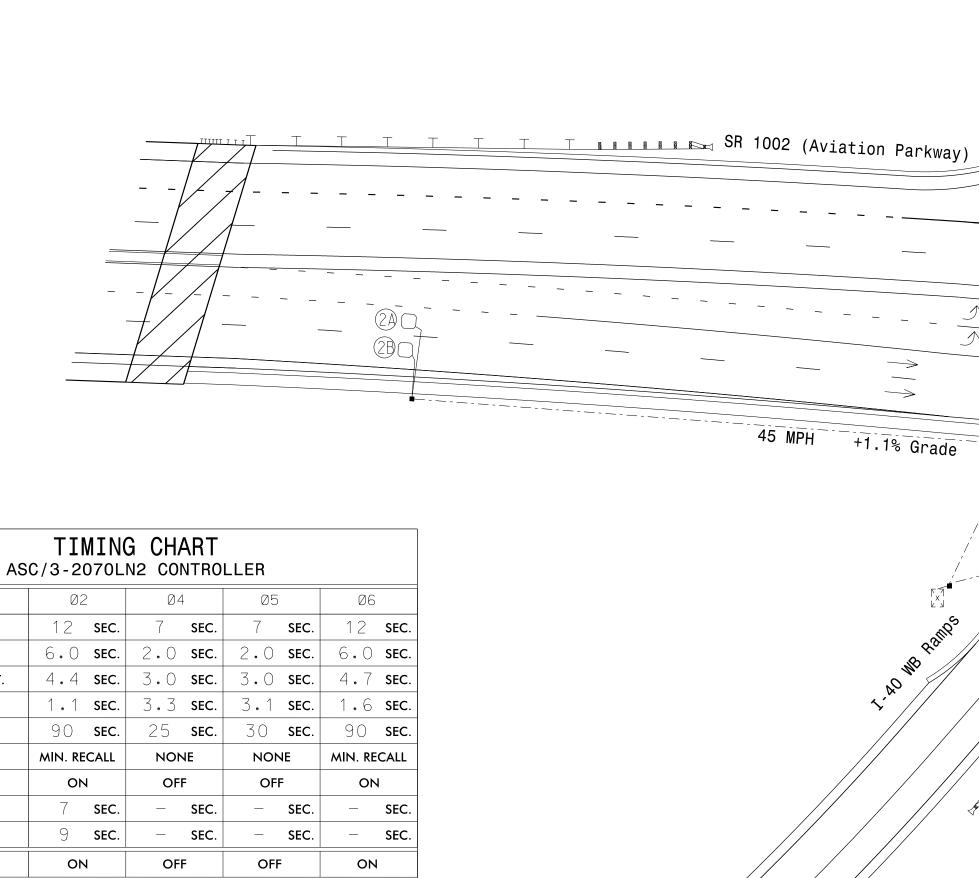
signal system timing values supersede

- 8. Pedestrian pedestals are conceptual and shown for reference only. See 2018 NCDOT Roadway Standard Drawings 1705.04 Sheets 1-3 for push button location details.
- 9. Cary signal system data: Fiber Channel #: 25.









1.5 **SEC**.

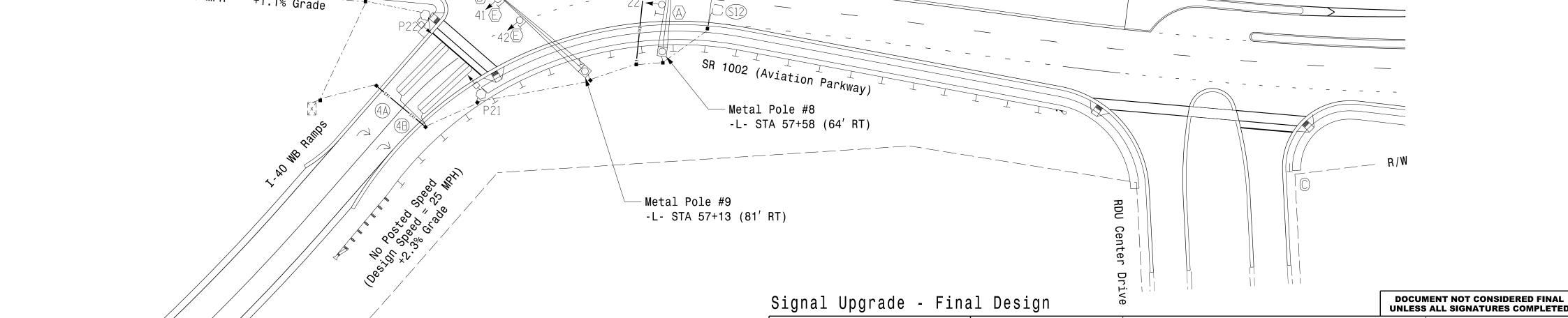
34 **SEC**.

15 **SEC**.

30 **SEC**.

OFF

SEC.



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LEGEND

Traffic Signal Head

Modified Signal Head

Sign

Pedestrian Signal Head

With Push Button & Sign

Signal Pole with Sidewalk Guy

Controller & Cabinet

Junction Box

Directional Arrow

Metal Pole with Mastarm

Curb Ramp

Guardrail

Type II Signal Pedestal

No Right Turn Sign (R3-1)

(B) No U-Turn/No Left Turn Sign (R3-18) (B)

"STOP" Sign (R1-1)

No Left Turn Sign (R3-2)

⟨E⟩ Right Arrow "ONLY" Sign (R3-5R) (E)

No U-Turn (R3-4)

Right of Way

2-in Underground Conduit -----

Inductive Loop Detector

Signal Pole with Guy

**PROPOSED** 

 $\bigcirc$ 

N/A

N/A

N/A

Metal Pole #7 -L- STA 56+41

-1.1% Grade

(62'LT)

**EXISTING** 

**-**

N/A

\_\_\_\_

SR 1002 (Aviation Parkway) I-40 WB Ramps

Division 5 Wake County Morrisville NOVEMBER 2017 REVIEWED BY: R. MUNCEY PREPARED BY: B. WRIGHT REVIEWED BY: B. WATSON

29449 12/1/2017 Betsy L. Watson

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INIT. DATE SIG. INVENTORY NO.

PHASE

12 **SEC**.

4.4 **SEC**.

1.1 SEC.

90 **SEC**.

MIN. RECALL

ON

7 **SEC**.

9 **SEC**.

ON

1.5 **SEC**.

34 **SEC**.

15 **SEC**.

30 **SEC**.

3.0 **SEC**.

OFF

OFF

MINIMUM GREEN

RED CLEARANCE

RECALL POSITION

**VOLUME DENSITY** 

MAX. INITIAL \*

ACTUATION B4 ADD

SEC. PER ACTUATION

TIME B4 REDUCTION

TIME TO REDUCE

MINIMUM GAP

DUAL ENTRY

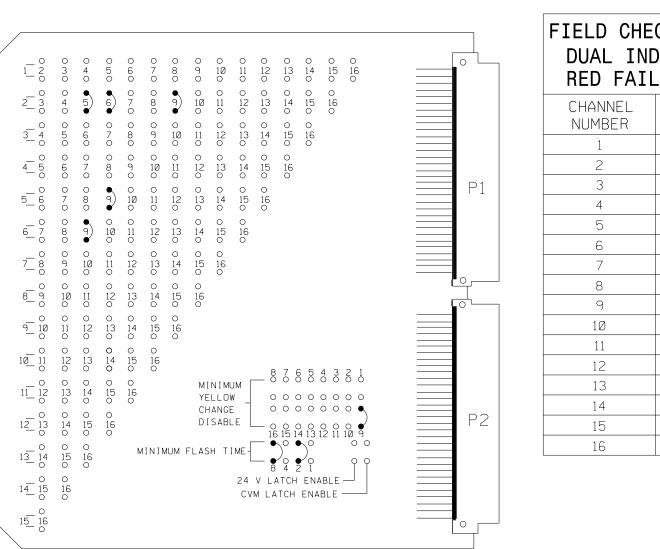
PED. CLEAR

YELLOW CHANGE INT.

VEHICLE EXT. \*

SIMULTANEOUS GAP \* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be

(program card and tables as shown)



MMU PROGRAMMING CARD

L3

ETAIL		OPTION
as shown	<del></del>	RECURRENT PL
us snowi	<i>(</i> )	WALK DISAE
		LOG CVM FAL
LD CHE	CK ENABLE	EXTERN WATCH
UAI TNE	) ENABLE	24V-2=12VI
	ENABLE	PGM CARD MEN
		LEDguard
HANNEL	ENABLE/	FORCE TYPE
UMBER	DISABLE	TYPE12-SD
1	DISABLE	VM 3x/Day La
2	ENABLE	
3	DISABLE	
4	ENABLE	FLASHING
5	ENABLE	CONFIG MO
6	ENABLE	ENABLE C
7	DISABLE	CH 1-13

DISABLE

ENABLE

DISABLE

DISABLE DISABLE

DISABLE

DISABLE

DISABLE

DISABLE

VII 9X7 Bdg Ed tell	011					
FLASHING YE	LLOW ARROW					
CONFIG MODE	В					
ENABLE CHANN	VEL PAIR, FYA					
CH 1-13	OFF					
CH 3-14	OFF					
CH 5-15	OFF					
CH 7-16	OFF					
RED/YEL INF	PUT ENABLE					
CH 1	OFF					
CH 3	OFF					
CH 5	OFF					
CH 7	OFF					
FLASH RATE FAULT	OFF					
FYA TRAP DETECT	OFF					

UNIT OPTIONS

SETTING

ON

OFF

ON

OFF

OFF

ON

ON

OFF OFF

MMU PROGRAMMING NOTE

ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING
IS ENABLED FOR ALL CHANNELS.

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 1,3,7,8,10,11,12,13,14,15 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all flash transfer relays are in
- 3. Program controller to start up in phases 2 Walk and 6 Green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. SHEET NO. SIG-16.1

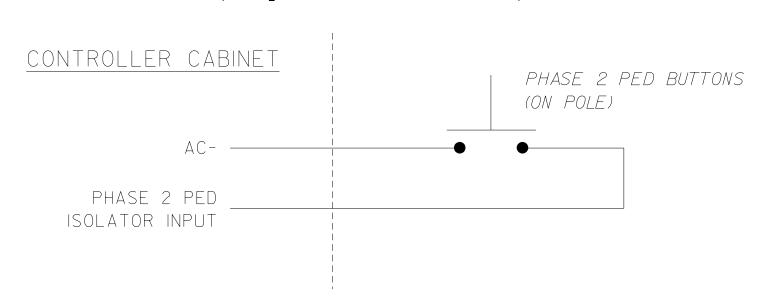
	SIGNAL HEAD HOOK-UP CHART															
PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	NU	21,22	NU	41,42	51,52	61,62	NU	NU	P21, P22	NU	NU	NU	NU	NU	NU	NU
RED		2R		4R		6R										
YELLOW		2Y				6Y										
GREEN		2G				6G										
RED ARROW					5R											
YELLOW ARROW				4 Y	5Y											
FLASHING YELLOW ARROW																
GREEN ARROW				4G	5G											
₩									9R							
於									9G							
	Jsed															

#### EQUIPMENT INFORMATION

CONTROLLER.....2070LN2

#### PEDESTRIAN PUSH-BUTTON WIRING DETAIL

(wire push-buttons as shown below)



#### Ø5 Ø6 Ø4 RACK 2A 6A 5A 4 🛆 BIU #1 CH2 CH2 CH2 L2 L6 L4 L8 Ø5 Ø4 Ø6 2B 5B 6B 4B

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW.

PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

| RACK |     | S<br>L<br>O<br>T      | CH1<br>L17<br>SYS<br>DET<br>S11 | S<br>L<br>O<br>T      |  |
|------|-----|-----------------------|---------------------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|--|
| #2   | BIU | E<br>M<br>P<br>T<br>Y | CH2<br>L18<br>SYS<br>DET<br>S12 | E<br>M<br>P<br>T<br>Y |  |

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW

IN THE C	CHART BELOW
LOOP NO.	LOOP PANEL TERMINALS
5 A	L1A,L1B
5B	L2A,L2B
2 A	L3A,L3B
2B	L4A,L4B
4 A	L5A,L5B
4B	L6A,L6B
6A	L7A,L7B
6B	L8A,L8B
NU	L9A,L9B
NU	L10A,L10B
NU	L11A,L11B
NU	L12A,L12B
NU	L13A,L13B
NU	L14A,L14B
NU	L15A,L15B
NU	L16A,L16B

PROGRAM CONTROLLER DETECTORS
ACCORDING TO THE SCHEDULE
SHOWN IN THE CHART BELOW

L7

SHOWN	IN THE	CHART E	SELOW
CONTROLLER	FUNCTION		MING
DETECTOR NO.		FEATURE	TIME(SEC)
1	Ø 5		
2	Ø 5		
<del>**</del> 3	Ø 2		
** 4	Ø 2		
. 5	Ø 4	DELAY	15
. 6	Ø 4	DELAY	15
<del>**</del> 7	Ø 6		
<del>**</del> 8	Ø 6		
. 9			
10			
· 11			
12			
13			
14			
15			
1.0			

WIRE LOOPS TO TERMINALS
ON LOOP PANEL AS SHOWN
IN THE CHART BELOW

IN THE (	CHART BELOW
LOOP NO.	LOOP PANEL TERMINALS
S11	L17A,L17B
S12	L18A,L18B
NU	L19A,L19B
NU	L20A,L20B
NU	L21A,L21B
NU	L22A,L22B
NU	L23A,L23B
NU	L24A,L24B
NU	L25A,L25B
NU	L26A,L26B
NU	L27A,L27B
NU	L28A,L28B
NU	L29A,L29B
NU	L30A,L30B
NU	L31A,L31B
NU	L32A,L32B

PROGRAM CONTROLLER DETECTORS
ACCORDING TO THE SCHEDULE
SHOWN IN THE CHART BELOW

CONTROLLER	FUNCTION	ΤI	MING
DETECTOR NO.	FONCTION	FEATURE	TIME(SEC)
<del>**</del> 17	SYSTEM		
<del>**</del> 18	SYSTEM		
19			
20			
21			
22			
23			
24			
25			
26			
27			
28			
29			
30			
. 31			
32			

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

\_\_\_\_

# LOAD SWITCH ASSIGNMENT DETAIL

(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION
1	-
2	Ø 2
3	-
4	Ø 4
5	Ø 5
6	Ø 6
7	-
8	-
9	Ø2 PED
10	-
11	-
12	-
13	-
14	-
15	-
16	_
	TITCH CHANNE

--Ø 2 PED -

SWITCH CHANNELS

UNUSED LOAD SWITCH CHANNELS
SHALL BE DISABLED IN CONTROLLER
PROGRAMMING
Stantec C
801 Jones
Raleigh, N

#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-13Ø9
DESIGNED: NOV 2Ø17
SEALED: 12-Ø1-2Ø17
REVISED: N/A

Electrical Detail - Final Design



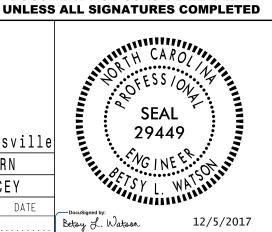
Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672



50 N.Greenfield Pkwy,Garner,NC 27529

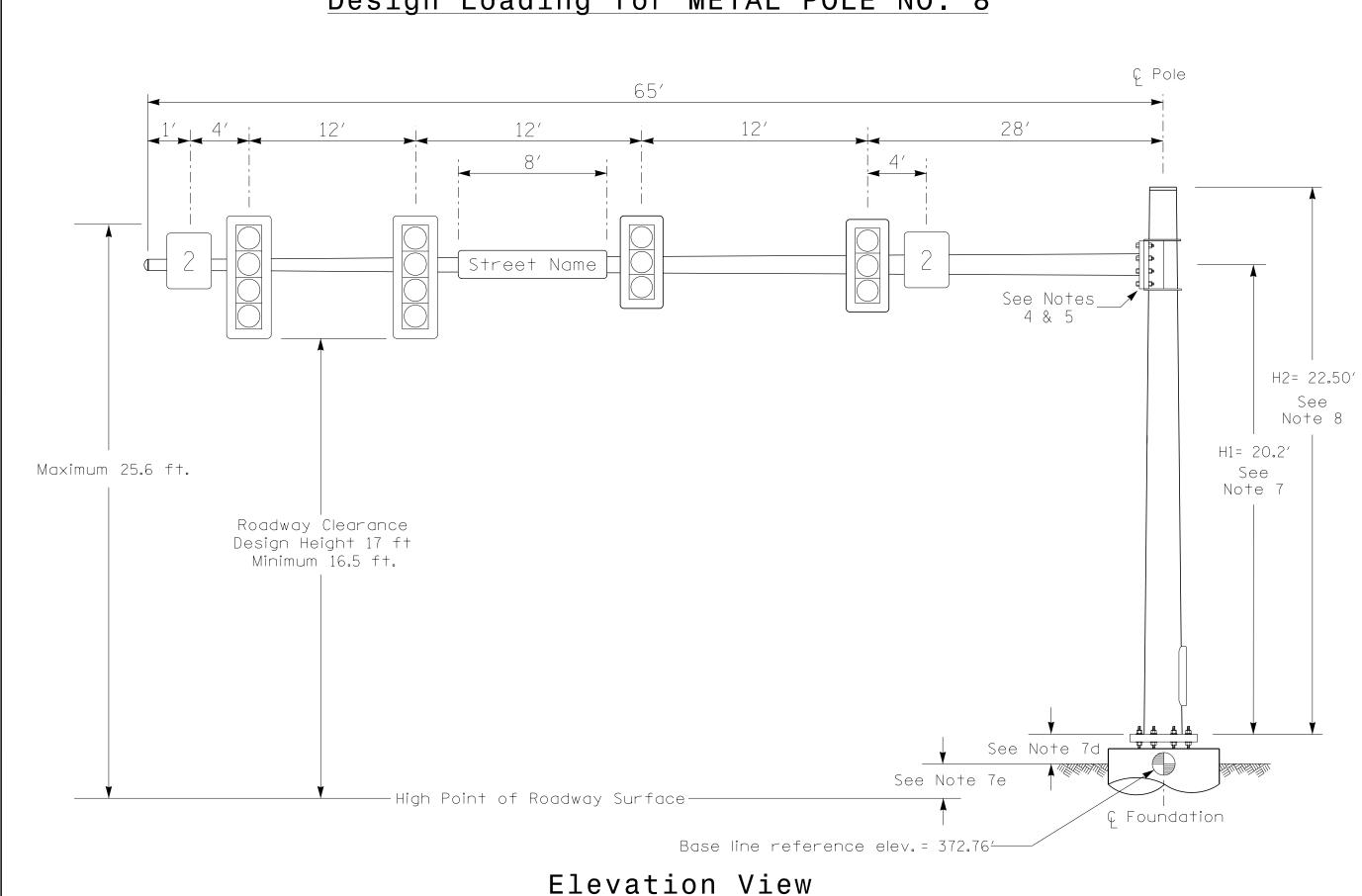
SR 1002 (Aviation Parkway) at I-40 WB Ramps

Division 5 Wake County Morrisville
PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN
PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY
REVISIONS INIT. DATE



**DOCUMENT NOT CONSIDERED FINAL** 

\*\* Detector Type - N

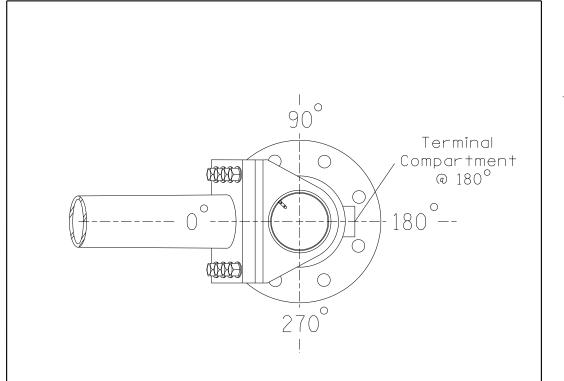


SPECIAL NOTE

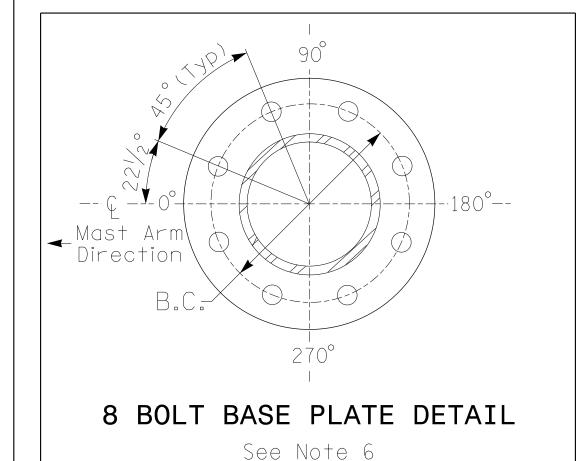
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

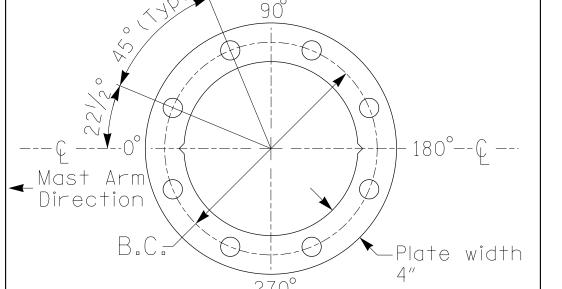
#### Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Pole 7	Pole 8
Baseline reference point at © Foundation @ ground level	369.25 ft.	372.76 ft.
Elevation difference at High point of roadway surface	+3.19 ft.	+0.39 ft.
Elevation difference at Edge of travelway or face of curb	+2.74 ft.	+1.18 ft.



#### POLE RADIAL ORIENTATION





BASE PLATE TEMPLATE & ANCHOR BOLT LOCK PLATE DETAIL

For 8 Bolt Base Plate

METAL POLE No. 7 and 8

<b>.</b> 0			I - 550	06	SIG-16.2
IG SC	HEDUI	LE			
	AREA	SIZE	WEIGHT		
EAD LATF	9.3 S.F.	25.5" W X	60 LBS		

PROJECT REFERENCE NO. SHEET NO.

	MAST ARM LOADING SC	HEDU	LE	
LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5″W X 52.5″L	60 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5″W X 66.0″L	74 LBS
2	SIGN RIGID MOUNTED	7.5 S.F.	30.0"W X 36.0"L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0" W X 96.0"L	36 LBS

#### **NOTES**

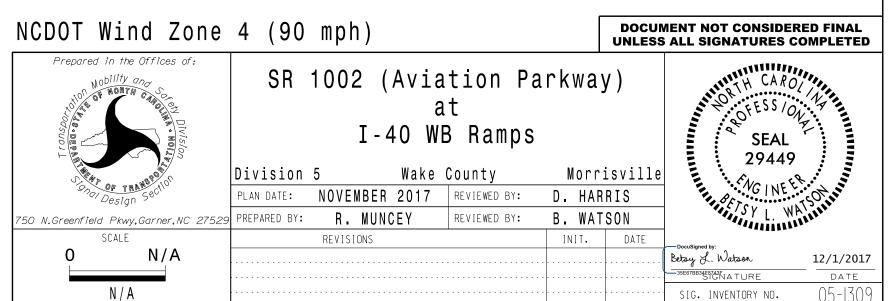
#### DESIGN REFERENCE MATERIAL

- 1. Design the traffic signalstructure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signalproject plans and specialprovisions.
- The NCDOT "MetalPole Standards" located at the following NCDOT website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

#### DESIGN REQUIREMENTS

- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
   Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch  $\times$  60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground leveland the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
- H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- 9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10. The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- 11. The contractor is responsible for providing soilpenetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



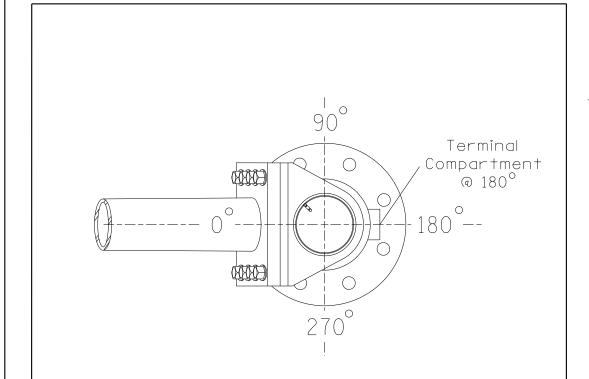


#### SPECIAL NOTE

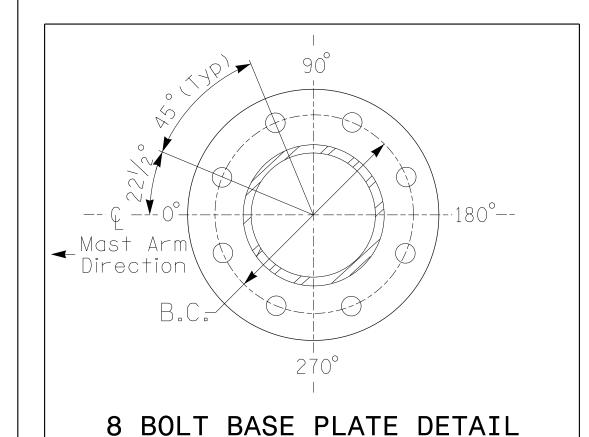
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

#### Elevation Data for Mast Arm Attachment (H1)

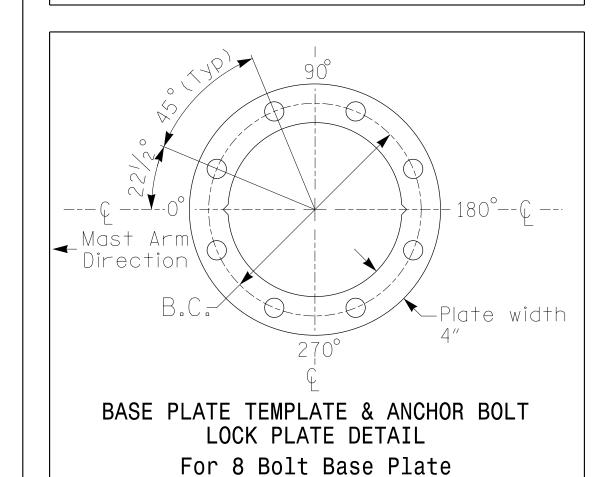
Elevation Differences for:	Pole 9	
Baseline reference point at £ Foundation @ ground level	367.99 ft.	
Elevation difference at High point of roadway surface	+4.99 ft.	
Elevation difference at Edge of travelway or face of curb	+4.27 ft.	



#### POLE RADIAL ORIENTATION







METAL POLE No. 9

I - 5506	SIG-16.3
PROJECT REFERENCE NO.	SHEET NO.

	MAST ARM LOADING SC	HEDU	LE	
LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5″W X 52.5″L	60 LBS
2	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0"L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0"W X 96.0"L	36 LBS

#### NOTES

#### DESIGN REFERENCE MATERIAL

- 1. Design the traffic signalstructure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signalproject plans and specialprovisions.
- The NCDOT "MetalPole Standards" located at the following NCDOT website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

#### DESIGN REQUIREMENTS

horizontal when fully loaded.

- 2. Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- 3. Design all signal supports using stress ratios that do not exceed 0.9.4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions:

  a. Mast arm slope and deflection are not considered in determining the arm attachment
- height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
  d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed
- foundation ground leveland the high point of the roadway.
  8. The pole manufacturer will determine the total height (H2) of each pole using the greater of
- the following:

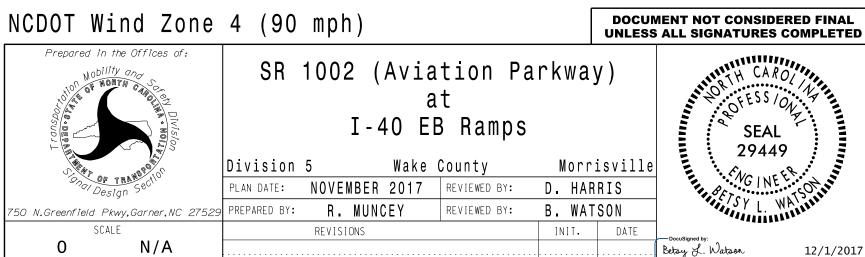
   Mact arm attachment height (III) plus 2 feet or
- Mast arm attachment height (H1) plus 2 feet, or

N/A

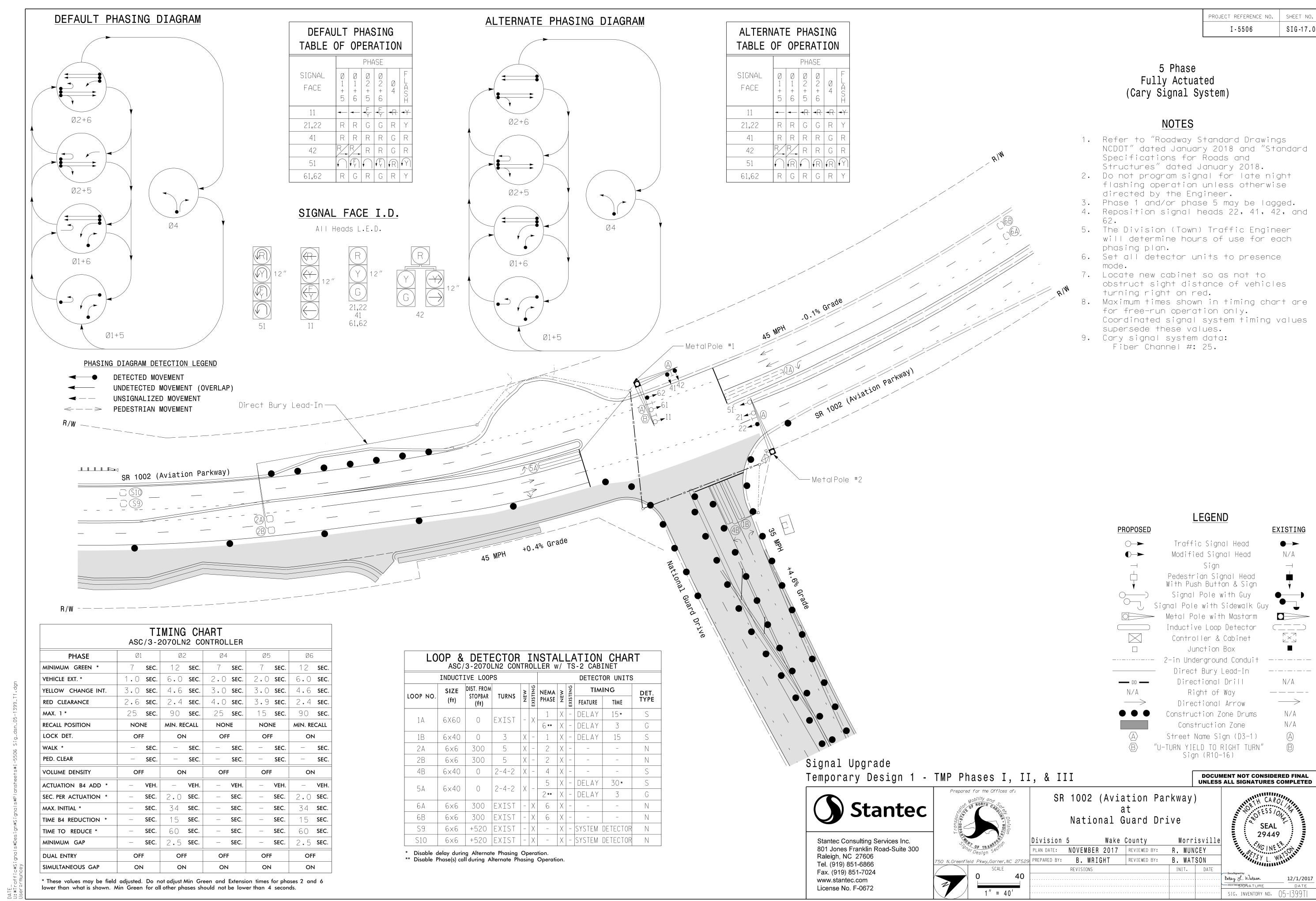
- H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
- 9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10.The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signalheads over the roadway.
- 11. The contractor is responsible for providing soilpenetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.



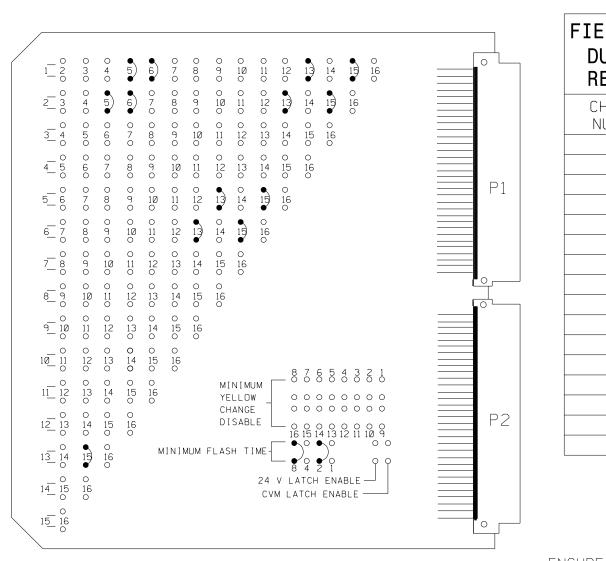
SIG. INVENTORY NO.



-raffic\*Signals\*Design\*Signals\*Metal Poles\*1-5506 S



(program card and tables as shown)



MMU PROGRAMMING CARD

FIELD CHE DUAL IND RED FAIL	) ENABLE				
CHANNEL NUMBER	ENABLE/ DISABLE				
1	ENABLE				
2	ENABLE				
3	DISABLE				
4	ENABLE				
5	DISABLE				
6	ENABLE				
7	DISABLE				
8	DISABLE				
9	DISABLE				
10	DISABLE				
11	DISABLE				
12	DISABLE				
13	ENABLE				
14	DISABLE				
15	ENABLE				

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	ELLOW ARROW				
CONFIG MODE	В				
ENABLE CHAN	NEL PAIR, FYA				
CH 1-13	ON				
CH 3-14	OFF				
CH 5-15	ON				
CH 7-16	OFF				
RED/YEL IN	PUT ENABLE				
CH 1	ON				
CH 3	OFF				
CH 5	ON				
CH 7	OFF				
FLASH RATE FAULT	ON				
FYA TRAP DETECT	ON				

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

DISABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,7,8,9,10,11,12,14 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green. 4. Set power-up flash time to 10 seconds and implement on the
- malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-17.1

SIGNAL HEAD HOOK-UP CHART																	
PHASE	1	l	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11	42	21,22	NU	41,42	<b>★</b> 51	61,62	NU	NU	NU	NU	NU	NU	11	NU	<b>★</b> 51	NU
RED	*		2R		4R	*	6R										
YELLOW			2Y		4 Y	*	6Y										
GREEN			2G		4 G		6G										
RED ARROW														13R		15R	
YELLOW ARROW		1Y												13Y		15Y	
FLASHING YELLOW ARROW														13G		15G	
GREEN ARROW	1G	1G				5G											
₩																	
Ķ																	

NU = Not Used

- \* Denotes install load resistor. See Load Resistor
- Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1					
		L3	L1	L7	L5	L11	L9	S	S	S	S	S
		Ø 1	$\emptyset$ 1	NOT USED	Ø2	Ø6	ø5	L	L		L	
RACK #1	BIU	1B	1A	USED	2A	6A	5A	T	T	T	T	T
., Τ	DIU	CH2	CH2	CH2	CH2	CH2	CH2	E	E	E	E	E
		L4	L2	L8	L6	L12	L1Ø	M	M	M	M	M
		NOT	Ø6	Ø 4	Ø2	Ø6	Ø2		T	<u> </u>	T	T
		USED	1A	4B	2B	6B	5A	Y	Y	Y	Y	Y

| RACK |     | S<br>L<br>O<br>T      | L17<br>SYS<br>DET<br>S9         | S<br>L<br>O<br>T      |  |
|------|-----|-----------------------|---------------------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|--|
| #2   | RIU | E<br>M<br>P<br>T<br>Y | CH2<br>L18<br>SYS<br>DET<br>S1Ø | E<br>M<br>P<br>T<br>Y |  |

ON LOOP PANEL AS SHOWN IN THE CHART BELOW LOOP NO. LOOP PANEL TERMINALS

1B L3A,L3B

NU L4A,L4B

2A L5A,L5B

2B L6A,L6B

NU L7A,L7B

4B L8A,L8B

6A L11A,L11E

6B L12A,L12E

NU L13A,L13E

NU L14A,L14B

NU L15A,L15B

NU | L16A, L16B |

1 A

5 A

L1A,L1B

L2A,L2B

L9A,L9B

L10A,L10B

SHOWN IN THE CHART BELOW

CONTROLLER

4

**\*** 10

13

14

15

DETECTOR NO. FUNCTION

Ø 2

Ø 2

Ø 4

Ø 6

Ø 6

ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN

TIMING

FEATURE TIME(SEC)

DELAY 15

Ø6 DELAY 3

Ø1 DELAY 15

Ø5 DELAY 30

Ø2 DELAY 3

IN THE CHART BELOW LOOP NO. LOOP PANEL TERMINALS

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

_OOP NO.	LOOP PANEL TERMINALS		CONTROLL	ΞR	FUNCTION	ΤI	MING
 S9	L17A,L17B		DETECTOR	NO.	TONCTION	FEATURE	TIME(SEC)
			<del>**</del> 17		SYSTEM		
S10	L18A,L18B		<del>**</del> 18		SYSTEM		
NU	L19A,L19B		- 19				
NU	L20A,L20B		20				
NU	L21A,L21B						
NU	L22A,L22B		21		,		
NU	L23A,L23B		- 22				
NU	L24A,L24B		• 23		*		
NU	L25A,L25B	-	- 24				
NU	L26A,L26B		- 25				
NU	L27A,L27B		26				
			. 27				
NU	L28A,L28B		- 28				
NU	L29A,L29B		29				
NU	L30A,L30B		. 30				
NU	L31A,L31B						
NU	L32A,L32B		31		÷		
	l	J	. 32				

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

### **EQUIPMENT INFORMATION**

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE ......ECONOLITE ASC/3-2070

CABINET MOUNT.....BASE LOADBAY POSITIONS.....16

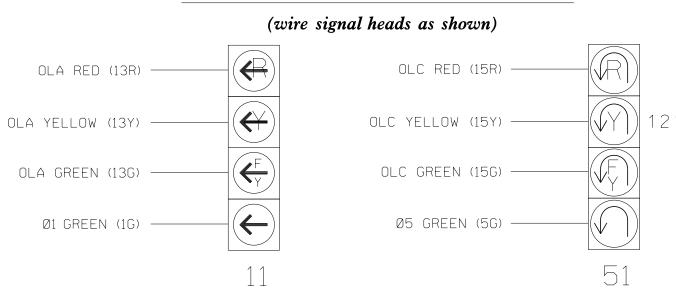
LOAD SWITCHES USED.....1,2,4,5,6,13,15 

OLA....\* OLB....NOT USED OLC....\*

OLD.....NOT USED

\* See overlap programming detail on sheet 2

### FYA SIGNAL WIRING DETAIL



#### LOAD SWITCH ASSIGNMENT DETAIL

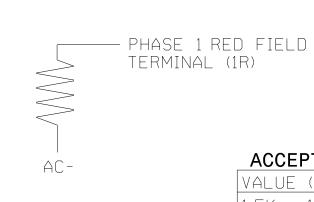
(program controller according to schedule in chart below)

FUNCTION
Ø 1
Ø 2
_
Ø 4
Ø 5
Ø 6
_
-
_
_
-
-
OLA
-
OLC
_

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

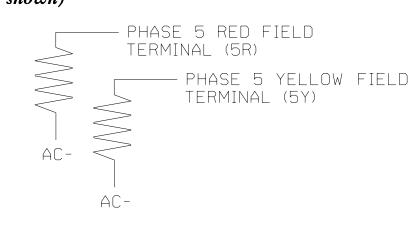
#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)



ACCEPTABLE VALUES VALUE (ohms) | WATTAGE

1.5K - 1.9K | 25W (min) 2.0K - 3.0K | 10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 3 Temporary Design 1 - TMP Phase III



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com

License No. F-0672



# SR 1002 (Aviation Parkway) National Guard Drive

Morrisville Division 5 Wake County NOVEMBER 2017 REVIEWED BY: L. OVERN REVIEWED BY: R. MUNCEY PREPARED BY: G. SPELL INIT. DATE REVISIONS

29449

**DOCUMENT NOT CONSIDERED FINAL** 

UNLESS ALL SIGNATURES COMPLETED

\*\* Detector Type - N

\* Detector Type - G

ADD JUMPERS FROM: L1A TO L2A, AND L1B TO L2B

L9A TO L10A, AND L9B TO L10B

50 N.Greenfield Pkwy,Garner,NC 27529

12/5/2017 DATE

# ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN

ACTION PLAN...[ 99] PATTERN......99 SYS OVERRIDE.... NO TIMING PLAN..... O SEQUENCE.... O VEH DETECTOR PLAN.. 2 DET LOG.....NONE FLASH..... -- RED REST..... NO VEH DET DIAG PLN... O PED DET DIAG PLN... DIMMING ENABLE.. NO PRIORITY RETURN, NO PED PR RETURN.. NO QUEUE DELAY.... NO PMT COND DELAY NO PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 SPC FCT X . . . X . . . (1-8) AUX FCT . . (1-3) 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

I-5506

SIG-17.2

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O

ACTION PLAN SF BIT DISABLE..... 1 / Toggle Twice

#### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH.... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 5

END PROGRAMMING

### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING <u>free run</u> — program changes (shown below) in a time based action plan, TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

TO RUN ALT, PHASING DURING <u>COORDINATION</u> - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

PHASING	<u>veh det plan</u>	<u>sf bits enabled</u>
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	1 2	NONE 1,5

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A



The PPLT FYA operation of Signal Head 11 (Overlap A), Signal Head 51 (Overlap C) can be altered to fully protected operation.

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OR WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1 AND 5

VEH DET PLAN 2:

Modifies overlap parent phases for heads 11 and 51 to run protected turns only.

on loop 1A to 0 seconds.

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Temporary Design 1 - TMP Phase III

Electrical Detail - Sheet 2 of 3

Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com

License No. F-0672



50 N.Greenfield Pkwy,Garner,NC 27529

# SR 1002 (Aviation Parkway) I-40 WB Ramps

Wake County

Division 5 Morrisville PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

PROJECT REFERENCE NO.

I-5506 SIG-17.3

# ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERATE PHASING

LOOPS 1A, 5A

(program controller as shown)

# 

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu selet 8. UTILITIES
- 2. From Utilities Submenu select | 1. COPY/CLEAR |
- 3. Copy from Detector Plan "1" to Detector Plan "2".

COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN.... > TIMING PLAN.... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - For Loop 1A, modify vehicle detectors.
  - Place cursor in VEH DETECTOR [ ] position and enter "1".
  - Set delay time to "0".

VEH DETECTOR [ 1] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR..... X ECPI LOG...... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O - ENSURE DELAY IS SET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO - Place cursor in VEH DETECTOR [ ] position and enter "2". - Set assigned phase to "0". VEH DETECTOR [ 2] VEH DET PLAN [ 2]

TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- For Loop 5A, modify vehicle detectors. - Place cursor in VEH DETECTOR [ ] position and enter "9". - Set delay time to "0".

- Set assigned phase to "0".

VEH DETECTOR [ 9] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR.... X ECPI LOG.... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O + ENSURE DELAY IS SET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO - Place cursor in VEH DETECTOR [ ] position and enter "10".

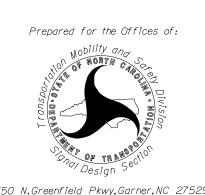
VEH DETECTOR [10] VEH DET PLAN [2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE →
IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399T1 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 3 of 3 Temporary Design 1 - TMP Phase III



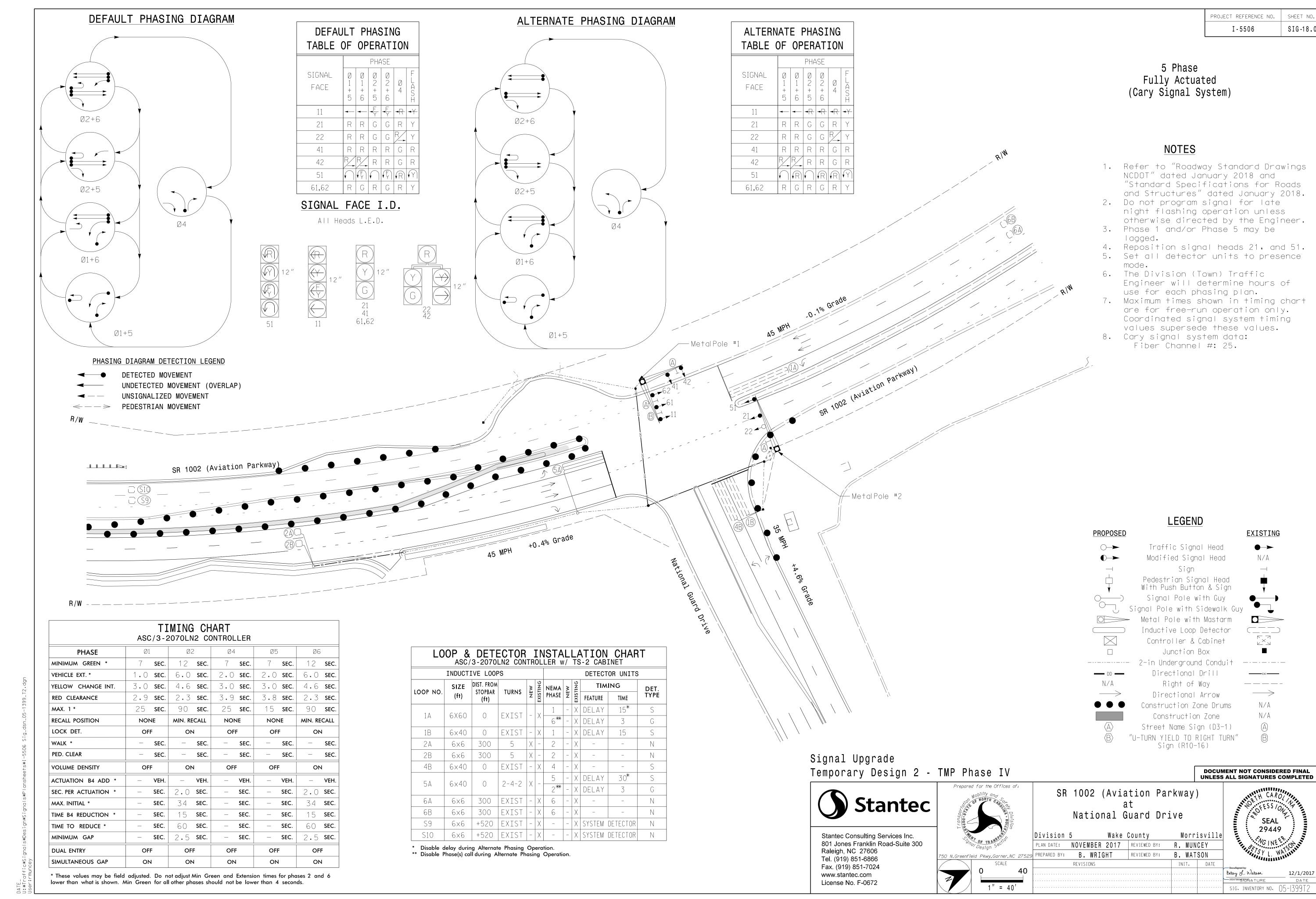
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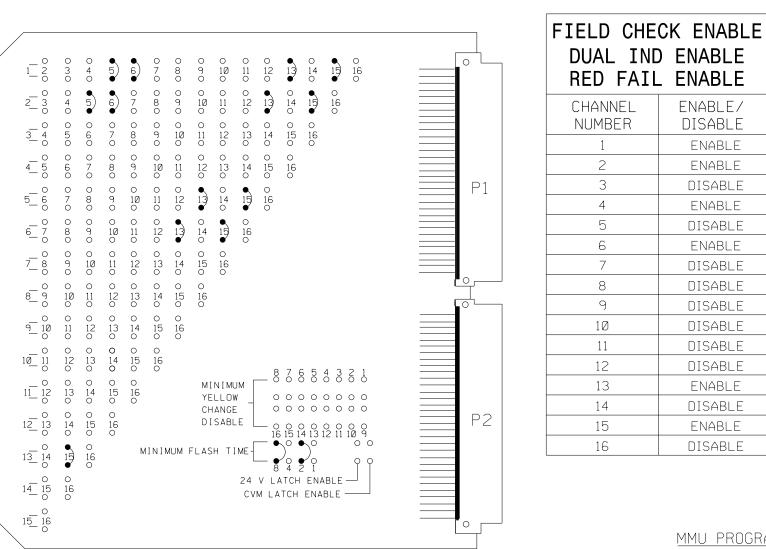
**DOCUMENT NOT CONSIDERED FINAL** UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 WB Ramps

Wake County Morrisville Division 5 PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN REVIEWED BY: R. MUNCEY PREPARED BY: G. SPELL REVISIONS INIT. DATE

29449 12/5/2017 sig. Inventory No. 05-1399



(program card and tables as shown)



MMU PROGRAMMING CARD

OPTION	SETTING
01 11011	0211110
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YELLOW ARROW									
CONFIG MODE	В								
ENABLE CHANN	NEL PAIR, FYA								
CH 1-13	ON								
CH 3-14	OFF								
CH 5-15	ON								
CH 7-16	OFF								
RED/YEL INF	PUT ENABLE								
CH 1	ON								
CH 3	OFF								
CH 5	ON								
CH 7	OFF								
FLASH RATE FAULT	ON								
FYA TRAP DETECT	ON								
	•								

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

ENABLE

ENABLE

DISABLE

ENABLE

DISABLE ENABLE

DISABLE

DISABLE

DISABLE

DISABLE

DISABLE DISABLE

ENABLE

DISABLE

ENABLE

DISABLE

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,7,8,9,10,11,12,14 & 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-18

	SIGNAL HEAD HOOK-UP CHART																	
PHASE	1		2	3	4	-	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	11	42	21,22	NU	41,42	22	<b>★</b> 51	61,62	NU	NU	NU	NU	NU	NU	<b>★</b>	NN	<b>★</b> 51	NU
RED	*		2R		4R		*	6R										
YELLOW			2Y		4 Y		*	6Y										
GREEN			2G		4G			6G										
RED ARROW															13R		15R	
YELLOW ARROW		1 Y				4 Y									13Y		15Y	
FLASHING YELLOW ARROW															13G		15G	
GREEN ARROW	1G	1G				4 G	5G											
₩																		
Ķ																		
NU = Not	llsed		1		1							1	1					

- \* Denotes install load resistor. See Load Resistor
- Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1					
		L3	∟1	L7	L5	L11	L9	S	S	S	S	S
		Ø 1	Ø 1	NOT USED	Ø2	Ø6	Ø5					
RACK #1		1B	1 🖰		2A	6A	5A	T	Ť	Ť	Ť	T
# ]	BIU	CH2	CH2	CH2	CH2	CH2	CH2	E	E	E	E	E
		L4	L2	L8	L6	L12	L10	M	M	M	М	M
		NOT USED	Ø6	Ø 4	Ø2	Ø6	Ø2	<u></u>	, T	<u> </u>	T	
		USED	1A	4B	2B	6B	5A	Y	Y	Y	Y	Y

RACK		SLOT	CH1 L17 SYS DET S9	SLOT	SLOT	SLOT	SLOT	SLOT	S L O T	SLOT	SLOT	SLOT	
#2	BIU	E M P T Y	CH2 L18 SYS DET	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T	E M P T	E M P T Y	E M P T Y	E M P T Y	

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE IN THE CHART BELOW SHOWN IN THE CHART BELOW OWN IN THE CHART BELOW

•	IN THE C	HARI BELUW
	LOOP NO.	LOOP PANEL TERMINALS
ADD JUMPERS FROM:	1 /	L1A,L1B
L1B TO L2B	IA	L2A,L2B
	1B	L3A,L3B
	NU	L4A,L4B
	2 A	L5A,L5B
	2B	L6A,L6B
	NU	L7A,L7B
	4B	L8A,L8B
ADD JUMPERS FROM:	5.1	L9A,L9B
L9B TO L10B	JA	L10A,L10B
	6 A	L11A,L11B
	6B	L12A,L12B
	NU	L13A,L13B
	NU	L14A,L14B
	NU	L15A,L15B
	NU	L16A,L16B
	ADD JUMPERS FROM: L9A TO L10A, AND	ADD JUMPERS FROM: L1A TO L2A, AND L1B TO L2B  1B  NU 2A 2B  NU 4B  ADD JUMPERS FROM: L9A TO L10A, AND L9B TO L10B  6A 6B  NU NU NU

SHOWN	IN THE	CHART E	BELOW
CONTROLLER	FUNCTION	TI	MING
DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)
· 1	Ø 1	DELAY	15
* 2	Ø 6	DELAY	3
. 3	Ø 1	DELAY	15
. 4			
<del>**</del> 5	Ø 2		
<del>**</del> 6	Ø 2		
. 7			
. 8	Ø 4		
. 9	Ø 5	DELAY	30
<del>*</del> 10	Ø 2	DELAY	3
<del>**</del> 11	Ø 6		
<del>**</del> 12	Ø 6		
• 13			
. 14			
. 15			
· 16			

	· .	200		· · · · ·		, , , , , ,
		IN THE (	CHART BELO	W		SHOW
2)		LOOP NO.	LOOP PANEL TERMINALS			TROLLE CTOR I
, )		S9	L17A,L17B			
		S10	L18A,L18B		**	
		NU	L19A,L19B		**	18
		NU	L20A,L20B			19
		NU	L21A,L21B			20
		NU	L22A,L22B			21
		NU	L23A,L23B			22
		NU	L24A,L24B			23
		NU	L25A,L25B		-	24
		NU	L26A,L26B		-	25
		NU	L27A,L27B		-	26
						27
		NU	L28A,L28B			28
		NU	L29A,L29B			29
		NU	L30A,L30B			30
		NU	L31A,L31B			31
		NU	L32A,L32B			32

9B	<del>**</del> 18	SYSTEM	
OB	19		
В	20		
	. 21		
'B	22		
iB	23		
В	24		
	- 25		
В	26		
B B	. 27		
В	28		
B B	29		
В	- 30		
В	. 31		
ט	. 32		

SYSTEM

FEATURE TIME (SEC)

### **EQUIPMENT INFORMATION**

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE .....ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....1,2,4,5,6,13,15 OLA....\*

OLB....NOT USED OLC....\* OLD.....NOT USED

\* See overlap programming detail on sheet 2

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown) OLC RED (15R) -OLA RED (13R) — OLC YELLOW (15Y) — OLA YELLOW (13Y) — OLA GREEN (13G) -OLC GREEN (15G) -Ø5 GREEN (5G) -Ø1 GREEN (1G) —

#### LOAD SWITCH ASSIGNMENT DETAIL

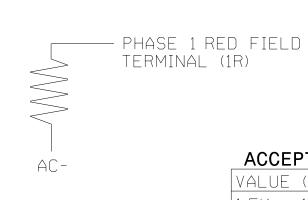
(program controller according to schedule in chart below)

LOAD SWITCH NUMBER	FUNCTION						
1	Ø 1						
2	Ø 2						
3	-						
4	Ø 4						
5	Ø 5						
6	Ø 6						
7	-						
8	-						
9	-						
10	-						
11	-						
12	-						
13	OLA						
14	-						
15	OLC						
16	-						
	TITCH CHANNEL						

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

#### LOAD RESISTOR INSTALLATION DETAIL

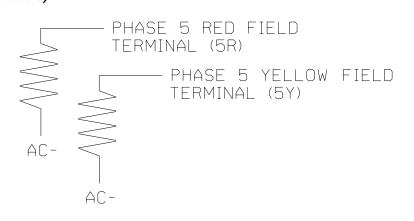
(install resistors as shown)



TERMINAL (1R) ACCEPTABLE VALUES VALUE (ohms) | WATTAGE

1.5K - 1.9K | 25W (min)

2.0K - 3.0K | 10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399T2 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 3 Temporary Design 2 - TMP Phase IV



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50 N.Greenfield Pkwy,Garner,NC 27529

### SR 1002 (Aviation Parkway) National Guard Drive

Wake County Morrisville Division 5 NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY INIT. DATE REVISIONS

29449 12/5/2017

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

SIG. INVENTORY NO.

\* Detector Type - G \*\* Detector Type - N

NOTE:
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

# ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN

ACTION PLAN...[ 99] PATTERN......99 SYS OVERRIDE.... NO TIMING PLAN..... O SEQUENCE.... O VEH DETECTOR PLAN.. 2 DET LOG.....NONE FLASH..... -- RED REST..... NO VEH DET DIAG PLN... O PED DET DIAG PLN... DIMMING ENABLE.. NO PRIORITY RETURN, NO PED PR RETURN.. NO QUEUE DELAY.... NO PMT COND DELAY NO PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 SPC FCT X . . . X . . . (1-8) AUX FCT . . (1-3) 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH13 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 1 /

Toggle Twice

I-5506

SIG-18.2

#### OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH.... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 5

END PROGRAMMING

### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING <u>free run</u> — program changes (shown below) in a time based action plan, TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

TO RUN ALT, PHASING DURING <u>COORDINATION</u> - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

PHASING	<u>veh det plan</u>	<u>sf bits enabled</u>
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u> ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	1 2	NONE 1,5

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399T2 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A



The PPLT FYA operation of Signal Head 11 (Overlap A), Signal Head 51 (Overlap C) can be altered to fully protected operation.

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OR WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1 AND 5

VEH DET PLAN 2:

Modifies overlap parent phases for heads 11 and 51 to run protected turns only.

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Electrical Detail - Sheet 2 of 3 Temporary Design 2 - TMP Phase IV



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50 N.Greenfield Pkwy,Garner,NC 27529

# SR 1002 (Aviation Parkway) I-40 WB Ramps

Division 5 Wake County Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN

PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

PROJECT REFERENCE NO. SIG-18.3

I-5506

# ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERATE PHASING

LOOPS 1A, 5A (program controller as shown) 

Program detectors per the input file connection and

programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu selet 8. UTILITIES
- 2. From Utilities Submenu select | 1. COPY/CLEAR |
- 3. Copy from Detector Plan "1" to Detector Plan "2".

COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN.... > TIMING PLAN.... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - For Loop 1A, modify vehicle detectors.
  - Place cursor in VEH DETECTOR [ ] position and enter "1".
  - Set delay time to "0".

VEH DETECTOR [ 1] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR..... X ECPI LOG...... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O - ENSURE DELAY IS SET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO - Place cursor in VEH DETECTOR [ ] position and enter "2". - Set assigned phase to "0". VEH DETECTOR [ 2] VEH DET PLAN [ 2]

TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- For Loop 5A, modify vehicle detectors. - Place cursor in VEH DETECTOR [ ] position and enter "9",

- Set delay time to "0".

VEH DETECTOR [ 9] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR..... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O + ENSURE DELAY IS SET TO 'O'

USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- Place cursor in VEH DETECTOR [ ] position and enter "10".

- Set assigned phase to "0".

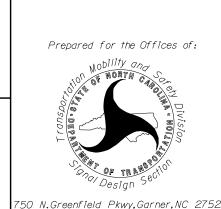
VEH DETECTOR [10] VEH DET PLAN [2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE →
IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399T2 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 3 of 3 Temporary Design 2 - TMP Phase IV



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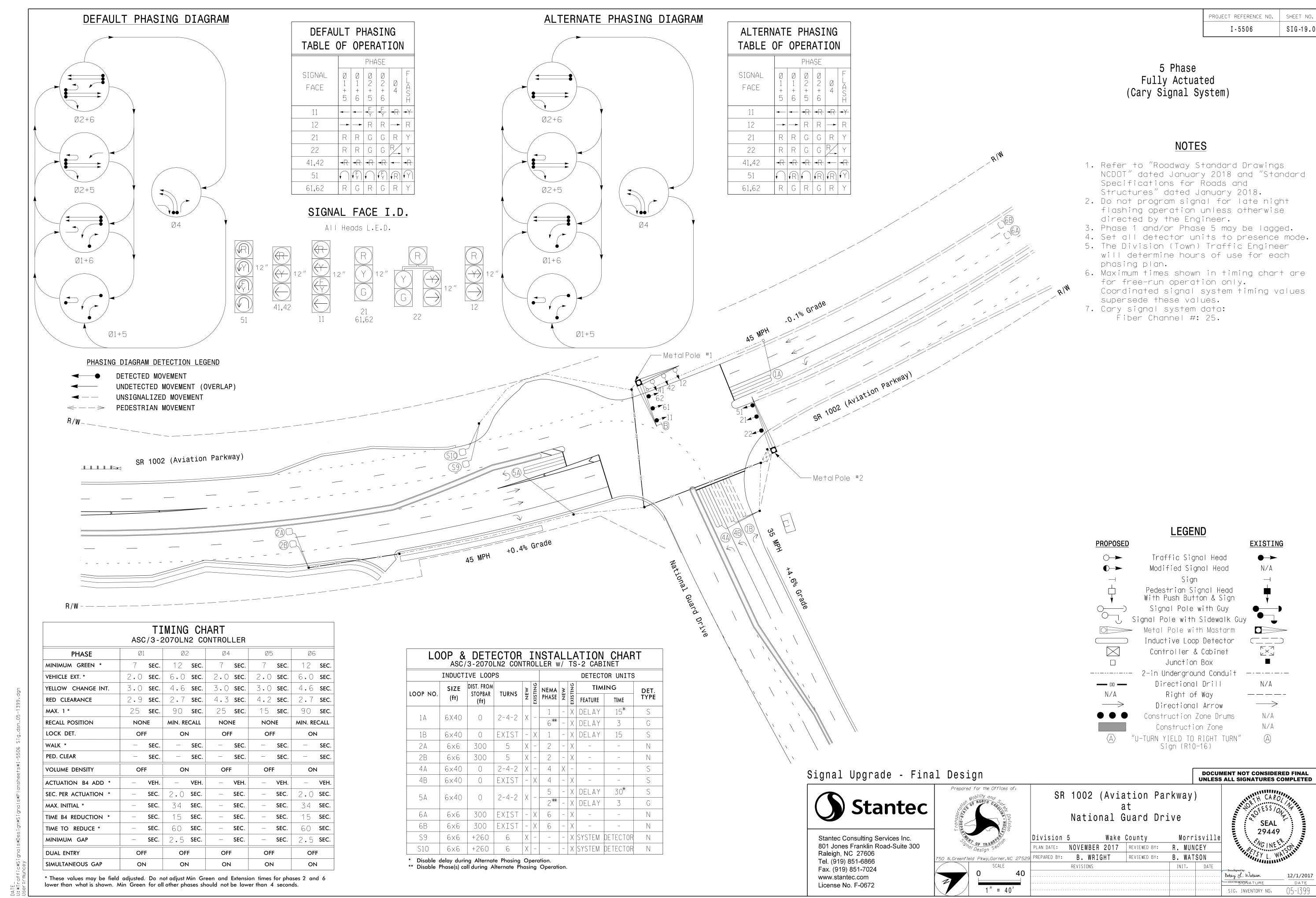
UNLESS ALL SIGNATURES COMPLETED SR 1002 (Aviation Parkway) I-40 WB Ramps

Wake County Division 5 Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

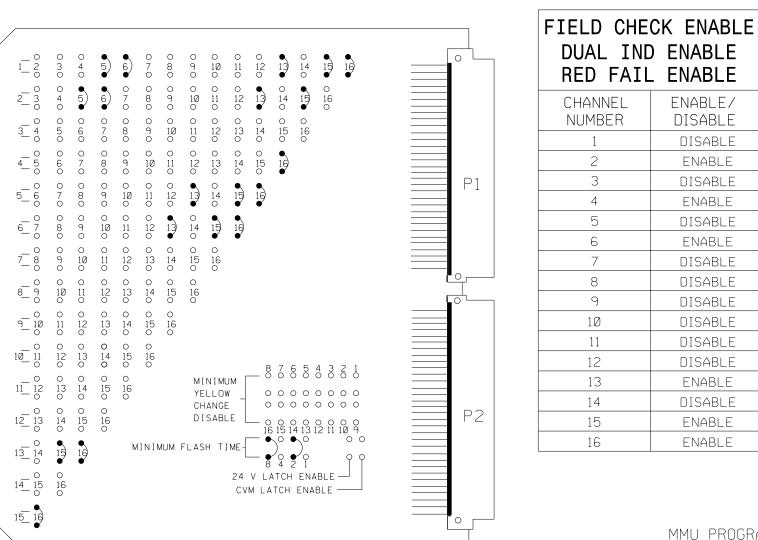
12/5/2017 SIG. INVENTORY NO. 05-1399

29449

**DOCUMENT NOT CONSIDERED FINAL** 



(program card and tables as shown)



MMU PROGRAMMING CARD

UNIT O	PTIONS
OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDLC	OFF
VM 3x/Day Latch	ON

FLASHING YE	LLOW ARROW					
CONFIG MODE	В					
ENABLE CHANN	NEL PAIR, FYA					
CH 1-13	ON					
CH 3-14	OFF					
CH 5-15	ON					
CH 7-16	OFF					
RED/YEL INF	PUT ENABLE					
CH 1	ON					
CH 3	OFF					
CH 5	ON					
CH 7	OFF					
FLASH RATE FAULT	ON					
FYA TRAP DETECT	ON					

TIMING

FEATURE TIME(SEC:

MMU PROGRAMMING NOTE ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

#### NOTES

- 1. To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- 2. To prevent red failures on unused monitor channels, tie unused load switch red outputs 3,7,8,9,10,11,12 & 14 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (Is AC+) to pin 3 (RED out). Make sure all flash transfer relays are in place.
- 3. Program controller to start up in phases 2 and 6 green.
- 4. Set power-up flash time to 10 seconds and implement on the malfunction management unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature, on controller unit, for all phases.
- 6. Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- 7. Program detector call delay and extension timing on the controller, unless otherwise specified.
- 8. Set all detector card unit channels to "presence" mode.
- 9. Program phases 2 and 6, on controller unit, for volume density operation.
- 10. The cabinet and controller are a part of the Cary Signal System.

PROJECT REFERENCE NO. I-5506 SIG-19.1

				5	SIG	NAL	HE	AD	НО	OK-	UP	CHA	ART				
PHASE	1	2	3		1	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD
SIGNAL HEAD NO.	<b>11</b> ★	21,22	NU	41,42	22	<b>★</b> 51	61,62	NU	NU	NU	NU	NU	NU	11	NU	<b>5</b> 1	12
RED	*	2R				*	6R										16R
YELLOW	*	2Y				*	6Y										
GREEN		2G					6G										
RED ARROW				4R										13R		15R	
YELLOW ARROW				4 Y	4 Y									13Y		15Y	16Y
FLASHING YELLOW ARROW														13G		15G	
GREEN ARROW	1G			4 G	4G	5G											16G
₩																	
Ķ																	
NU = Not	Used	1							ı	I			1				

- \* Denotes install load resistor. See Load Resistor
- Installation Detail.
- ★ See pictorial of head wiring detail this sheet.

#### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

		CH1	CH1	CH1	CH1	CH1	CH1					
		L3	L1	L7	L5	L11	L9	S	S	S	S	S
		Ø 1	Ø 1	Ø 4	Ø2	Ø6	Ø5		L		L	
RACK #1	DIII	1B	1A	4 A	2A	6A	5A	T	T	T	T	T
., 1	BIU	CH2	CH2	CH2	CH2	CH2	CH2	E	E	E	E	E
		L4	L2	L8	L6	L12	L1Ø	M	M P	M	M P	M
		NOT USED	Ø6	Ø 4	Ø2	Ø6	Ø2	<u> </u>	T	T	T	T
		USED	1A	4 A	2B	6B	5A	Y	Y	Y	Y	Y

| RACK |     | S<br>L<br>O<br>T      | CH1<br>L17<br>SYS<br>DET<br>S9  | S<br>L<br>O<br>T      | S L O T          |  |
|------|-----|-----------------------|---------------------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|-----------------------|------------------|--|
| #2   | BIU | E<br>M<br>P<br>T<br>Y | CH2<br>L18<br>SYS<br>DET<br>S10 | E<br>M<br>P<br>T<br>Y | E<br>M<br>P<br>T |  |

WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS WIRE LOOPS TO TERMINALS PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE ON LOOP PANEL AS SHOWN ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW IN THE CHART BELOW IN THE CHART BELOW

-		BITAINT BELOV
	LOOP NO.	LOOP PANEL TERMINALS
ADD JUMPERS FROM:	1 1	L1A,L1B
LIB TO L2B	I I A	L2A,L2B
	1B	L3A,L3B
	NU	L4A,L4B
	2 A	L5A,L5B
	2B	L6A,L6B
	4 A	L7A,L7B
	4B	L8A,L8B
ADD JUMPERS FROM:	5.4	L9A,L9B
L9B TO L10B	JA	L10A,L10B
	6A	L11A,L11B
	6B	L12A,L12B
	NU	L13A,L13B
	NU	L14A,L14B
	NU	L15A,L15B
	NU	L16A,L16B
	L1A TO L2A, AND L1B TO L2B  ADD JUMPERS FROM: L9A TO L10A, AND	ADD JUMPERS FROM: L1A TO L2A, AND L1B TO L2B  1B  NU 2A 2B 4A 4B  ADD JUMPERS FROM: L9A TO L10A, AND L9B TO L10B  6A 6B NU NU NU

SHOWN	IN THE	CHARI B	ELOW			
CONTROLLER	FUNCTION	TIMING				
DETECTOR NO.	FUNCTION	FEATURE	TIME(SEC)			
· 1	Ø 1	DELAY	15			
* 2	Ø 6	DELAY	3			
. 3	Ø 1	DELAY	15			
. 4						
<del>**</del> 5	Ø 2					
<del>**</del> 6	Ø 2					
· 7	Ø 4					
. 8	Ø 4					
. 9	Ø 5	DELAY	30			
<del>*</del> 10	Ø 2	DELAY	3			
<del>**</del> 11	Ø 6					
<del>**</del> 12	Ø 6					
13						
. 14						
· 15						
. 16						

HOWN	IN THE	CHARI B	BELOW		IN IHE (	CHARI BELOW			SHOWN	IN IHE
ILLER	FUNCTION		MING		LOOP NO.	LOOP PANEL TERMINALS			ROLLER	FUNCTION
OR NO.		FEATURE	TIME(SEC)		S 9	L17A,L17B		DETE	CTOR NO.	
	Ø 1	DELAY	15		S10	L18A,L18B		**	17	SYSTEM
	Ø 6	DELAY	3		NU	L19A,L19B		**	18	SYSTEM
	Ø 1	DELAY	15						19	•
					NU	L20A,L20B	-		20	4
	Ø 2				NU	L21A,L21B	-		21	4
	Ø 2			-	NU	L22A,L22B	-			
					NU	L23A,L23B			22	
	Ø 4				NU	L24A,L24B			23	*
	Ø 4				NU	L25A,L25B		-	24	
1	Ø 5	DELAY	30		NU	L26A,L26B		-	25	
C	Ø 2	DELAY	3		NU	L27A,L27B			26	
1	Ø 6						•		27	
2	Ø6				NU	L28A,L28B	-	-	28	
2 3	,			-	NU	L29A,L29B	=	-	29	
<u></u> 1					NU	L30A,L30B	-		30	
5 5					NU	L31A,L31B				
)					NU	L32A,L32B			31	· · · · · · · · · · · · · · · · · · ·
<u> </u>								-	32	•
NOTE BE S AND	SURE TO PR	OGRAM DE SHOWN O	TECTOR TYP N THE SIGN	ES AN AL PL	ND TIMERS	(EXTEND				

#### **EQUIPMENT INFORMATION**

CONTROLLER.....2070LN2 CABINET .....TS-2 SOFTWARE ......ECONOLITE ASC/3-2070 CABINET MOUNT.....BASE LOADBAY POSITIONS.....16 LOAD SWITCHES USED.....1,2,4,5,6,13,15,16 OLA.....\* OLB.....NOT USED OLC....\* OLD.....1+4

\* See overlap programming detail on sheet 2

### FYA SIGNAL WIRING DETAIL (wire signal heads as shown) OLC RED (15R) OLA RED (13R) — OLC YELLOW (15Y) — OLA YELLOW (13Y) — OLA GREEN (13G) -OLC GREEN (15G) -Ø5 GREEN (5G) -Ø1 GREEN (1G) —

### LOAD SWITCH ASSIGNMENT DETAIL

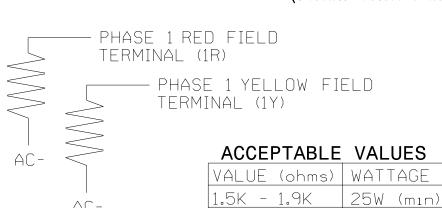
(program controller according to schedule in chart below)

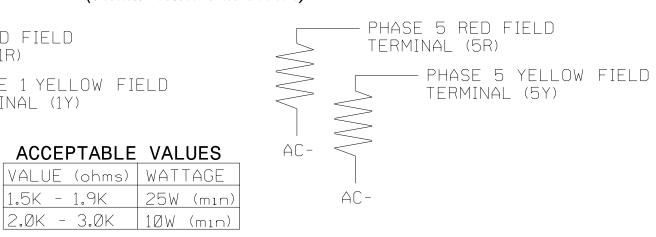
LOAD SWITCH NUMBER	FUNCTION						
1	Ø 1						
2	ø 2						
3	-						
4	Ø 4						
5	Ø 5						
6	Ø 6						
7	_						
8	_						
9	-						
10	_						
11	_						
12	-						
13	OLA						
14	-						
15	OLC						
16	OLD						

UNUSED LOAD SWITCH CHANNELS SHALL BE DISABLED IN CONTROLLER PROGRAMMING

#### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown)





THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 1 of 3 Final Design



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com

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50 N.Greenfield Pkwy,Garner,NC 27529

# SR 1002 (Aviation Parkway) National Guard Drive

Division 5 Wake County Morrisville NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

UNLESS ALL SIGNATURES COMPLETED 29449 12/5/2017 DATE

SIG. INVENTORY NO.

05-1399

**DOCUMENT NOT CONSIDERED FINAL** 

\*\* Detector Type - N

\* Detector Type - G

# ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE

2. From TIME BASE Submenu select | 2. ACTION PLAN

ACTION PLAN...[ 99] PATTERN......99 SYS OVERRIDE.... NO TIMING PLAN..... O SEQUENCE.... O VEH DETECTOR PLAN.. 2 DET LOG.....NONE FLASH..... -- RED REST..... NO VEH DET DIAG PLN... O PED DET DIAG PLN... DIMMING ENABLE.. NO PRIORITY RETURN, NO PED PR RETURN.. NO QUEUE DELAY.... NO PMT COND DELAY NO PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 SPC FCT X . . X . . . (1-8) AUX FCT . . (1-3) 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: Ø5-1399

DESIGNED: NOV 2017 SEALED: 12-01-2017

REVISED: N/A

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

I-5506

SIG-19.2

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 1 OPPOSING THROUGH..... PHASE 2 FLASHING ARROW OUTPUT....CH13 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 1 / Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

TMG VEH OVLP...[C] TYPE: .... PPLT FYA PROTECTED LEFT TURN.... PHASE 5 OPPOSING THROUGH.... PHASE 6 FLASHING ARROW OUTPUT....CH15 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 5 Toggle Once

OVERLAP D Select TMG VEH OVLP [D] and 'NORMAL'

LAG GRN 0.0 YEL 0.0 RED 0.0

TMG VEH OVLP...[D] TYPE: .....NORMAL PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 

END PROGRAMMING

The PPLT FYA operation of Signal Head 11 (Overlap A), Signal Head 51 (Overlap C) can be altered to fully protected operation.

### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT, PHASING DURING <u>free run</u> — program changes (shown below) in a time based action plan, TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

TO RUN ALT, PHASING DURING <u>COORDINATION</u> - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 AND 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN ALTERNATE PHASING	2	1,5

#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OR WHAT TAKES PLACE WHEN SF BITS 1, 5, AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

Modifies overlap parent phases for

SF BITS 1 AND 5

VEH DET PLAN 2:

heads 11 and 51 to run protected turns only.

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

Electrical Detail - Sheet 2 of 3 Final Design



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50 N.Greenfield Pkwy,Garner,NC 27529

SR 1002 (Aviation Parkway) National Guard Drive

Division 5 Wake County Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

DOCUMENT NOT CONSIDERED FINAL

**UNLESS ALL SIGNATURES COMPLETED** 

PROJECT REFERENCE NO. I-5506 SIG-19.3

# ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERATE PHASING

### LOOPS 1A, 5A

(program controller as shown) 

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu selet 8. UTILITIES
- 2. From Utilities Submenu select | 1. COPY/CLEAR |
- 3. Copy from Detector Plan "1" to Detector Plan "2".

COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... TIMING PLAN.... > TIMING PLAN.... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - For Loop 1A, modify vehicle detectors.
  - Place cursor in VEH DETECTOR [ ] position and enter "1".
  - Set delay time to "0".

VEH DETECTOR [ 1] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR..... X ECPI LOG...... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O - ENSURE DELAY IS SET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY, NO - Place cursor in VEH DETECTOR [ ] position and enter "2". - Set assigned phase to "0".

VEH DETECTOR [ 2] VEH DET PLAN [ 2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

- For Loop 5A, modify vehicle detectors.

- Place cursor in VEH DETECTOR [ ] position and enter "9",
- Set delay time to "0".

- Set assigned phase to "0".

VEH DETECTOR [ 9] VEH DET PLAN [ 2] TYPE: S-STANDARD TS2 DETECTOR.... X ECPI LOG.... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 EXTEND TIME... O.O DELAY TIME... O.O + ENSURE DELAY IS SET TO 'O' USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO - Place cursor in VEH DETECTOR [ ] position and enter "10".

VEH DETECTOR [10] VEH DET PLAN [2] TYPE: G-GREEN EXTENSION/DELAY TS2 DETECTOR.... X ECPI LOG..... NO DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 ENSURE PHASE →
IS SET TO 'O' EXTEND TIME... 0.0 DELAY TIME... 3.0 USE ADDED INITIAL . CROSS SWITCH PH.. O LOCK IN..... NONE NTCIP VOL . OR OCC . PMT QUEUE DELAY. NO

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø5-1399 DESIGNED: NOV 2017 SEALED: 12-01-2017 REVISED: N/A

Electrical Detail - Sheet 3 of 3 Final Design



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# SR 1002 (Aviation Parkway) National Guard Drive

Wake County Division 5 Cary PLAN DATE: NOVEMBER 2017 REVIEWED BY: L. OVERN PREPARED BY: G. SPELL REVIEWED BY: R. MUNCEY REVISIONS INIT. DATE

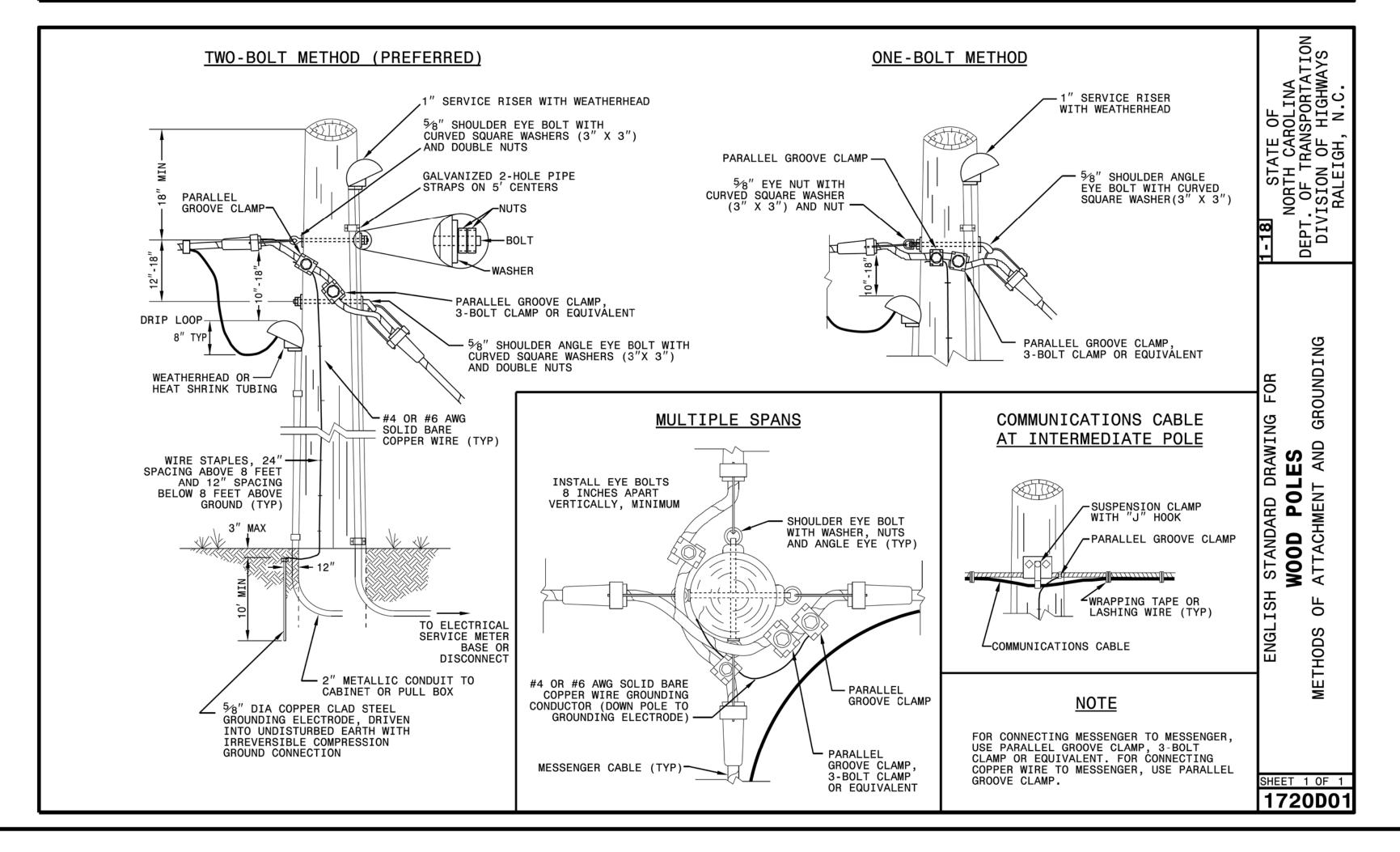
29449 12/5/2017

**DOCUMENT NOT CONSIDERED FINAL** 

**UNLESS ALL SIGNATURES COMPLETED** 

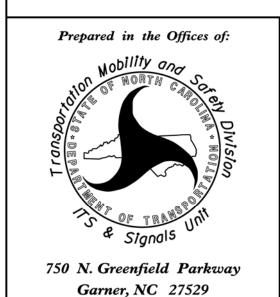
50 N.Greenfield Pkwy,Garner,NC 2752

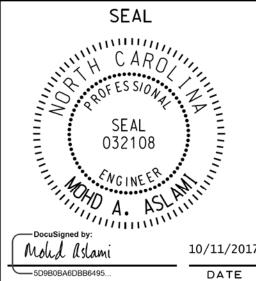
1-18 STATE OF
NORTH CAROLINA
DEPT. OF TRANSPORTAT
DIVISION OF HIGHWAY
RALEIGH, N.C. CONDUCTOR TO POWER MESSENGER CABLE\_ GROUNDING CONNECTION SYSTEM POLE GROUND METER BASE CONNECTION LOCK NUT #8 AWG MIN #8 AWG MIN STRANDED COPPER (BLACK) STRANDED COPPER (WHITE) - SERVICE DISCONNECT 120 V SINGLE POLE BREAKER - NEUTRAL BUS - MAIN BONDING SCREW #8 AWG MIN\_ STRANDED COPPER (WHITE) #6 AWG MIN GROUNDING GREEN INSULATED #8 AWG MIN STRANDED COPPER (BLACK) STRANDED COPPER WIRE GROUNDING/BONDING BUSHING-#4 AWG SOLID BARE
- COPPER WIRE TO
GROUNDING ELECTRODE LOCK NUTS -FOR JOINT USE POLES ONLY, #6 AWG MIN SOLID BARE COPPER WITH SPLIT BOLT CONNECTORS OR SYSTEM PARALLEL GROOVE CLAMPS ON EACH END
(CONNECTION TO BE MADE ABOVE
SPECIAL ROUTING SHOWN BELOW) RICAL SERVICE GROUNDING AND BON WIRE STAPLES, 24" SPACING ABOVE 8 FEET AND 12" SPACING BELOW 8 FEET ABOVE GROUND (TYP) PROVIDE WIRING ROUTING AND STAPLING SO THAT STAPLES MAY BE TEMPORARILY REMOVED AND GROUNDING WIRES CAN BE
PULLED MIN 1.5" OFF POLE & SPACED MAX
0.75" APART TO ENABLE TESTING OF GROUNDING ── ELECTRICAL SERVICE ELECTRODE RESISTANCE BY CLAMP ON TESTER TO CABINET TR S 5/8" DIA COPPER CLAD STEEL 딥 GROUNDING ELECTRODES, WITH IRREVERSIBLE COMPRESSION GROUND CONNECTOR SHEET 1 OF 1 1700D01



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# See Plate for Title



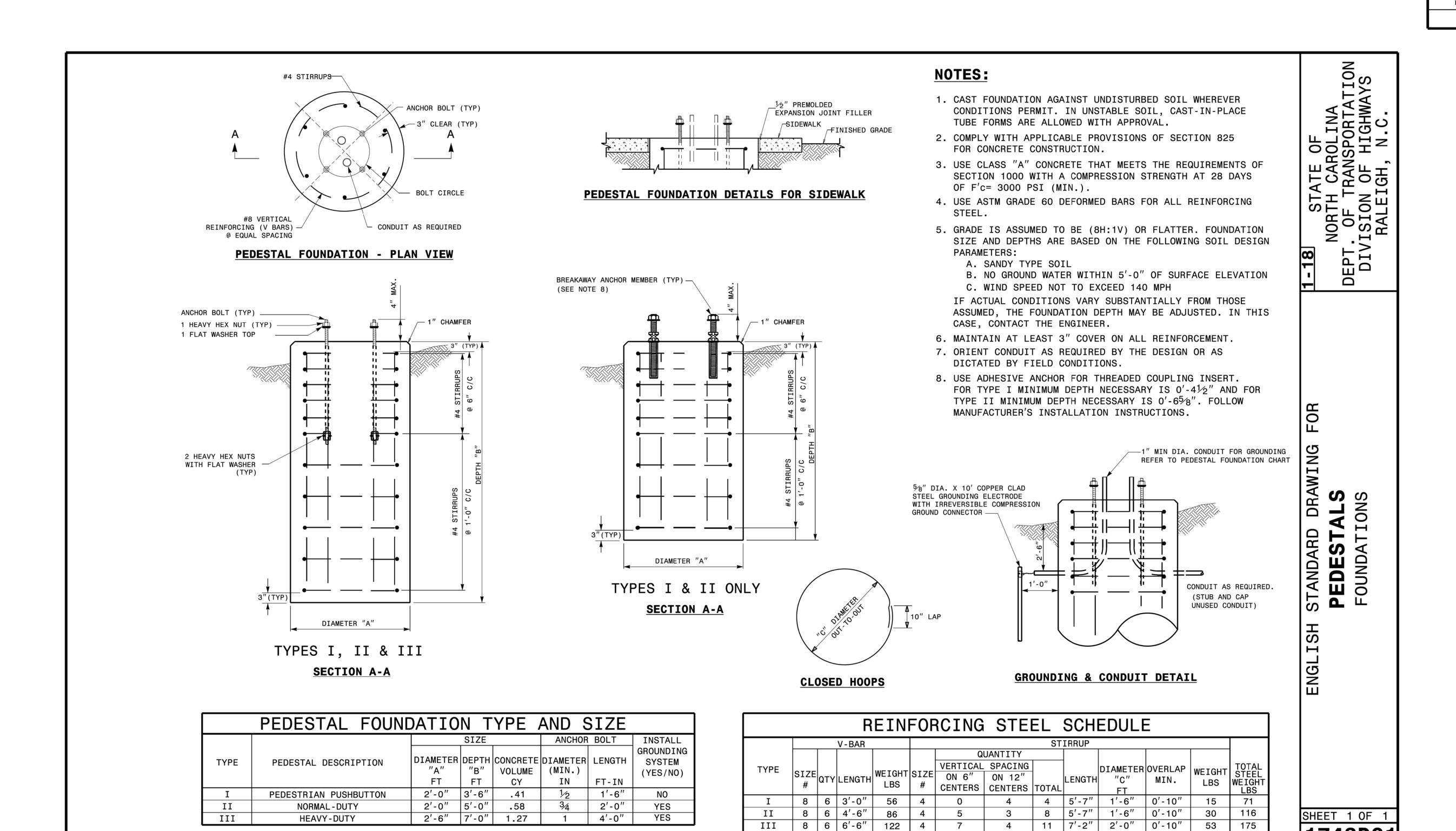


PROJECT NO.

I-5506

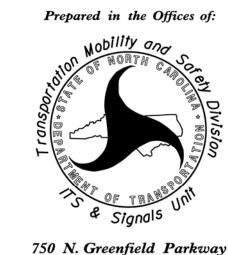
SHEET NO

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122

See Plate for Title



Garner, NC 27529

1743D01

175

53

Debesh C. Sarkar DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

#### STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS STANDARD DRAWINGS FOR ALL METAL POLES DIVISION 11 DIVISION 9 DIVISION 7 DIVISION 4 DIVISION 5 WIND ZONE 4 & 5 WIND ZONE 4 WIND ZONE 4 WIND ZONE 3 WIND ZONE 4 DIVISION 13 WIND ZONE 4 & 5 GUILFORD ALAMANCE ORANGE RANDOLPH LINCOLN DIVISION 12 RICHMOND WIND ZONE 4 DIVISION 14 WIND ZONE 4 & 5 DIVISION 10 WIND ZONE 4 DIVISION 8 WIND ZONE LEGEND WIND ZONE 4 DIVISION 3 WIND ZONE 2 WIND ZONE 1 (140 mph) Special Wind Zone DIVISION 6 WIND ZONE 2 (130 mph) Coastal Region WIND ZONE 3 WIND ZONE 3 (110 mph) Eastern Region WIND ZONE 4 (90 mph) Central & Mtn. Region L WIND ZONE 5 (120 mph) Special Wind Zone . https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx INDEX OF PLANS **NCDOT CONTACTS:** Prepared in the Offices of: Designed in conformance **DRAWING** with the latest **DESCRIPTION** NUMBER MOBILITY AND SAFETY DIVISION – ITS AND SIGNALS UNIT 2015 Interim to the 6th Edition 2013 Sig. M 1 Statewide Wind Zone Map M.M. MCDIARMID, P.E. – STATE ITS AND SIGNALS ENGINEER *AASHTO* Typical Fabrication Details-All Metal Poles Sig. M 2 Typical Fabrication Details-Strain Poles Sig. M 3 J. P. GALLOWAY, P.E. - STATE SIGNALS ENGINEER Standard Specifications for Typical Fabrication Details-Mast Arm Poles Sig. M 4 D.C. SARKAR, P.E. – ITS AND SIGNALS SENIOR STRUCTURAL ENGINEER Structural Supports for Typical Fabrication Details-Mast Arm Connection **Sig.** M 5

Typical Fabrication Details-Strain Pole Attachments

Standard Strain Pole Foundation-All Soil Conditions

Construction Details-Foundations

Sig. M 6

Sig. M 7

Sig. M 8

Highway Signs, Luminaires,

and Traffic Signals

750 N.Greenfield Pkwy, Garner,NC 27529

PROJECT I.D. NO.

I-5506

SHEET NO

Sig.M1

DIVISION 1

WIND ZONE 1 & 2

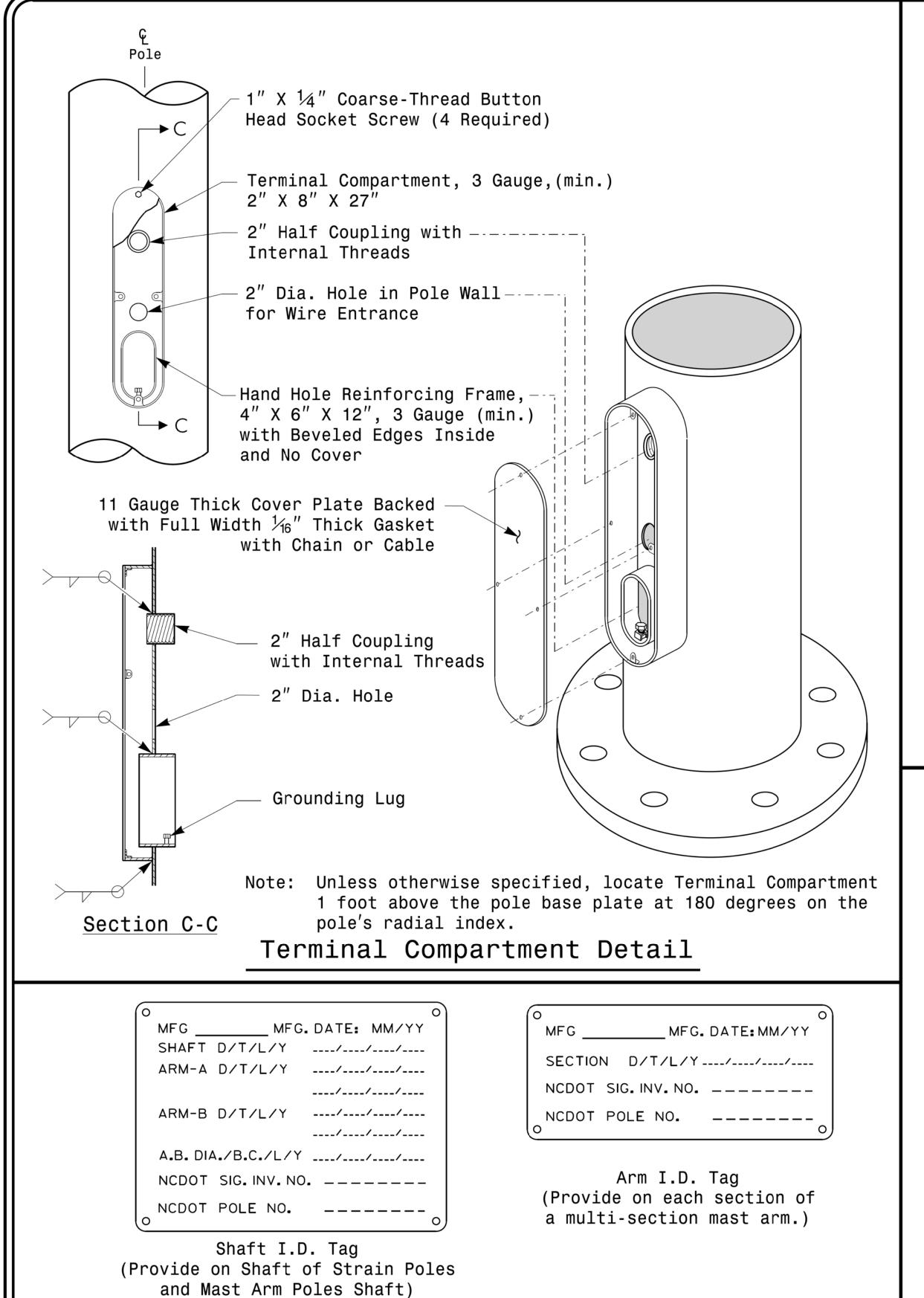
DIVISION 2

WIND ZONE 2

**SEAL** 

10/11/2017

Debesh C. Sarkar



1) D= Diameter, T= Thickness, L= Length, Y= Yield Strength

5) See drawing M3 and M4 for mounting positions of I.D. tags.

Identification Tag Details

Bottom

Anchor Bolt Detail

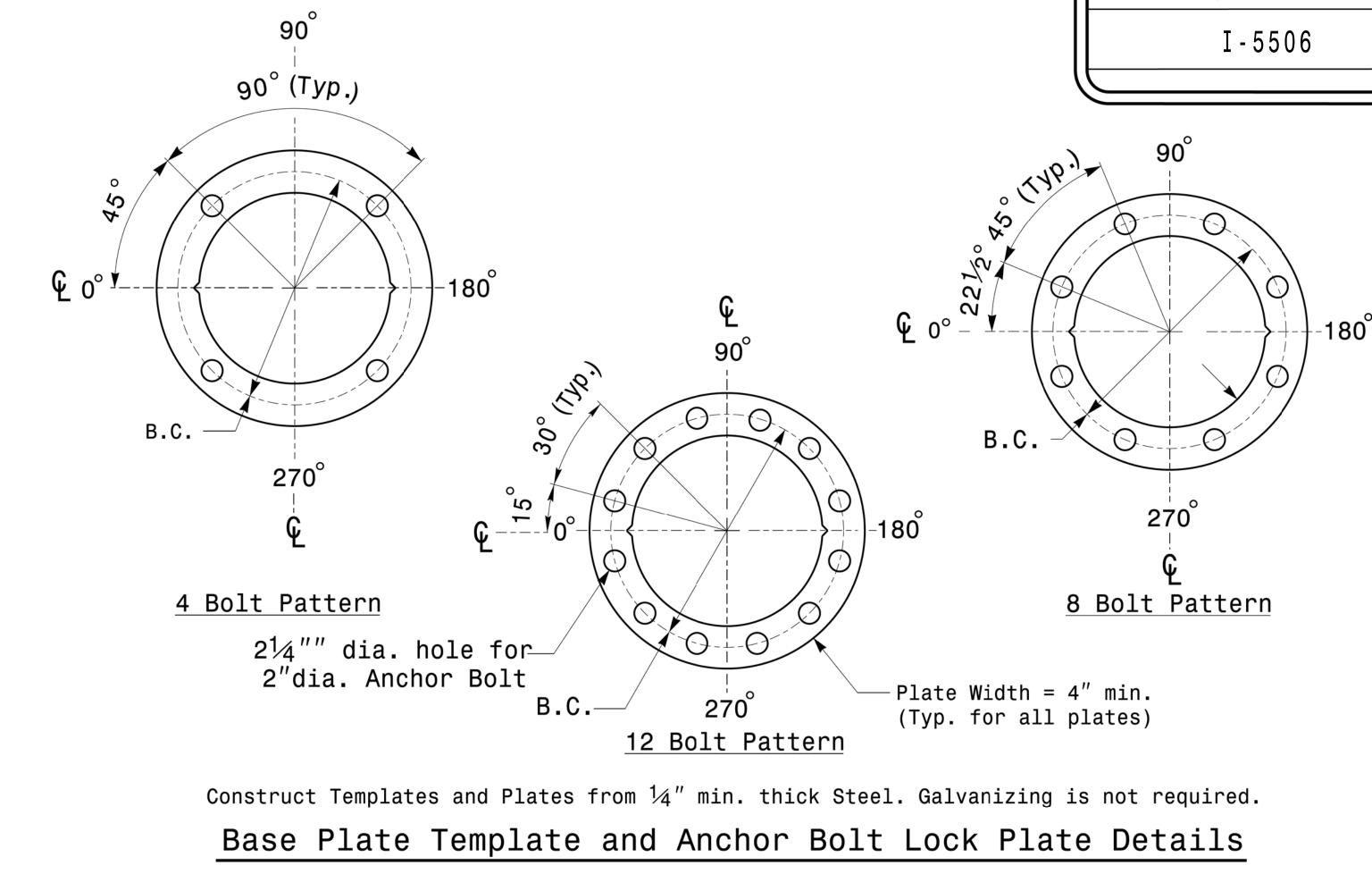
B.C. = Bolt Circle of Anchor Bolts

4) If Custom Design, use "NCDOT STANDARD" line for

Signal Inv. Number and pole I.D. number

Notes:

2) A.B. = Anchor Bolt



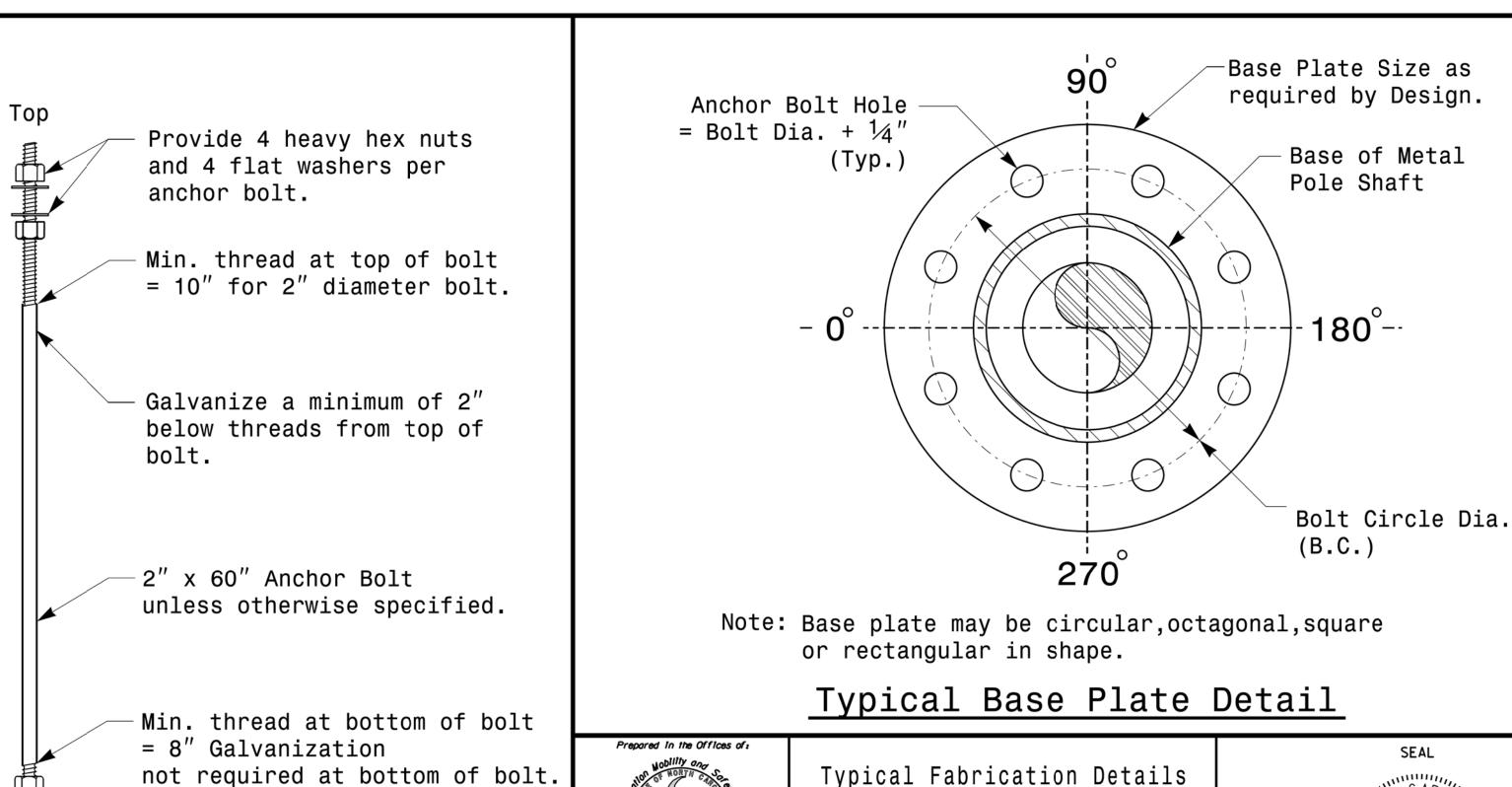
PROJECT ID. NO.

SHEET NO.

Sig.M2

-

eta



NONE

For

All Metal Poles

PLAN DATE: OCTOBER 2017 DESIGNED BY: C.F.ANDREWS
PREPARED BY: N. BITTING REVIEWED BY: D.C. SARKAR

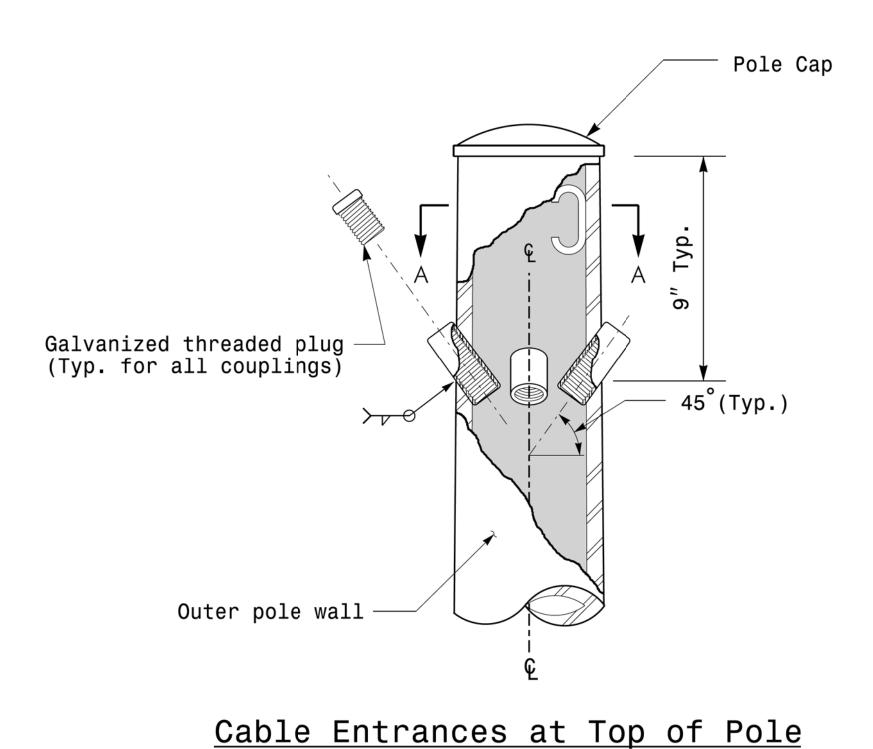
Debesh C. Sarkar

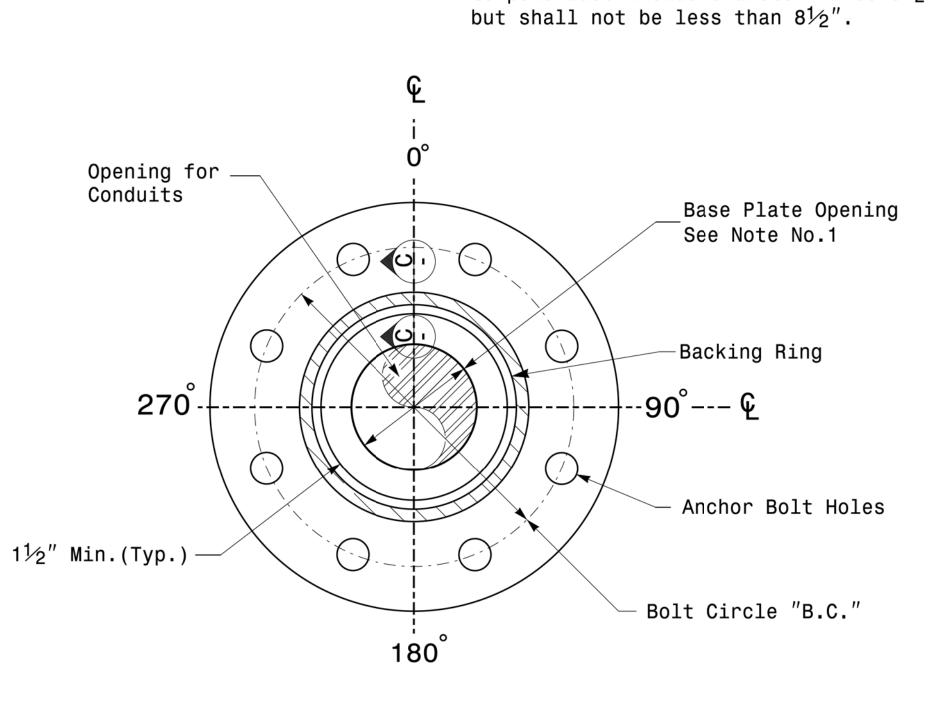
10/11/2017

Strail

eta

Fabrication





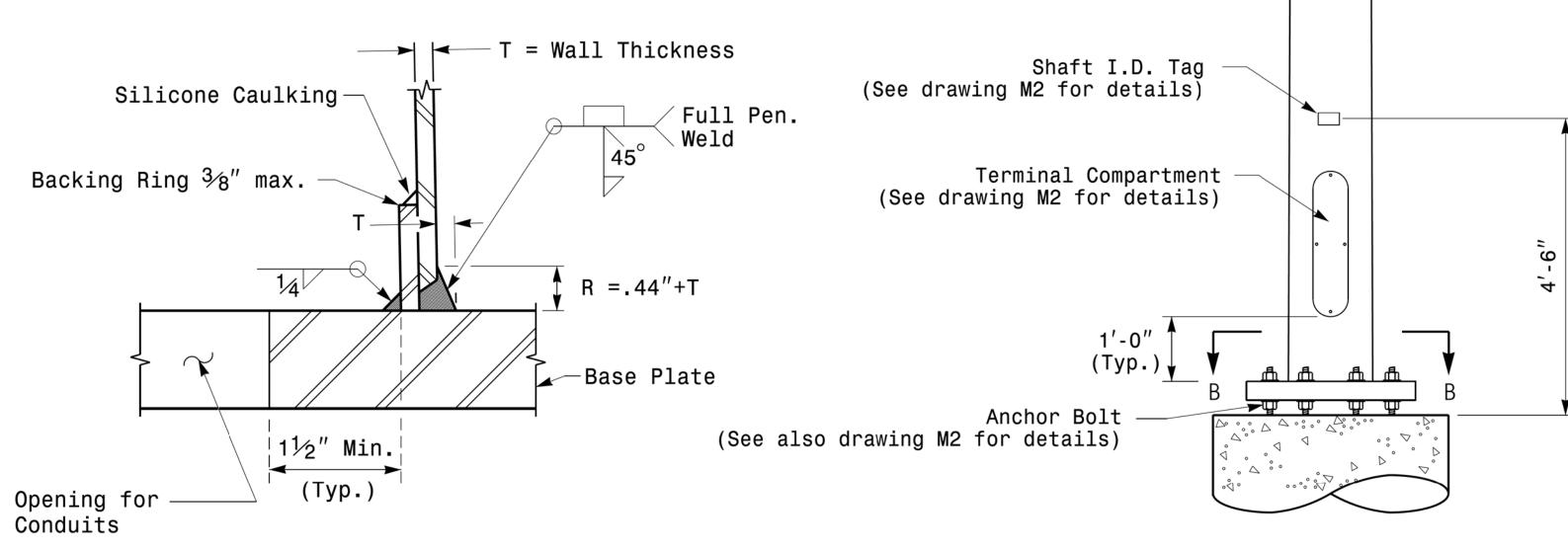
Note:

2 Cable Clamps designed for variable attachment heights from 1'-6" to 5'-0" below the top of the pole.

Section B-B

Pole Base Plate Details
(8 and 12 Bolt Pattern)

1.Opening in pole base plate shall be equal to pole base inside diameter minus  $3\frac{1}{2}$ "



2" Half Coupling with Internal Threads

2" Half Coupling "C" Hook @ 45° (Typ.)

1" Half Coupling with Internal Threads

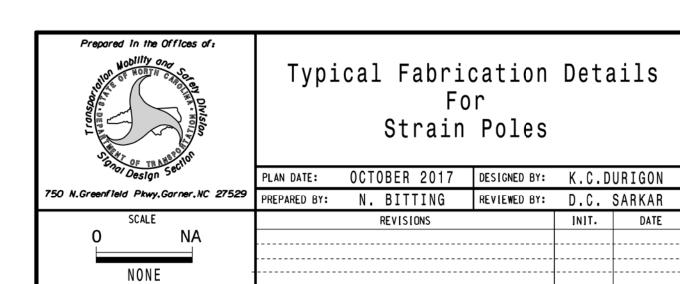
Section A-A

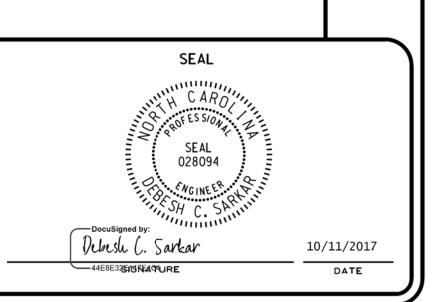
Radial Orientation for Factory Installed
Accessories at Top of Pole

Section C-C (Pole Attachment to Base Plate)

<u>Full-Penetration</u> <u>Groove Weld Detail</u>

<u>Monotube Strain Pole</u>





rnzinser