

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON

SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON

SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON

SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #43 ON

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL
(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: RED YELLOW GREEN
FLASH COLORS: RED YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: RED YELLOW GREEN
FLASH COLORS: RED YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-1418
DESIGNED: June 2017
SEALED: 8/30/2017
REVISED:

10-SEP-2017 1:54
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Electrical Detail - Sheet 2 of 2

	DETAILS FOR: US 301 (Brightleaf Boulevard) at Ava Gardner Street		
	Prepared In the Offices of: 	Division 4 Johnston County Smithfield	
PLAN DATE: August 2017	REVIEWED BY: T. Joyce	PREPARED BY: C. Strickland	REVIEWED BY:
REVISIONS	INIT.	DATE	DocuSign by: <i>T. Todd Joyce</i> 9/13/2017 DATE
SIG. INVENTORY NO. 04-1418			DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED