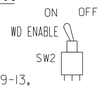
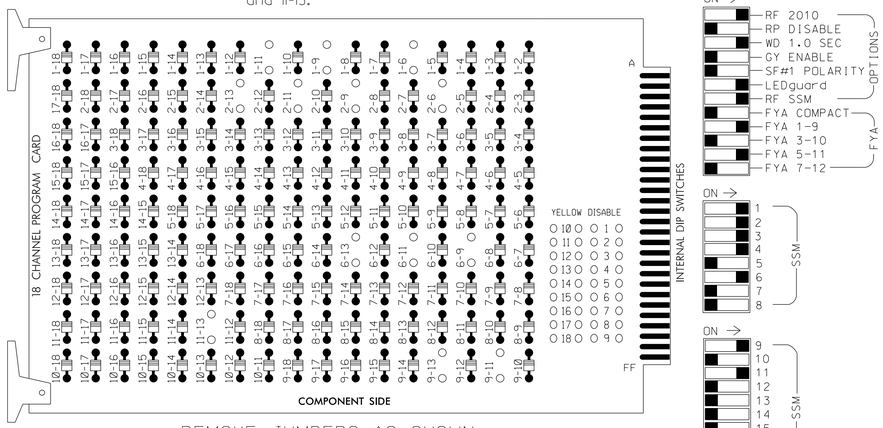
EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 2-6, 2-9, 2-11, 2-13, 6-9, 6-11, 6-13, 9-11, 9-13, and 11-13.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phase 2 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
7. The cabinet and controller are part of the Wilmington System.

EQUIPMENT INFORMATION

CONTROLLER.....EXISTING 2070E
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S8,AUX S1, AUX S4.
PHASES USED.....1,2,2 PED,3,4,6.
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....6
OVERLAP "D".....NOT USED

PROJECT REFERENCE NO.	SHEET NO.
U-4751	Sig. 2.1

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1		S2	S3	S4		S5		S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1		2	13	3		4		14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1		2	2 PED	3		4		4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11 ★	32	22,23	P21, P22	23	31	32	41	42	NU	NU	61,62	NU	NU	NU	NU	11 ★	NU	NU	21 ★	NU	NU
RED		✳	128			116	116	101	101			134									
YELLOW			129			117	117	102	102			135									
GREEN			130			118	118	103	103			136									
RED ARROW																A121			A114		
YELLOW ARROW		126			117											A122			A115		
FLASHING YELLOW ARROW																A123			A116		
GREEN ARROW	127	127			118	118		103													
✋				113																	
🚶				115																	

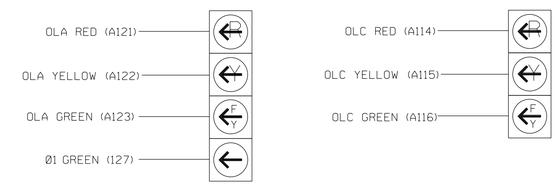
NU = Not Used

✳ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for signal head 11 requires special logic programming. See sheet 2 of 4 for programming instructions.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I" U	Ø 1 1A	Ø 1 1B	Ø 2 2B	SLOT EMPTY	Ø 3 3A	Ø 3 3B	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	SLOT EMPTY	FS DC ISOLATOR
FILE "I" L	NOT USED	Ø 2 2A	Ø 2 2C		NOT USED	Ø 4 4A						NOT USED		ST DC ISOLATOR
FILE "J" U	SLOT EMPTY	Ø 6 6A	SLOT EMPTY	WAFER ⊗ INCT	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
FILE "J" L		Ø 6 6B												

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10 ★	26	6	Y	Y	Y		3
	-	I1U	56	18 ★	51	1	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A	TB2-7,8	I2L	43	5	12	2	Y	Y			
2B	TB2-9,10	I3U	63	25	32	2	Y	Y			
2C	TB2-11,12	I3L	76	38	42	2	Y	Y	Y		3
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
3B	TB4-9,10	I6U	41	3	4	3	Y	Y			
4A	TB4-11,12	I6L	45	7	14	4	Y	Y			10
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					

NOTE:

INSTALL DC ISOLATORS IN INPUT FILE SLOT I12.

[1] Add jumper from I1-W to J4-W, on rear of input file.

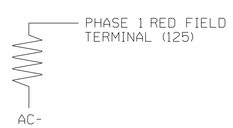
★ See Input Page Assignment programming details on sheet 3.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0882T1
DESIGNED: April 2017
SEALED:
REVISED:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Signal Upgrade - Temporary Design (Electrical Detail Sheet 1 of 4)

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:
Transportation Mobility and Safety Division
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signal Management Section
750 N.Greenfield Pkwy.Garner,NC 27529

SR 1409 (Military Cutoff Road)
at
SR 1940 (Covil Farm Road)

Division 03 New Hanover County Wilmington

PLAN DATE: April 2017	REVIEWED BY: MB Toth
PREPARED BY: AM Encarnacion	REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 025892 ENGINEER MELISSA B. TOTH

Digitally signed by Melissa B. Toth
SIGNATURE 6/6/2017 DATE

SIG. INVENTORY NO. 03-0882T1

ATKINS 1616 EAST MILLBROOK ROAD, SUITE 160 RALEIGH, NORTH CAROLINA 27609 (919) 876-6888 NCBEES #F-0326