

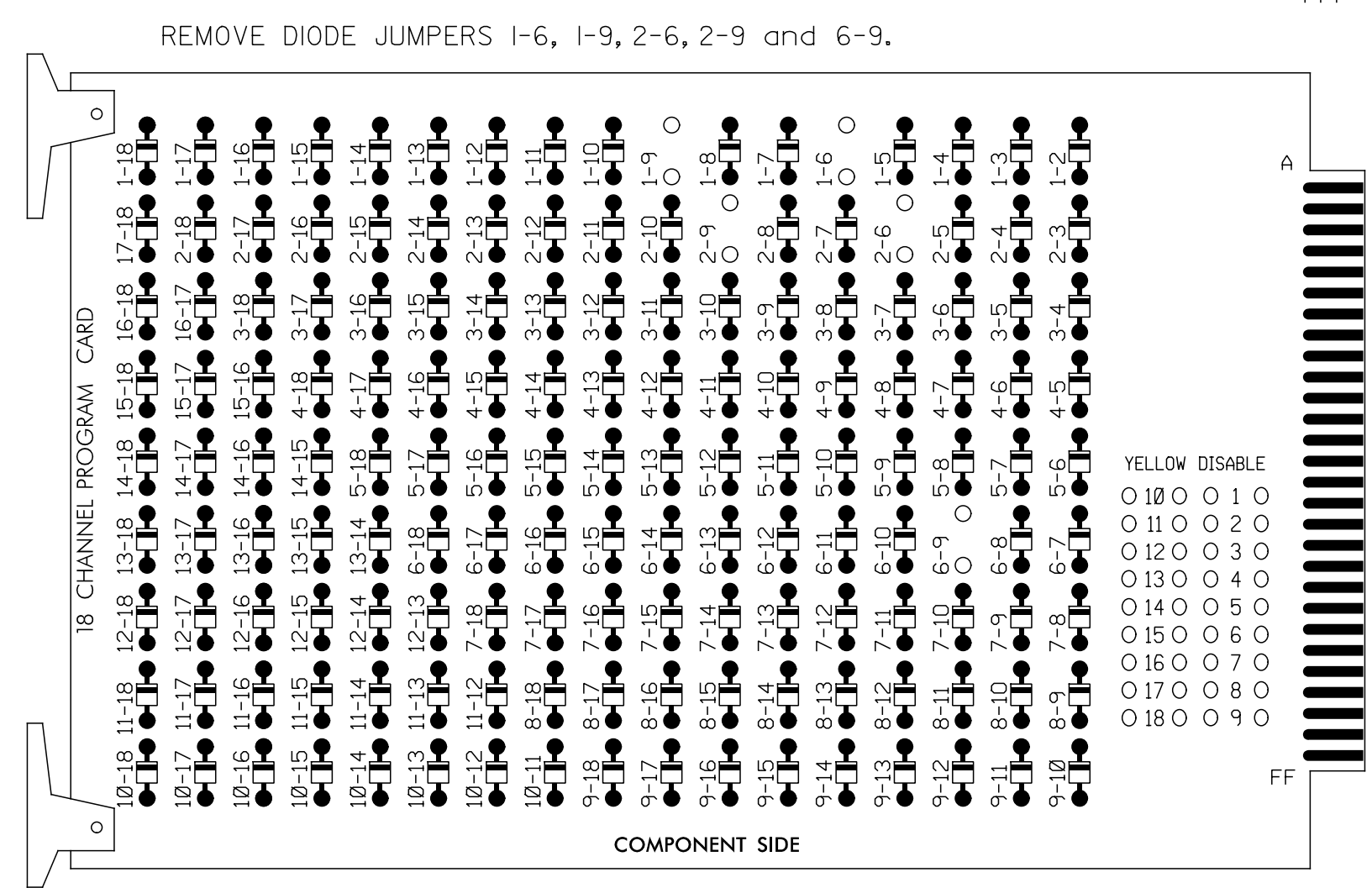
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EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the NC 210 Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S5,S8,AUX S1
 PHASES USED.....1,2,4,6
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

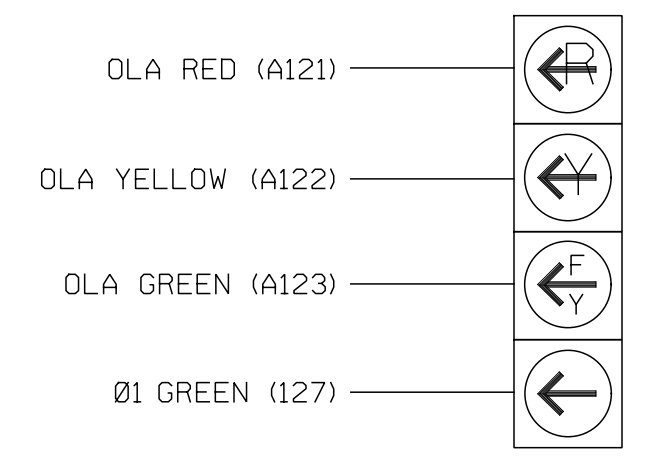
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CNU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21,22	NU	NU	41,42,43	NU	NU	61,62	NU	NU	NU	NU	11★	NU	NU	NU	NU	NU
RED		128			101			134										
YELLOW	*	129			102			135										
GREEN		130			103			136										
RED ARROW													A121					
YELLOW ARROW													A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW	127																	

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

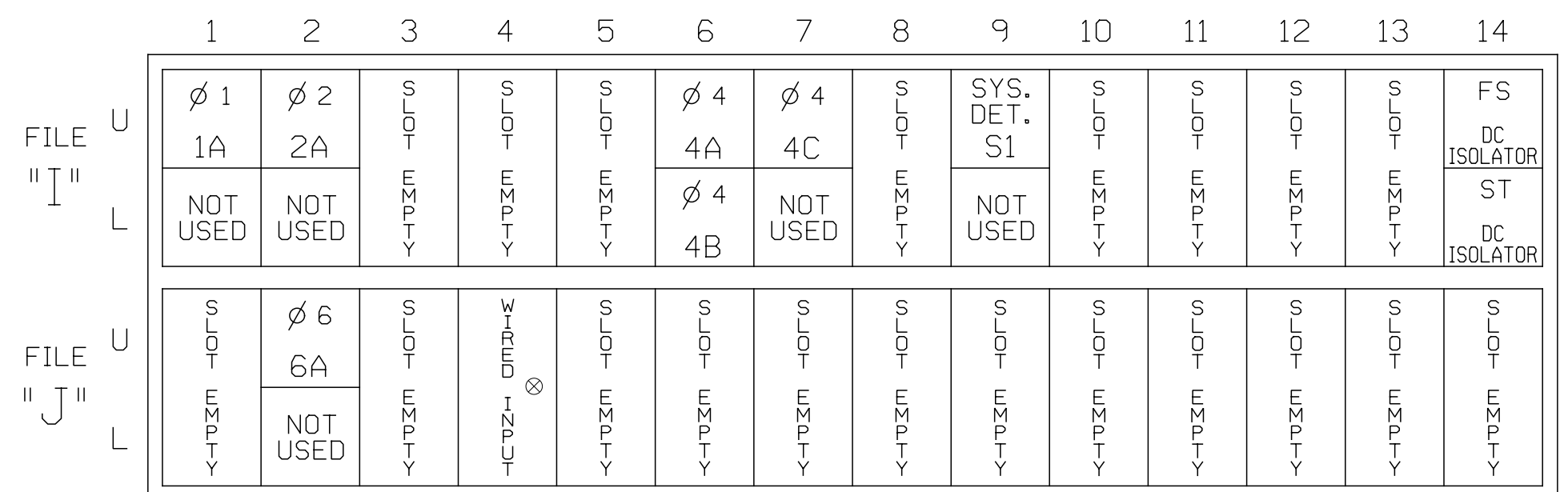


NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)



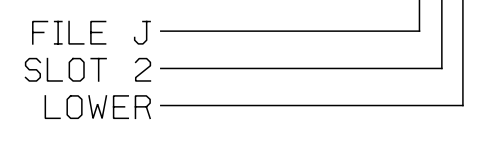
EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			15
4C	TB6-1,2	I7U	65	27	34	4	Y	Y			15
* S1	TB6-9,10	I9U	60	22	11	SYS					
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			

- Add jumper from I1-W to J4-W, on rear of input file.
- See Input Page Assignment programming detail on sheet 3.
- System detector only. Remove the vehicle phase assigned to this detector in the default programming.

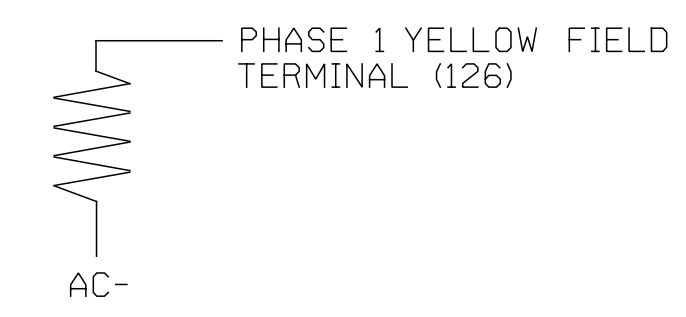
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



SEPI
 ENGINEERING & CONSTRUCTION
 1025 Wade Avenue
 Raleigh, NC 27605
 Tel: 919-789-9977
 Fax: 919-789-9591
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Electrical Detail - Sheet 1 of 4

ELECTRICAL AND PROGRAMMING DETAILS FOR:
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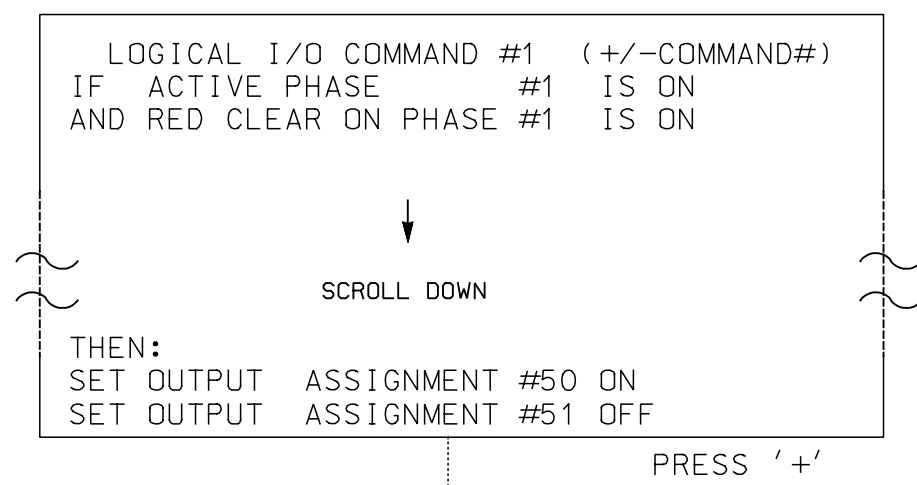
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NC 210
 at
 I-40 Eastbound Off/On Ramps
 Division 4 Johnston County Near Benson
 PLAN DATE: June 2017 REVIEWED BY: BL Johnson
 PREPARED BY: J. Kopaskie REVIEWED BY:
 REVISIONS INIT. DATE

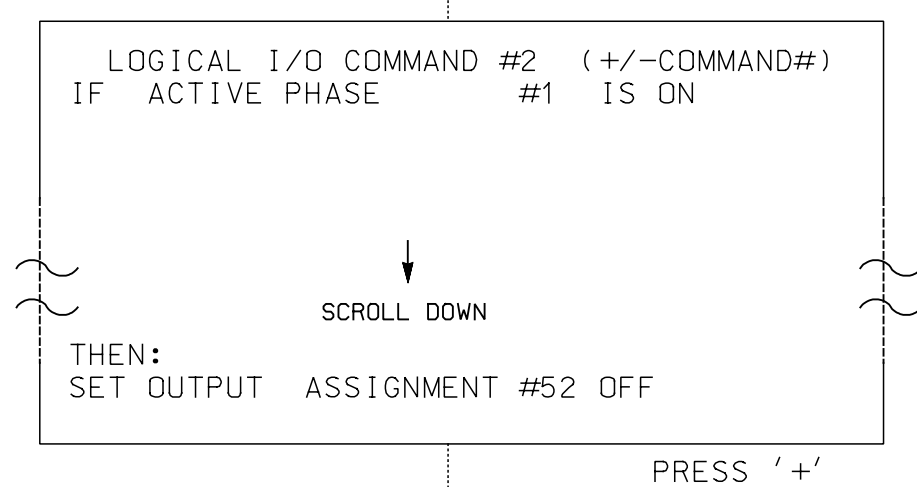
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 NORTH CAROLINA PROFESSIONAL ENGINEER
 JOSEPH B. KOPASKIE
 029669
 Date: 8/16/2017
 SIG. INVENTORY NO. 04-1088

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE (program controller as shown below)

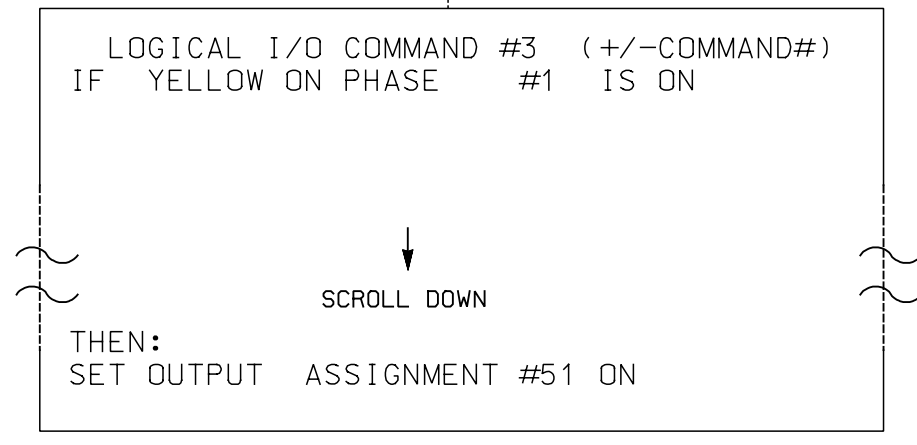
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE	
OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2

```

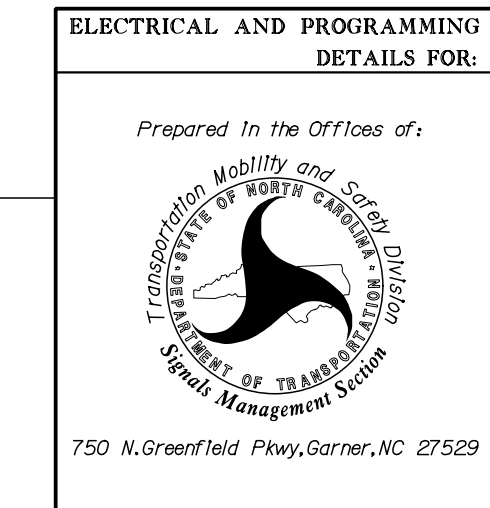
PAGE 2: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

OVERLAP PROGRAMMING COMPLETE

THESE PLANS SUPERSEDE
PLANS SEALED BY
JOSEPH B. KOPASKIE
ON 07-31-2017

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-1088
DESIGNED: June 2017
SEALED: 8/16/2017
REVISED:

Electrical Detail - Sheet 2 of 4



ELECTRICAL AND PROGRAMMING DETAILS FOR:	
Prepared In the Offices of:	
SEPI ENGINEERING & CONSTRUCTION 1025 Wade Avenue Raleigh, NC 27605 Tel: 919-789-9977 Fax: 919-789-9591 License: C-2197	
PLAN DATE: June 2017	REVIEWED BY: BL Johnson
PREPARED BY: J. Kopaskie	REVIEWED BY:
REVISIONS	INIT. DATE

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DocuSigned by: Joseph Kopaskie	8/16/2017
DATE	
SIG. INVENTORY NO. 04-1088	

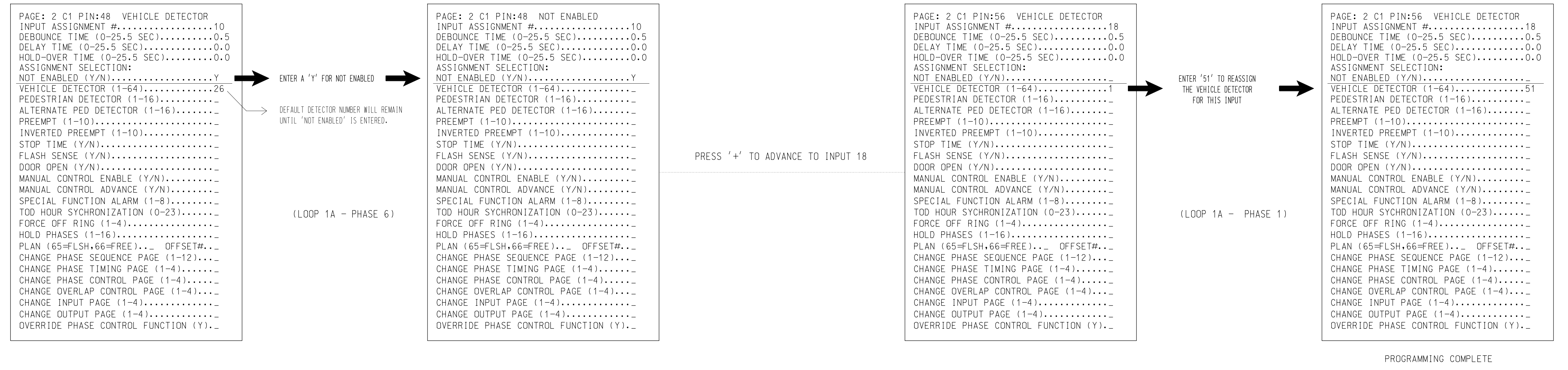
8/14/2017
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 J.Kopaskie

INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 3 SECONDS.

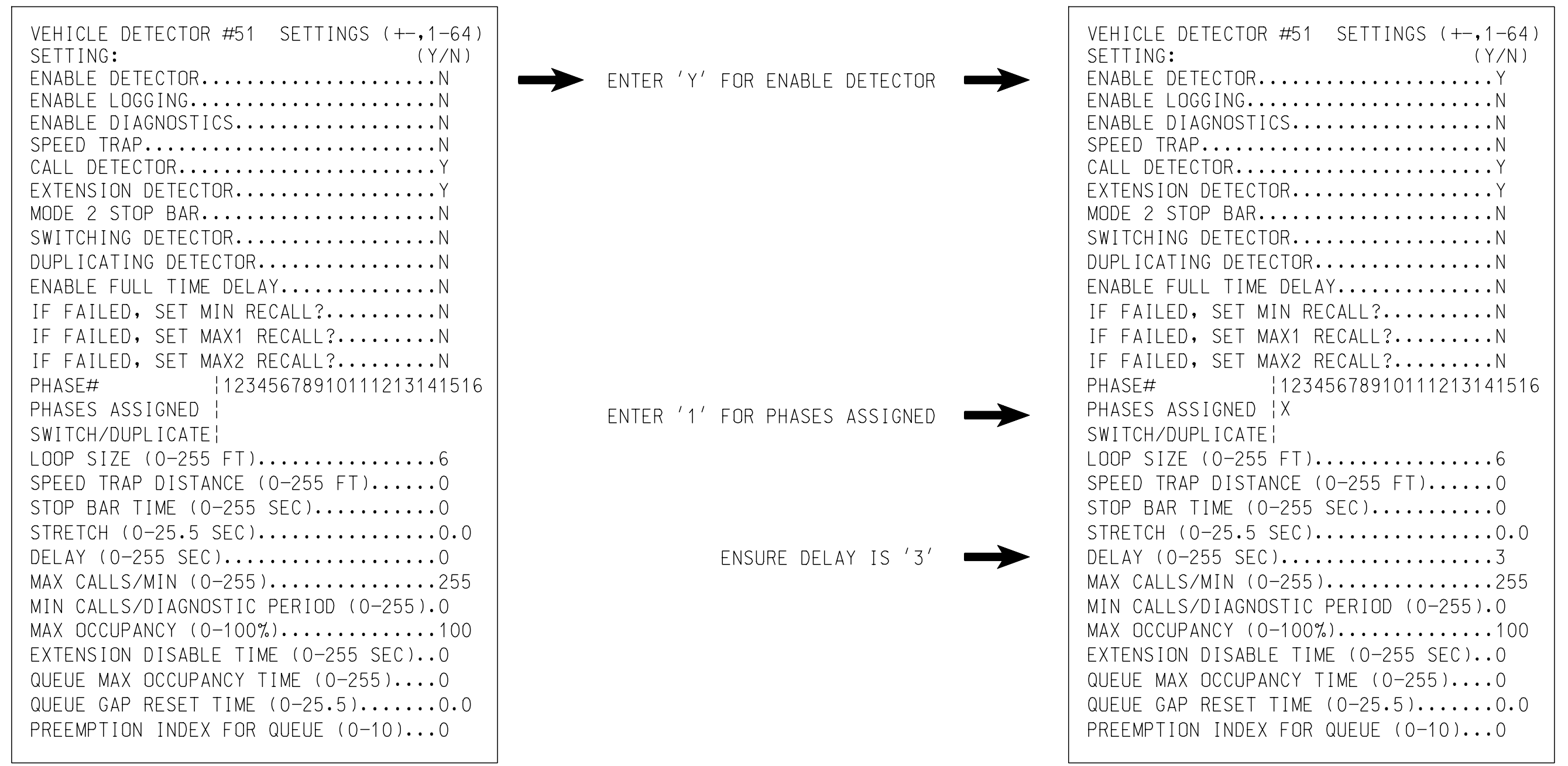
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.



SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THESE PLANS SUPERSEDE PLANS SEALED BY JOSEPH B. KOPASKIE ON 07-31-2017

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8/14/2017 10:56:00 I-5781-TP-TrfOff:ck45:gnols:DESIGN:04-1088(EL)_SIG-2_20170621.dgn J.Kopaskie

SEPI
ENGINEERING & CONSTRUCTION

1025 Wade Avenue
Raleigh, NC 27605
Tel:919-789-9977
Fax:919-789-9591
License: C-2197

Electrical Detail - Sheet 3 of 4

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NORTH CAROLINA PROFESSIONAL ENGINEER
JOSEPH B. KOPASKIE
029669

NC 210 at I-40 Eastbound Off/On Ramps

Division 4 Johnston County Near Benson

PLAN DATE: June 2017 REVIEWED BY: BL Johnson

PREPARED BY: J. Kopaskie REVIEWED BY:

REVISIONS INIT. DATE

Designed by Joseph Kopaskie 8/16/2017

SIG. INVENTORY NO. 04-1088

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

<u>PHASING</u>	<u>INPUTS PAGE</u>	<u>OVERLAPS PAGE</u>
ACTIVE PAGES REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	1
ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	2

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAPS PAGE 2: Modifies overlap parent phases for head 11 to run protected turns only.

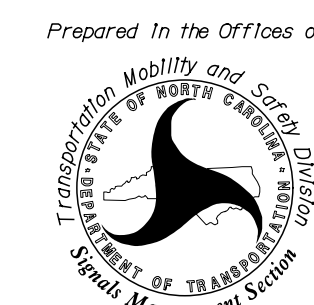
INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.

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JOSEPH B. KOPASKIE
ON 07-31-2017**

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THE SIGNAL DESIGN: 04-1088
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Electrical Detail - Sheet 4 of 4

**ELECTRICAL AND PROGRAMMING
DETAILS FOR:**



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