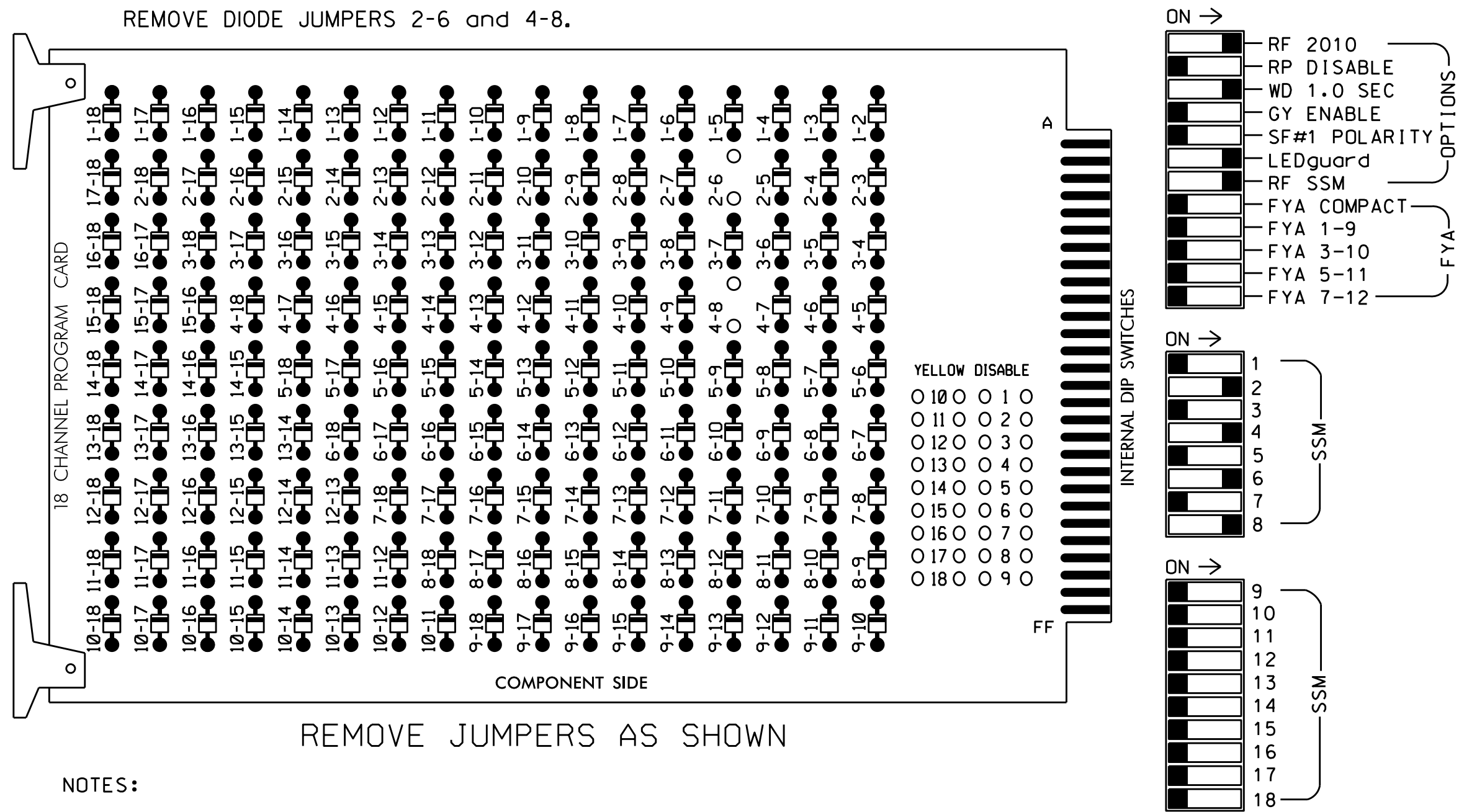


**EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Startup In Green.
6. Program phases 2 and 6 for Yellow Flash.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....336  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....POLE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S2,S5,S8,S11  
 PHASES USED.....2,4,6,8  
 OVERLAPS.....NONE

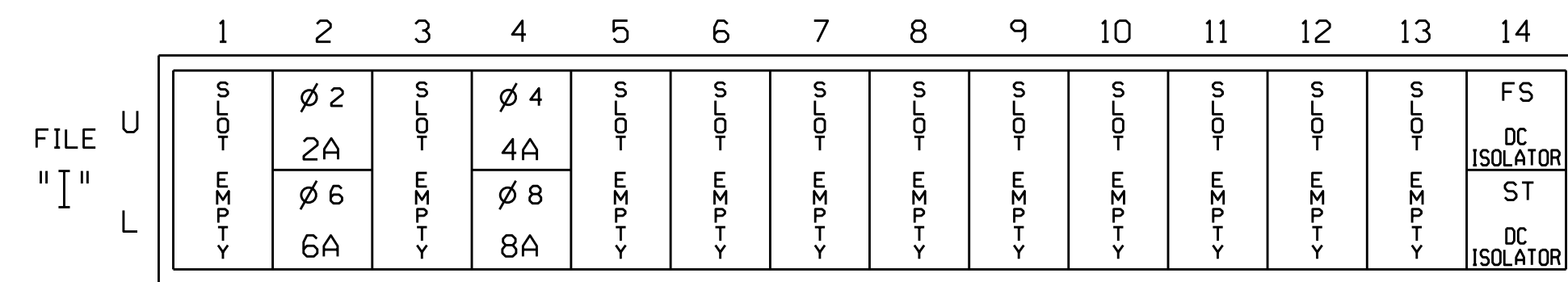
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	NU	61,62	NU	NU	81,82	NU
RED		128			101			134			107	
YELLOW		129			102			135			108	
GREEN		130			103			136			109	
RED ARROW												
YELLOW ARROW												
GREEN ARROW												

NU = Not Used

**INPUT FILE POSITION LAYOUT**

(front view)



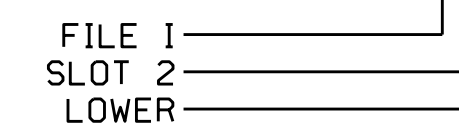
EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB21-3,4	I2U	39	1	2	2	Y	Y			
6A	TB23-3,4	I2L	43	5	12	6	Y	Y			
4A	TB21-7,8	I4U	41	3	4	4	Y	Y			5
8A	TB23-7,8	I4L	45	7	14	8	Y	Y			5

INPUT FILE POSITION LEGEND: I2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1582  
 DESIGNED: March 2017  
 SEALED: 3-23-17  
 REVISED: N/A

**Electrical Detail**

Electrical and Programming Details for:

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

NC 54 at SR 1944 (Hatch Road)

Division 7 Orange County Near Calvander

PLAN DATE: March 2017 REVIEWED BY: DTJ

PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: Keith M. Minis 3/24/2017 2:07:06 PM

SIG. INVENTORY NO. 07-1582

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SEAL 036880

KEITH M. MINIS ENGINEER

24-MAR-2017 09:50  
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 J.peterson