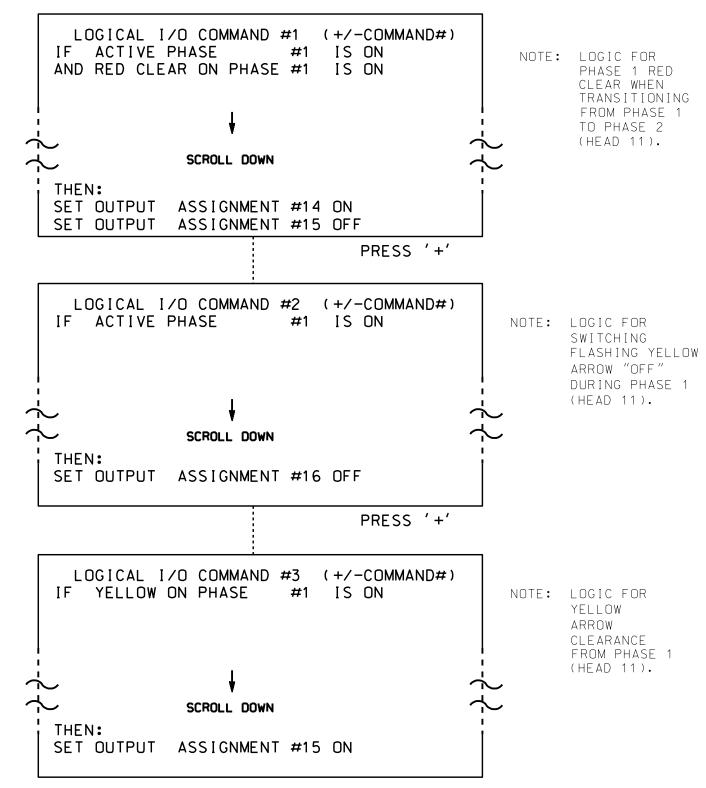
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

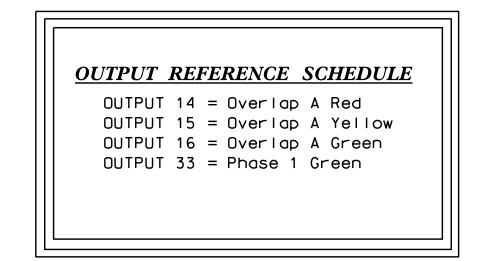
## TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 and 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

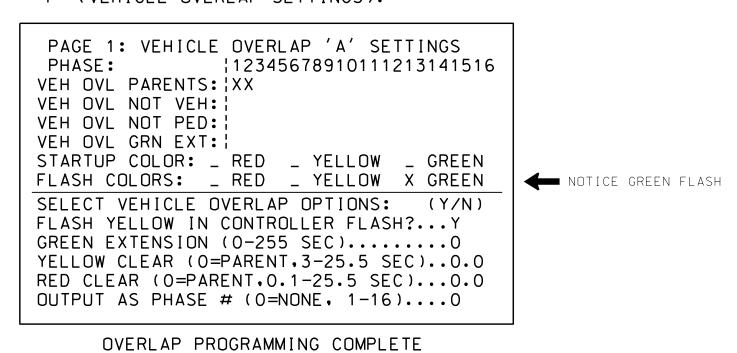


Note: All outputs shown above have been remapped. See sheet 3 of this electrical detail.

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

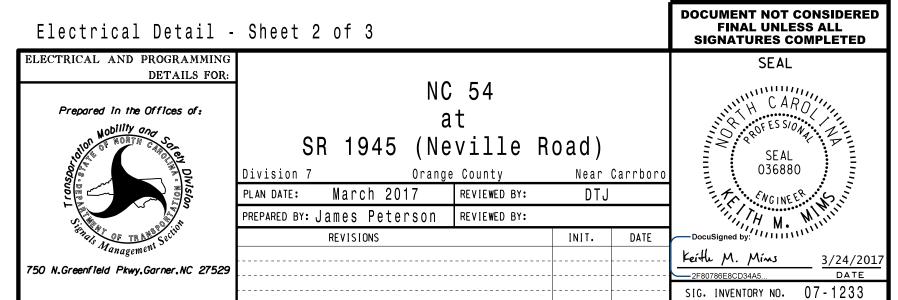
FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



DESIGNED: March 2017 SEALED: 3-23-17 REVISED: N/A

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 07-1233



S:\*IIS&SU\*IIS SIGNAIS\*WORKGroups\*SIG MAN\*Meterson\*U/1299\_SM\_ele\_xxx jtpeterson