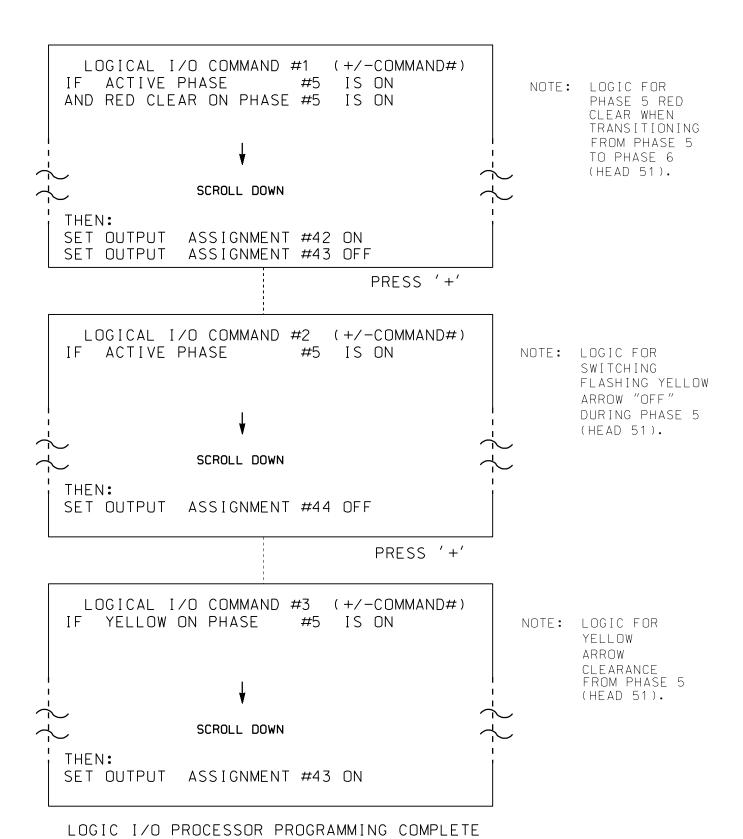
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

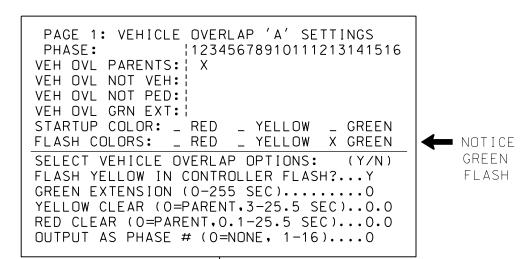


OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green

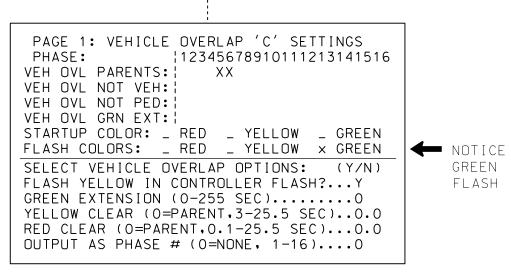
OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



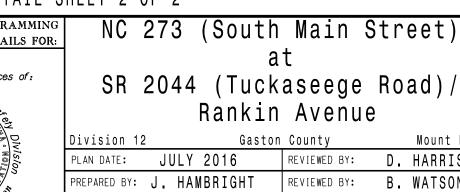
PRESS '+' TWICE



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-0538-T1 DESIGNED: July 2016 SEALED: 10/4/2016 REVISED:

Signal Upgrade Temporary Design 1 - TMP Phases 1 & O1A ELECTRICAL DETAIL SHEET 2 OF 2



INIT. DATE Betsy L. Watson 10/4/2016

SIG. INVENTORY NO. 12-0538-T1

Stantec Consulting Services Inc.

801 Jones Franklin Road

Raleigh, NC 27606

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of:

Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com License No. F-0672

Suite 300

D. HARRIS PREPARED BY: J. HAMBRIGHT REVIEWED BY: B. WATSON REVISIONS