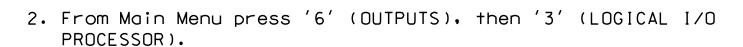
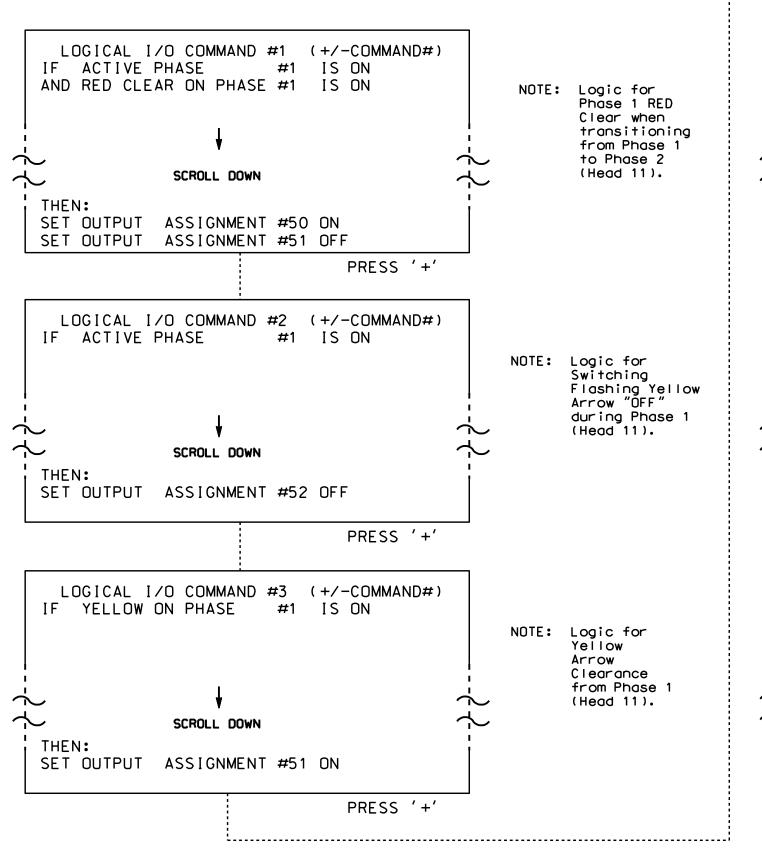
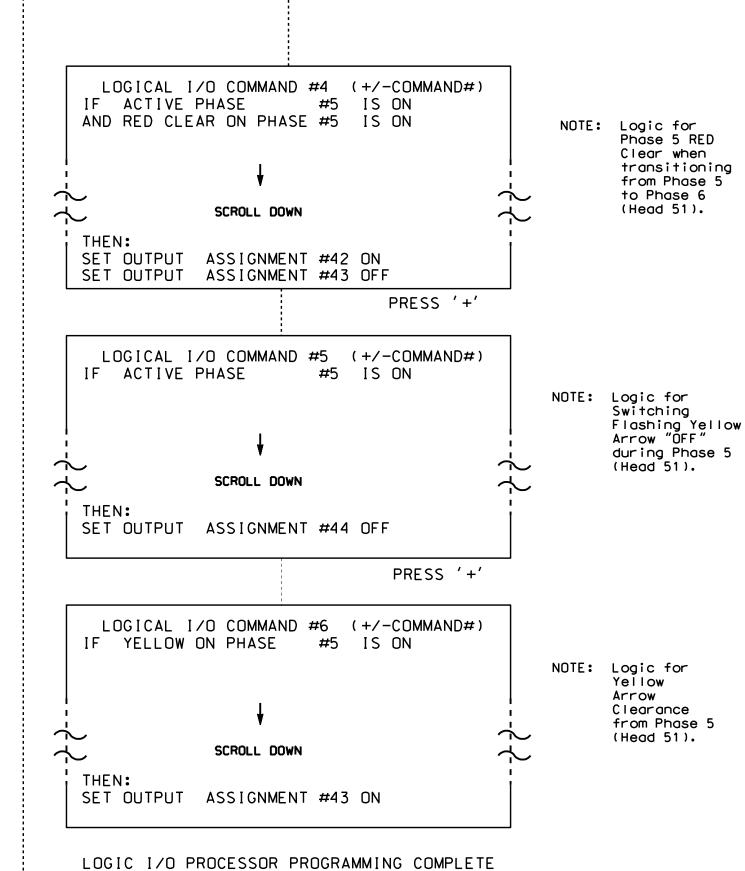
(program controller as shown below)

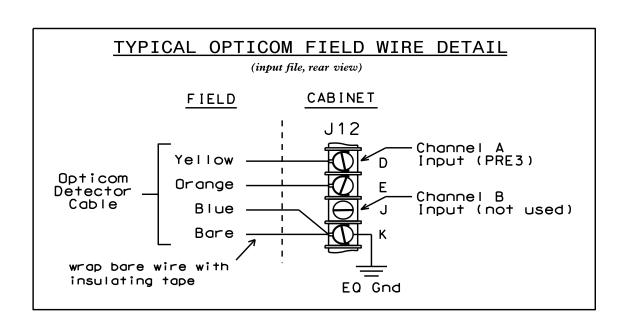
1. From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2, 3, 4, 5 and 6.

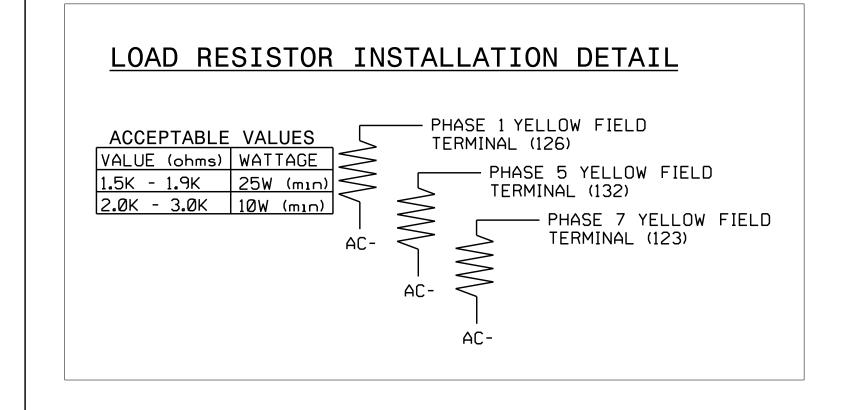






OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green



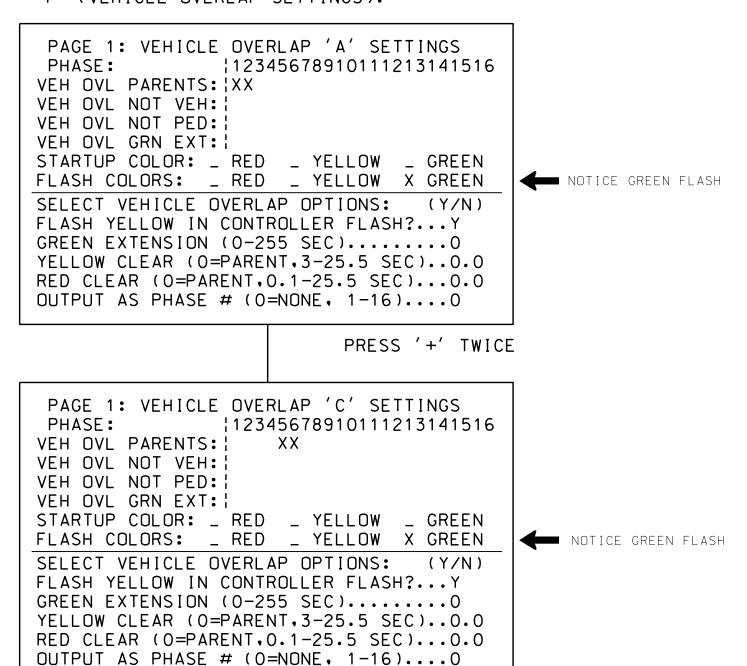


PROJECT REFERENCE NO. Sig. 2.2 R-5516

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

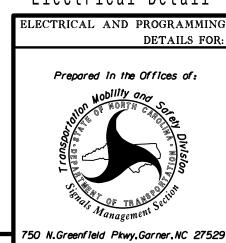
From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 02-0563T DESIGNED: October 2016 SEALED: 2/2/2017 REVISED: N/A

Electrical Detail - Temp Design (Phase II) - Sheet 2 of 3



SR 1761 (Hickman Hill Loop Rd.) SR 1772 (Pine Grove Rd.)

US 70

ivision 2 PLAN DATE: January 2017 REVIEWED BY: PREPARED BY: B. SIMMONS REVIEWED BY: REVISIONS INIT. DATE

SIG. INVENTORY NO. 02-0563T

SEAL

030530

DOCUMENT NOT CONSIDERED FINAL **UNLESS ALL SIGNATURES COMPLETED**