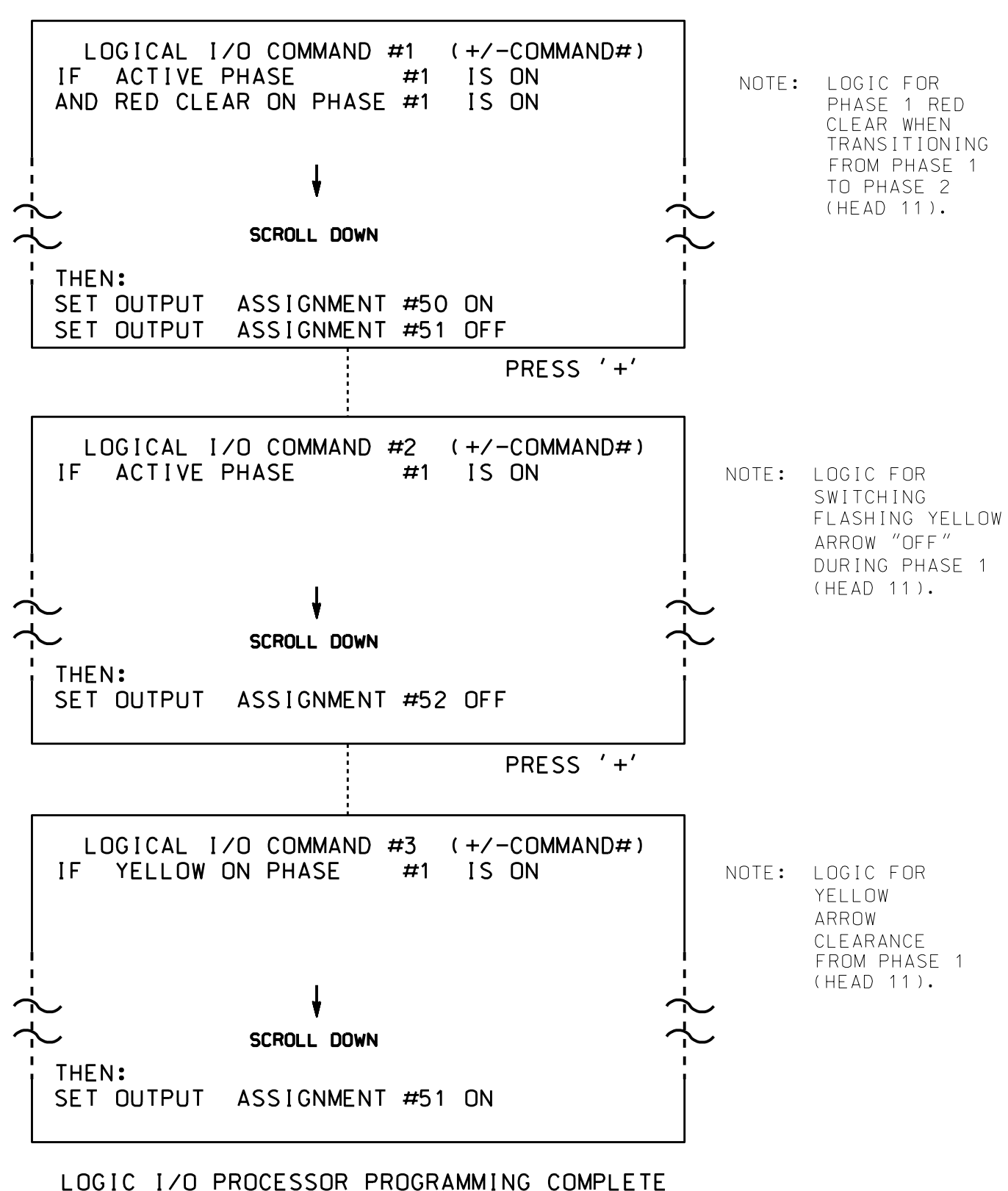


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



**OUTPUT REFERENCE SCHEDULE**

OUTPUT 50	= Overlap A Red
OUTPUT 51	= Overlap A Yellow
OUTPUT 52	= Overlap A Green

**OVERLAP PROGRAMMING DETAIL**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: :12345678910111213141516  
VEH OVL PARENTS:|XX  
VEH OVL NOT VEH:|  
VEH OVL NOT PED:|  
VEH OVL GRN EXT:|  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS  
PHASE: :12345678910111213141516  
VEH OVL PARENTS:|X X  
VEH OVL NOT VEH:|  
VEH OVL NOT PED:|  
VEH OVL GRN EXT:|  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW - GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...N  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: :12345678910111213141516  
VEH OVL PARENTS:| X  
VEH OVL NOT VEH:|  
VEH OVL NOT PED:|  
VEH OVL GRN EXT:|  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

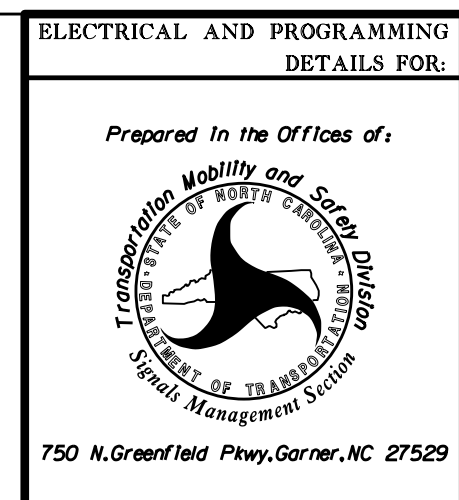
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1555  
DESIGNED: February 2017  
SEALED: 3/1/2017  
REVISED:

THIS ELECTRICAL DETAIL SUPERSEDES THE DETAIL SEALED ON 01/24/17



SR 1921 (Mebane Rogers Road) at NC 119

Division 7	Alamance County	Mebane
PLAN DATE: February 2017	REVIEWED BY: T. Joyce	
PREPARED BY: C. Strickland	REVIEWED BY:	
REVISIONS	INIT.	DATE

DocuSigned by:  
*Gregory M. Little*  
3/6/2017  
0021EFD04F5341F  
DATE  
SIG. INVENTORY NO. 07-1555

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