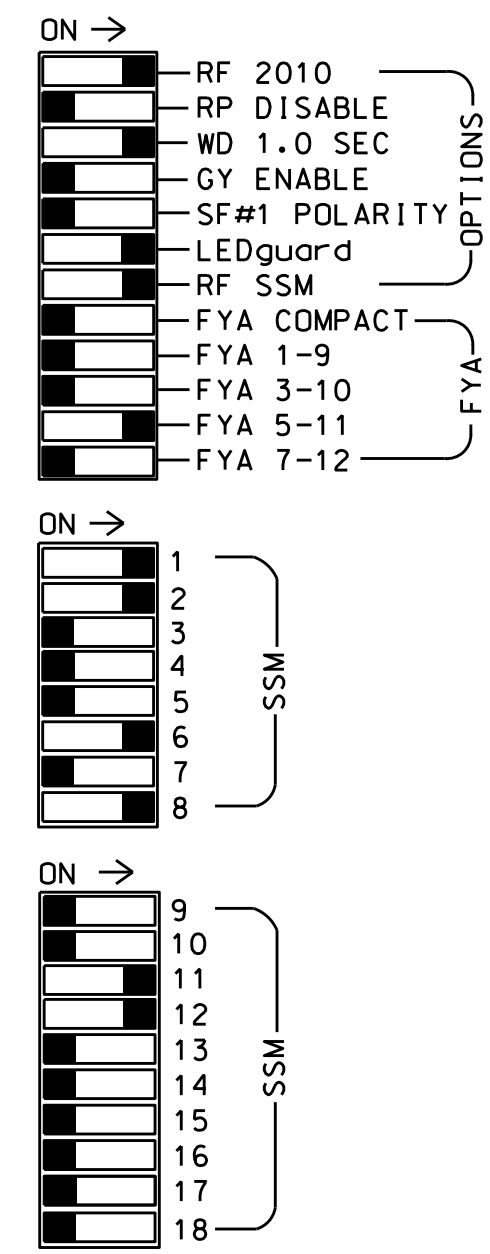
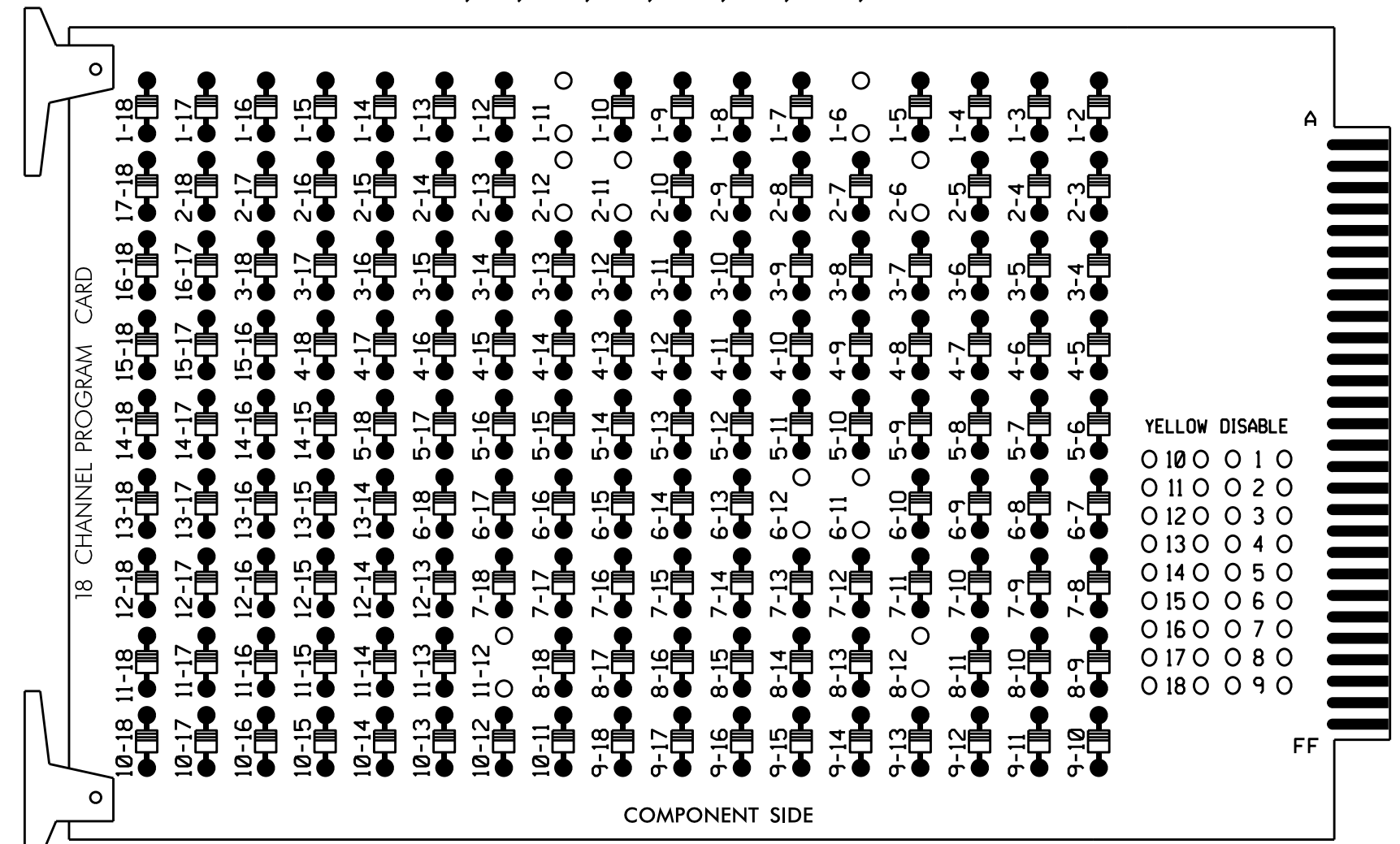


**EDI MODEL 2018ECL-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-11, 2-6, 2-11, 2-12, 6-11, 6-12, 8-12 and 11-12.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S4,AUX S5  
 PHASES USED.....1,2,6,8  
 OVERLAP "A".....NOT USED  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....6  
 OVERLAP "D".....2+8

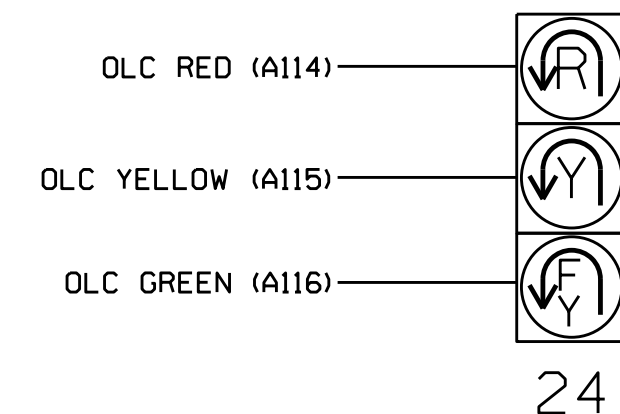
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	82	21,22	NU	NU	NU	NU	61,62	NU	NU	81,82	NU	NU	NU	NU	24*	23	NU
RED			128						134			107						A101
YELLOW			129						135			108						
GREEN			130						136			109						
RED ARROW	125																	A114
YELLOW ARROW	126	126																A115 A102
FLASHING YELLOW ARROW																		A116
GREEN ARROW	127	127																A103

NU = Not Used  
 \* See pictorial of head wiring in detail below.

**FYA SIGNAL WIRING DETAIL**

(wire signal head as shown)



**INPUT FILE POSITION LAYOUT**

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1 1A	∅ 2 2A	∅ 2 2C	∅ S	∅ S	∅ S	∅ S	∅ S	∅ 1 1B	∅ S	∅ S	∅ S	∅ S	FS DC ISOLATOR
L	NOT USED	∅ 2 2B	NOT USED	∅ S	∅ S	∅ S	∅ S	∅ S	∅ 1 1C	∅ S	∅ S	∅ S	∅ S	DC ISOLATOR
U	∅ S	∅ 6 6A	∅ S	∅ S	∅ S	∅ 8 8A	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S
L	∅ S	∅ 6 6B	∅ S	∅ S	∅ S	NOT USED	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S

EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME

**OVERLAP PROGRAMMING DETAIL**

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS:  X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED _ YELLOW _ GREEN
FLASH COLORS:  _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

```

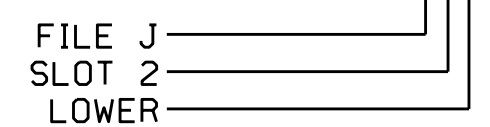
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS:  X X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED _ YELLOW _ GREEN
FLASH COLORS:  _ RED _ YELLOW _ GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

OVERLAP PROGRAMMING COMPLETE

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	I1U	56	18	1	1	Y	Y			
1B	TB6-9,10	I9U	60	22	11	1	Y	Y			
1C	TB6-11,12	I9L	62	24	13	1	Y	Y			15
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
2C	TB2-9,10	I3U	63	25	32	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3

INPUT FILE POSITION LEGEND: J2L



**Electrical Detail**

Electrical and Programming Details for NC 119 at James Walker Road. Prepared by C. Strickland, reviewed by T. Joyce. Includes professional seal of Zachary M. Little, Engineer, State of North Carolina. Document not considered final unless all signatures completed.

23-1116-2017\_08-25  
 C:\MITSAS\1116\1116\_Signal\work\hgc\p\51g\_Maps\1116\1116\_Smle\_01060\_sml\_e\_001.dgn  
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