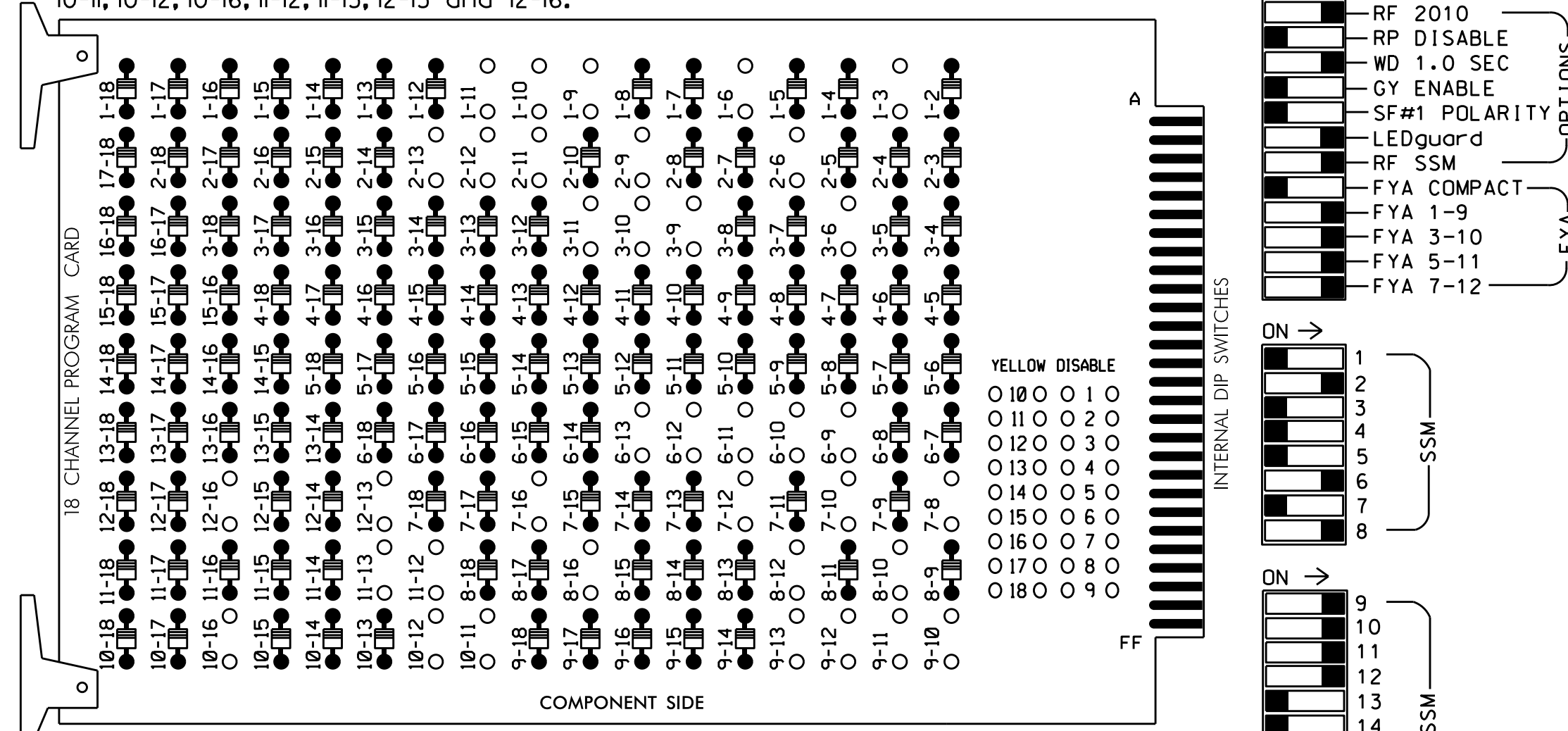


### EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-3, 1-6, 1-9, 1-10, 1-11, 2-6, 2-9, 2-11, 2-12, 2-13, 3-6, 3-9, 3-10, 3-11, 6-9, 6-10, 6-11, 6-12, 6-13, 7-8, 7-10, 7-12, 7-16, 8-10, 8-12, 8-16, 9-10, 9-11, 9-12, 9-13, 10-11, 10-12, 10-16, 11-12, 11-13, 12-13 and 12-16.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 8 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the NC 119 Closed Loop System.

### EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S4,S8,S10,S11,S12,AUX S1,  
 AUX S2,AUX S4,AUX S5  
 PHASES USED.....1,2,2 PED,6,8,8 PED  
 OVERLAP "A".....1+2  
 OVERLAP "B".....1+8  
 OVERLAP "C".....6  
 OVERLAP "D".....2+8  
 OVERLAP "E".....NOT USED  
 OVERLAP "F".....NOT USED  
 OVERLAP "G".....1  
 OVERLAP "H".....8

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	OLG	4	4 PED	5	6	6 PED	OLH	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	12	NU	NU	NU	61, 62,63	NU	23	81,82	P81, P82	11	12	NU	24	23	NU
RED		128						134					A124					A101
YELLOW	*	129		*				135		*								
GREEN		130						136										
RED ARROW												107	A121					A114
YELLOW ARROW												108	A122	A125				A115 A102
FLASHING YELLOW ARROW													A123	A126				A116 A103
GREEN ARROW	127									124	109							
Hand icon				113														110
Walking person icon				115														112

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

### INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS	∅ 2/SYS
L	1A	1B	2B/S13	∅ 2/SYS	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2
U	NOT USED	∅ 2/SYS	∅ 2	∅ 2/SYS	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2
L	2A/S12	2C	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2	∅ 2

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

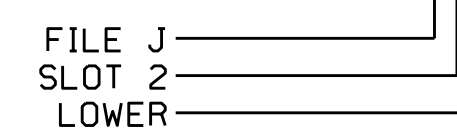
### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A'	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
2A/S12	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
2B/S13	TB2-9,10	I3U	63	25	32	2/SYS	Y	Y			
2C	TB2-11,12	I3L	76	38	42	2	Y	Y	Y		3
6A/S14	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S15	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
6C/S16	TB3-9,10	J3U	64	26	36	6/SYS	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:  
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

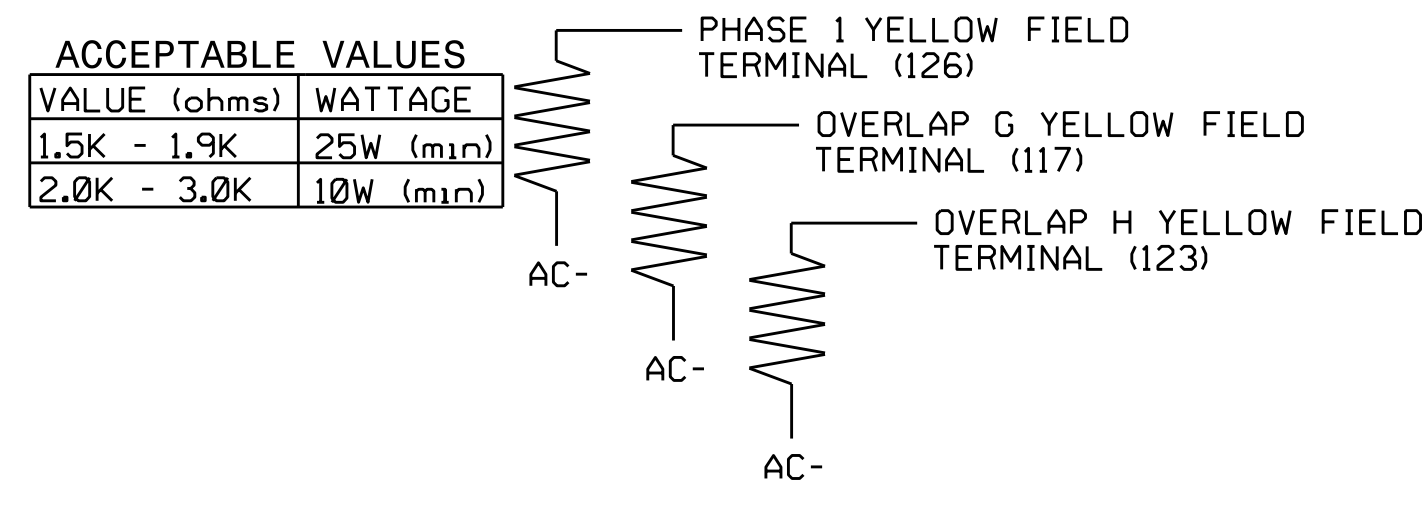
† Add jumper from I1-W to J4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



### LOAD RESISTOR INSTALLATION DETAIL

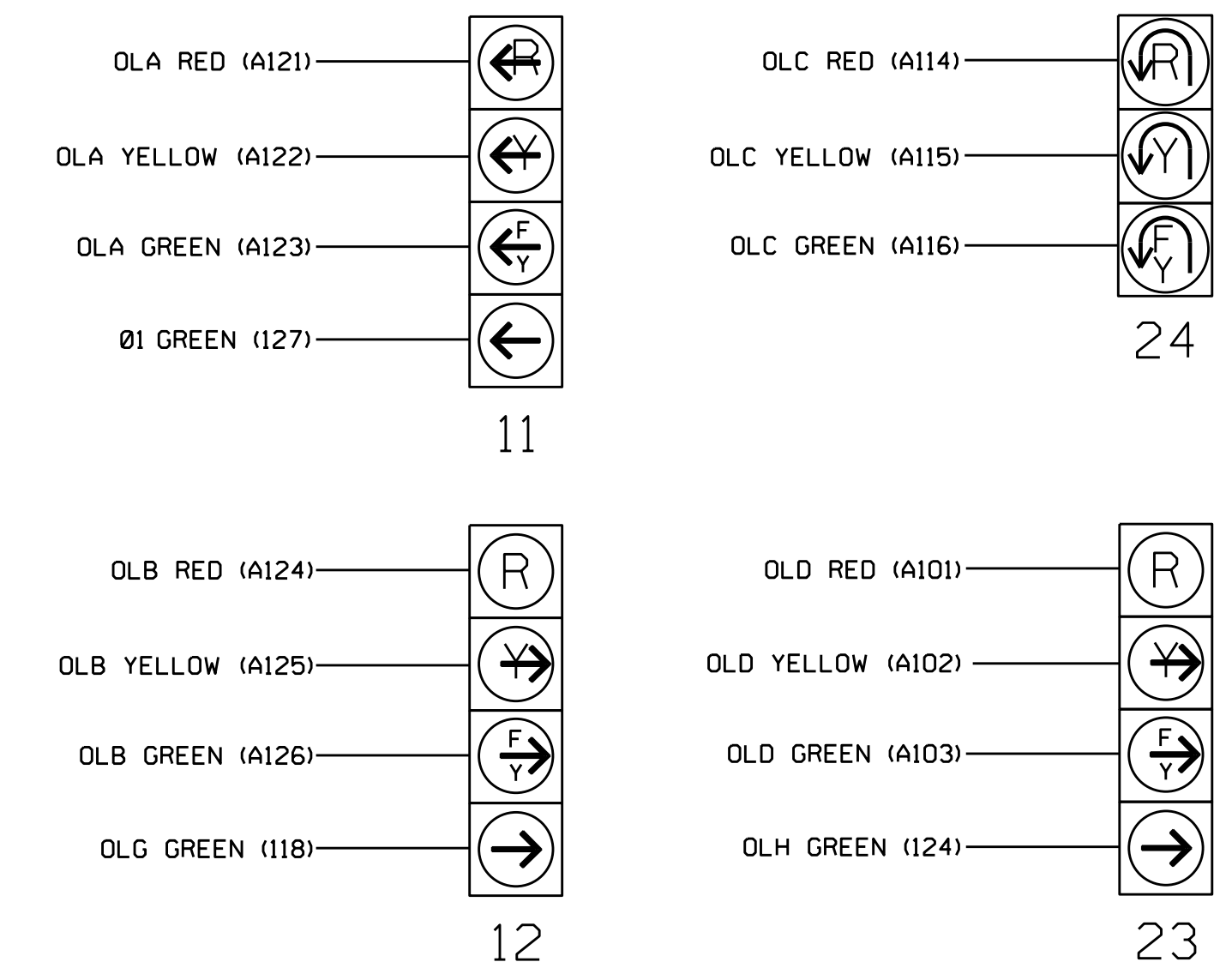
(install resistors as shown below)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-0443  
 DESIGNED: November 2016  
 SEALED: 1/20/2017  
 REVISED:

### FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



**NOTE**

1. The sequence display for signal heads 11, 12 and 23 require special logic programming. See sheet 2 for programming instructions.

### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	NC 119 at SR 1962 (3rd Street)		SEAL  ZACHARY M. LITTLE ENGINEER
	Division 7 Alamance County Webahe PLAN DATE: December 2016 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY:	REVISIONS INIT. DATE	