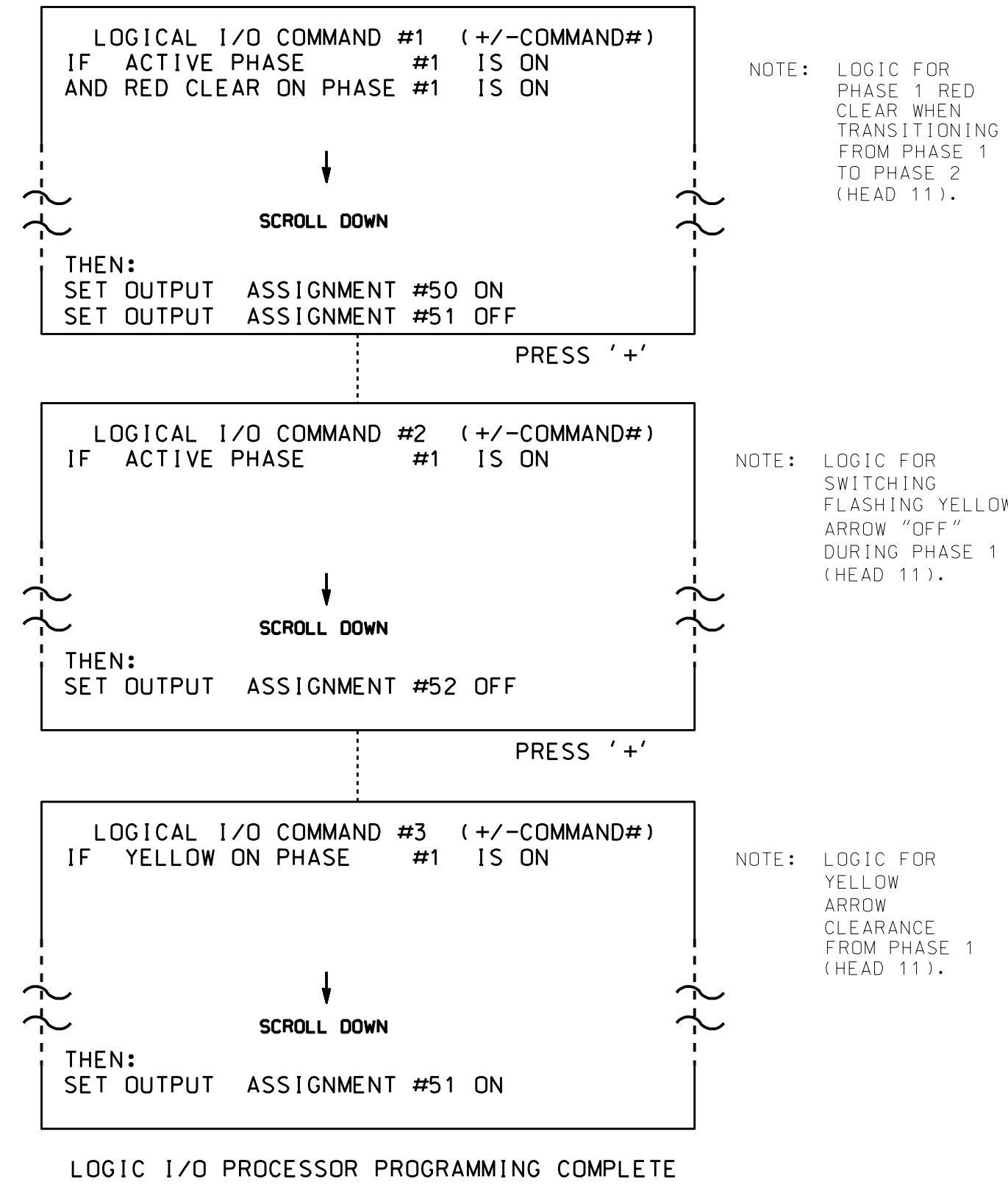


**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



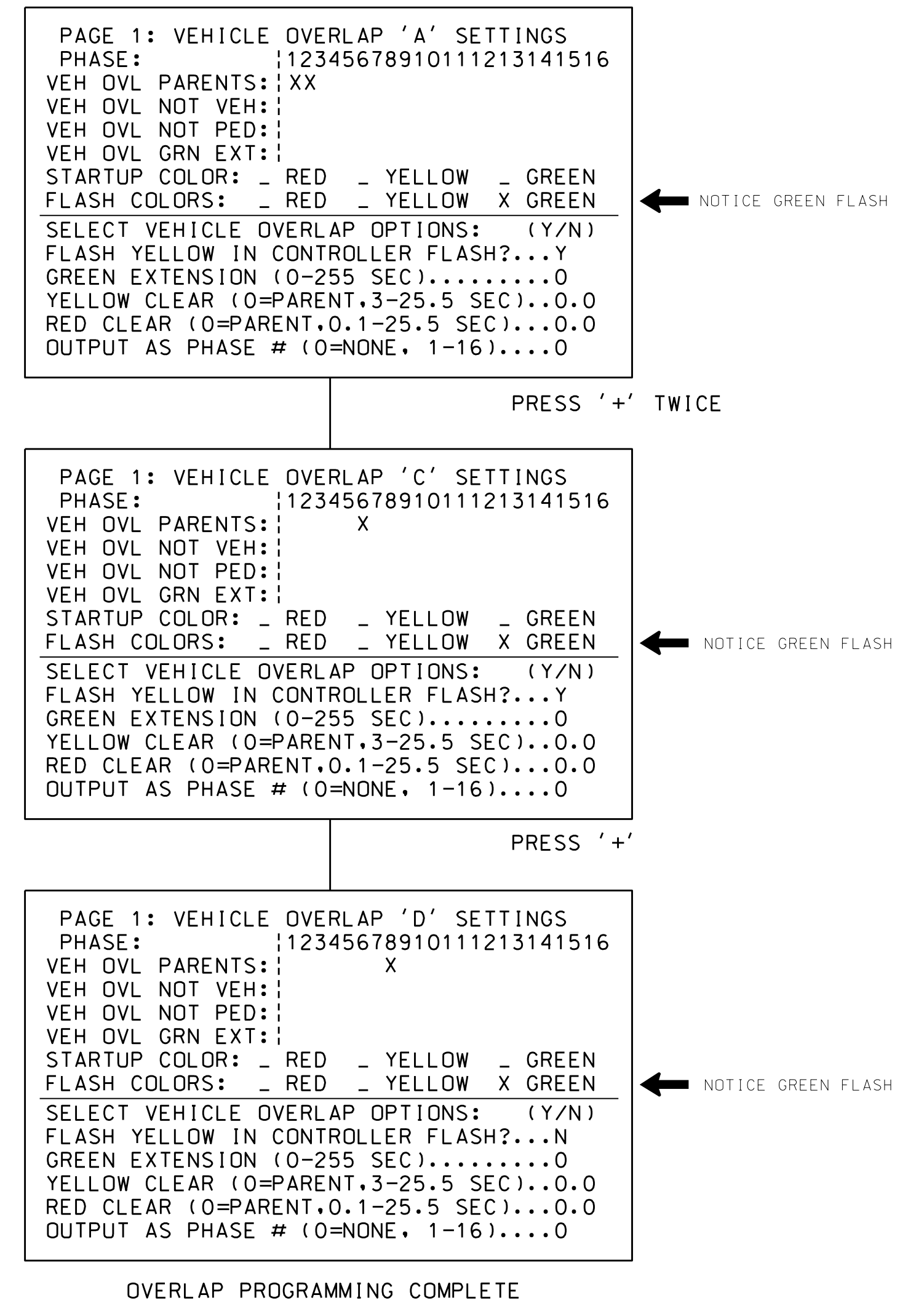
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2059T1
DESIGNED: November 2016
SEALED: 1/25/2017
REVISED:

Electrical Detail - Temp 1 - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:
TRANSPORTATION MOBILITY AND SAFETY DIVISION
STATE OF NORTH CAROLINA
Signal Management Section
750 N. Greenfield Pkwy, Garner, NC 27529

NC 119
at
Deerfield Trace and
Lowe's Boulevard

Division 7 Alamance County Mebane
PLAN DATE: January 2017 REVIEWED BY: T. Joyce
PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by:
Zachary M. Little 1/26/2017
0021EFD04F5341F DATE

SEAL
NORTH CAROLINA
PROFESSIONAL ENGINEER
SEAL
030530
ZACHARY M. LITTLE

SIG. INVENTORY NO. 07-2059T1

06-1416-2017_07-12
C:\PITS\SIGNALS\SIGNAL\work\hgr\opus5\g_Maps\Strickland\02059_sml_e_xxr.dgn
C:\PITS\SIGNALS\SIGNAL\work\hgr\opus5\g_Maps\Strickland