PROJECT REFERENCE NO.	SHEET NO.
W-5520	Sig. 23.2

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, and 3.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE         #3  IS ON
AND RED CLEAR ON PHASE #3  IS ON

            SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #47 ON
SET OUTPUT  ASSIGNMENT #48 OFF
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 6 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE         #3  IS ON

            SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #49 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE     #3  IS ON

            SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #48 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE:           |12345678910111213141516
VEH OVL PARENTS: |  X  X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: _ RED   _ YELLOW  _ GREEN
FLASH COLORS:  _ RED   _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS NEXT TO ADVANCE TO PAGE 2.

PRESS '+'

NOTICE PAGE 2

```
PAGE 2: VEHICLE OVERLAP 'B' SETTINGS
PHASE:           |12345678910111213141516
VEH OVL PARENTS: |  X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: _ RED   _ YELLOW  _ GREEN
FLASH COLORS:  _ RED   _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

OVERLAP PROGRAMMING COMPLETE

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 10-2195
DESIGNED: June 2015
SEALED: August 30, 2016
REVISED:

PLANS PREPARED BY:

DRMP
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DRMP, INC.
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CHARLOTTE, NC 28210
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ELECTRICAL DETAIL SHEET 2 OF 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:
Transportation Mobility and Safety Division
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

US 74 (Andrew Jackson Highway)
Westbound
at
SR 1515 (Sardis Church Road)

Division 10 | Union County | Indian Trail

PLAN DATE: June 2015	REVIEWED BY: L Moon
PREPARED BY: K Smith	REVIEWED BY: B Humfleet

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022516 ENGINEER LISA M. MOON

Lisa M. Moon 12/12/2016
DATE

SIG. INVENTORY NO. 10-2195

09-DEC-2016 14:16
N:*Traffic*Signals*Design*Wiring*102195-20160808e.dgn
rlawton AT CAR-RLAWTON-W7