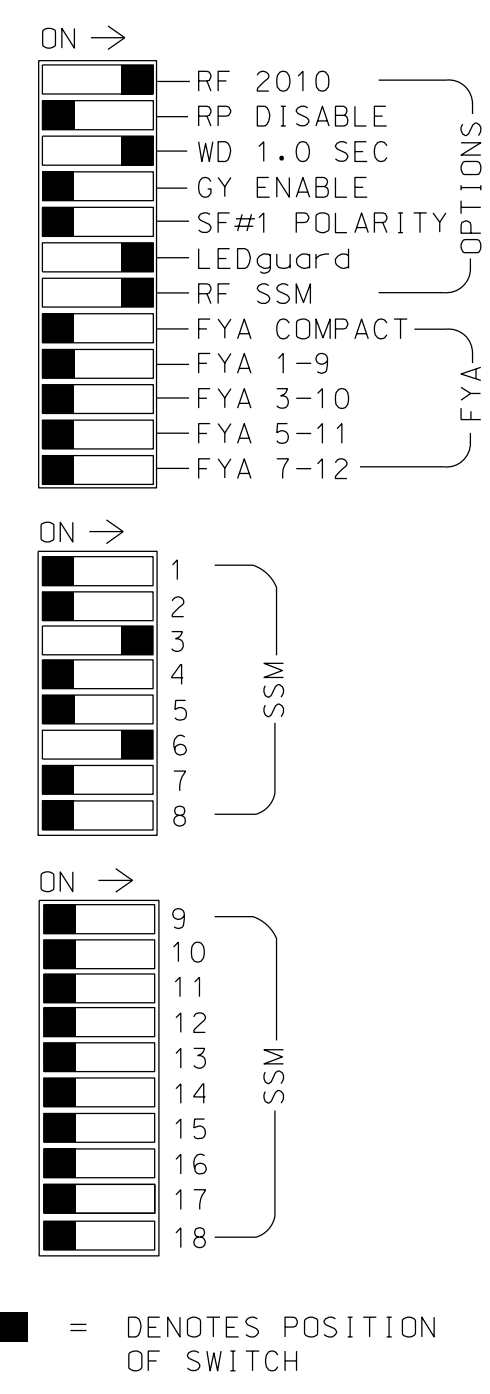
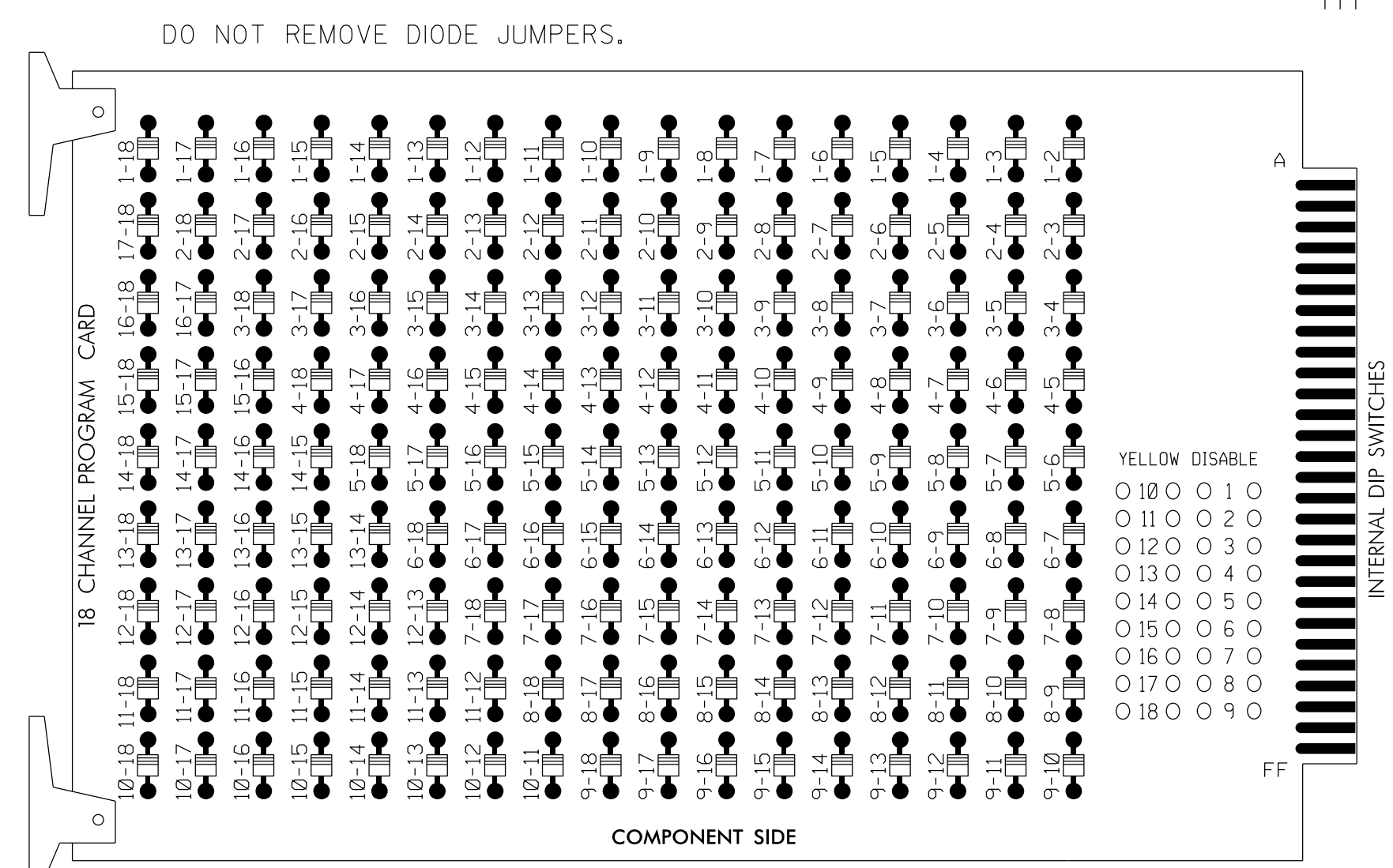


### EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phase 6 for Variable Initial and Gap Reduction.
- Program phase 6 for Start Up In Green.
- Program phase 6 for Yellow Flash.
- The cabinet and controller are part of the US 74 - Indian Trail Closed Loop System #2.

### EQUIPMENT INFORMATION

CONTROLLER.....2070E  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE\*  
 LOAD SWITCHES USED.....S4,S8.  
 PHASES USED.....3,6.  
 OVERLAP "A".....NOT USED  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....NOT USED  
 \*INSTALL AUX. OUTPUT FILE FOR FUTURE USE

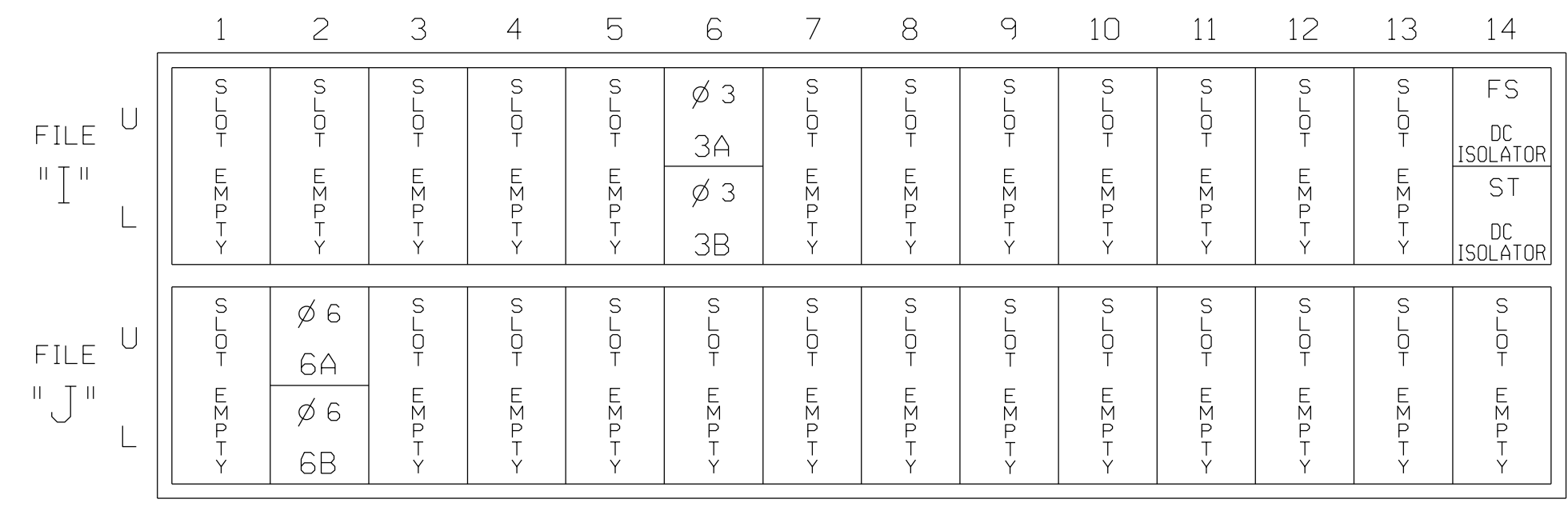
### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	DLC	OLD	SPARE
SIGNAL HEAD NO.	NU	NU	NU	31,32	NU	NU	NU	61,62	NU	NU	NU	NU	NU	NU	NU	NU	NU	NU
RED				116				134										
YELLOW								135										
GREEN								136										
RED ARROW																		
YELLOW ARROW				117														
FLASHING YELLOW ARROW																		
GREEN ARROW				118														

NU = Not Used

### INPUT FILE POSITION LAYOUT

(front view)



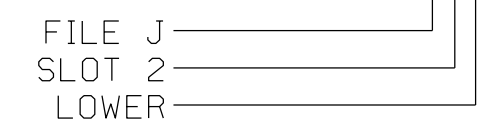
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
3A	TB4-9,10	I6U	41	3	4	3	Y	Y			10
3B	TB4-11,12	I6L	45	7	14	3	Y	Y			10
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-2195T1  
 DESIGNED: June 2015  
 SEALED: August 30, 2016  
 REVISED:

### ELECTRICAL DETAIL

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: **US 74 (Andrew Jackson Highway) Westbound at SR 1515 (Sardis Church Road)**

Prepared for the Offices of: **DRMP ENGINEERS • PLANNERS • SCIENTISTS**

DRMP, INC.  
 5950 FAIRVIEW ROAD, SUITE 320  
 CHARLOTTE, NC 28210  
 NC LICENSE NO. C-2213 • (704) 332-2289

Division 10 Union County Indian Trail

PLAN DATE: June 2015 REVIEWED BY: L Moon  
 PREPARED BY: K Smith REVIEWED BY: B Humfleet

REVISIONS INIT. DATE

SEAL  
 NORTH CAROLINA PROFESSIONAL ENGINEER  
 SEAL 022516  
 LISA M. MOON  
 12/12/2016  
 DATE

SIG. INVENTORY NO. 10-2195T1

09-DEC-2016 14:16  
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