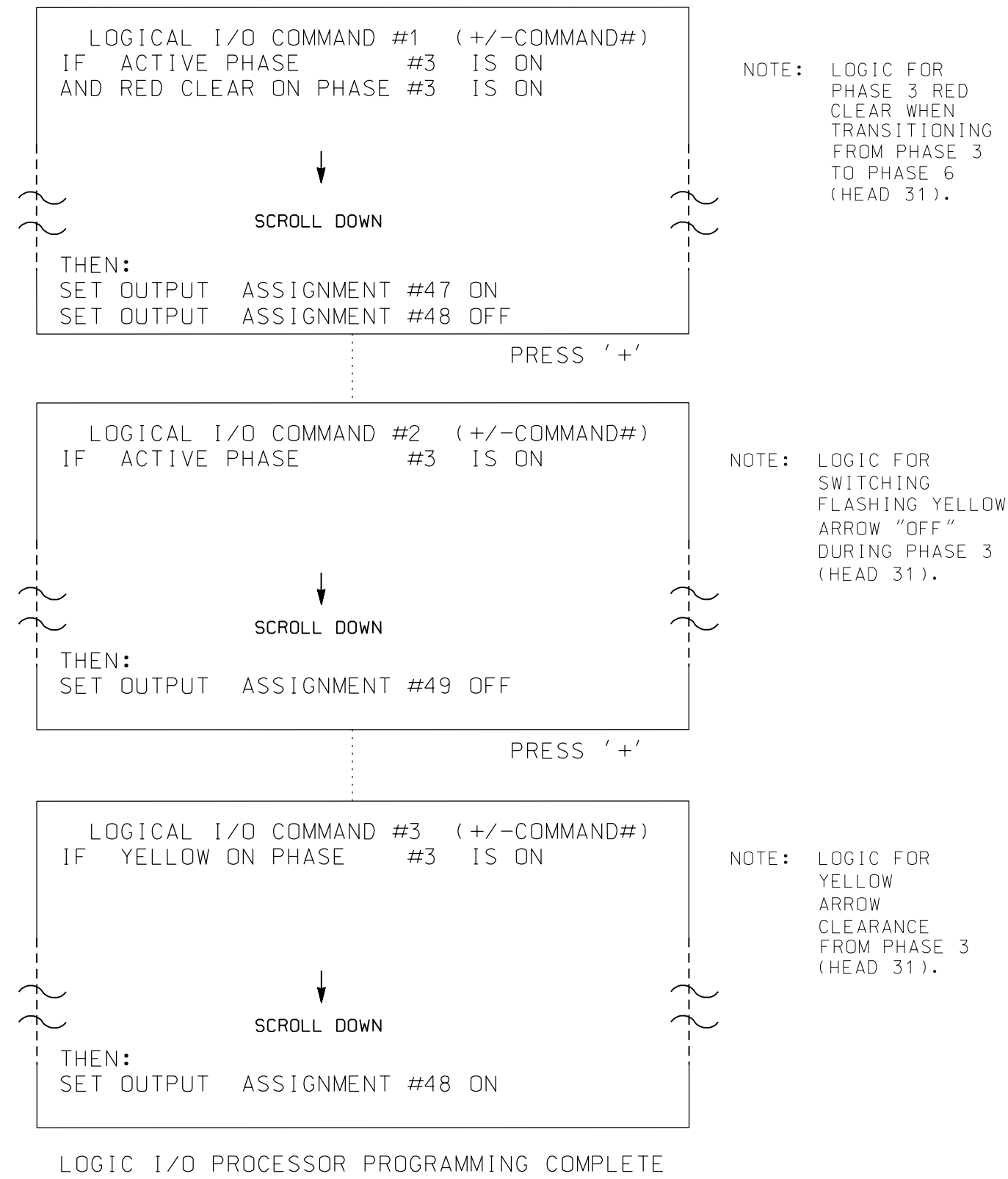


### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, and 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE SCHEDULE	
USE TO INTERPRET LOGIC PROCESSOR	
OUTPUT 47	= Overlap B Red
OUTPUT 48	= Overlap B Yellow
OUTPUT 49	= Overlap B Green

### OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+'

```

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS: | X X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | RED  _ YELLOW  _ GREEN
FLASH COLORS:   | _ RED  _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

### OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS NEXT TO ADVANCE TO PAGE 2.

PRESS '+'

```

PAGE 2: VEHICLE OVERLAP 'B' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS: | X
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR:  | RED  _ YELLOW  _ GREEN
FLASH COLORS:   | _ RED  _ YELLOW  _ GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    
```

← NOTICE PAGE 2 →

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-2193  
DESIGNED: June 2015  
SEALED: August 30, 2016  
REVISED:

ELECTRICAL DETAIL SHEET 2 OF 4

**DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED**

PLANS PREPARED BY:

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Prepared for the Offices of:

US 74 (Andrew Jackson Hwy) WB  
at  
Harris Teeter Distribution  
Center Eastbound U-turn

Division 10 Union County Indian Trail

PLAN DATE: June 2015 REVIEWED BY: B Humfleet

PREPARED BY: LM Moon REVIEWED BY:

REVISIONS	INIT.	DATE

750 N. Greenfield Pkwy, Garner, NC 27529

Seal: Lisa M. Moon, Professional Engineer, No. 022516, State of North Carolina. Date: 12/12/2016

SIG. INVENTORY NO. 10-2193

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